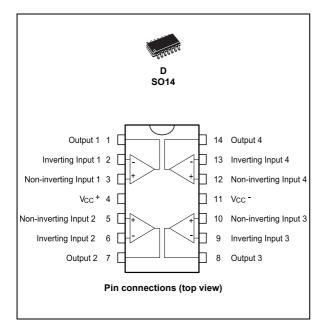


LM248, LM348

Datasheet - production data

Four UA741 quad bipolar operational amplifiers



Features

- Low supply current: 0.53 mA per amplifier
- Class AB output stage: no crossover distortion
- Pin compatibility with LM124, LM224, LM324
- Low input offset voltage: 1 mV
- Low input offset current: 2 nA
- Low input bias current: 30 nA
- Gain bandwidth product: 1.3 MHz
- High degree of isolation between amplifiers: 120 dB
- Overload protection for inputs and outputs

Description

The LM248 and LM348 consist of four independent, high-gain internally-compensated, low-power operational amplifiers which have been designed to provide functional characteristics identical to those of the familiar UA741 operational amplifier. In addition, the total supply current for all four amplifiers is compatible with the supply current of a single UA741 type operational amplifier. Other features include input offset current and input bias current which are much less than those of a standard UA741. Also, excellent isolation between amplifiers has been achieved by independently biasing each amplifier and using layout techniques which minimize thermal coupling.

The LM248 and LM348 can be used where multiple UA741 type amplifiers are being used and in applications where amplifier matching or high packaging density is required.

Table	1.	Device	summary

Part number	Temperature range	Package			
LM248	-40 ° C to 105 ° C	ר ⁽¹⁾			
LM348	0 ° C to 70° C				
Order code example: LM348DT ⁽²⁾					

1. D = Small outline package (SO)

2. See Table 5: Order codes

This is information on a product in full production.

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1 Schematic diagram

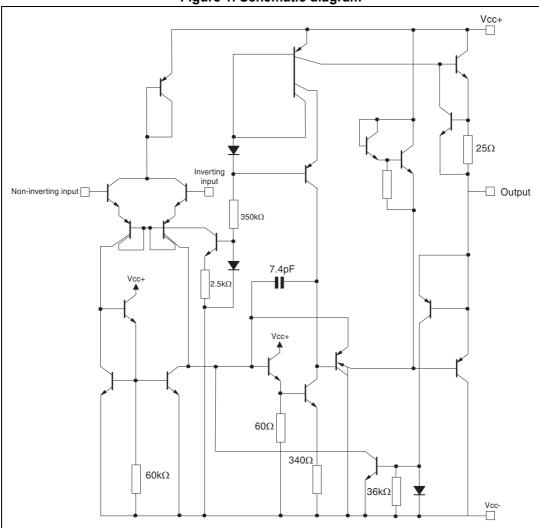


Figure 1. Schematic diagram



2 Absolute maximum ratings

Symbol	Parameters	LM248 LM348		Unit	
V _{CC}	Supply voltage	+2	20		
Vi	Input voltage ⁽¹⁾				
V _{id}	Differential input voltage	Differential input voltage ±44			
	Output short-circuit duration ⁽²⁾	Infinite		-	
P _{tot}	Power dissipation	500		mW	
T _{oper}	Operating free-air temperature range	-40 to 105	0 to 70 C	°C	
T _{stg}	Storage temperature range	-65 to 150			
	HBM: human body model ⁽³⁾	200		- V	
ESD	MM: machine model ⁽⁴⁾	50			
	CDM: charged device model ⁽⁵⁾	1.5		kV	

Table 2.	Absolute	maximum	ratings
	/		

1. For supply voltages less than the maximum value, the absolute maximum input voltage is equal to the supply voltage.

2. Any of the amplifier outputs can be shorted to ground indefinitely, however, more than one should not be simultaneously shorted as the maximum junction will be exceeded.

3. Human body model: 100pF discharged through a $1.5k\Omega$ resistor between two pins of the device, done for all couples of pin combinations with other pins floating.

 Machine model: a 200pF cap is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5Ω), done for all couples of pin combinations with other pins floating.

5. Charged device model: all pins plus package are charged together to the specified voltage and then discharged directly to the ground.



3 Electrical characteristics

Table 3. Electrical performances at V _{CC} = ± 15 V, T _{amb} = 25 $^{\circ}$ C
(unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Unit	
<i>\</i>	Input offset voltage (R _s \leq 10 kΩ), T _{amb} = 25 °C		1	5		
V _{io}	Input offset voltage ($R_s \le 10 \text{ k}\Omega$), $T_{min} \le T_{amb} \le T_{max}$			6	- mV	
	Input offset current, T _{amb} = 25 °C		2	25		
I _{io}	Input offset current, $T_{min} \le T_{amb} \le T_{max}$			75		
I	Input bias current, T _{amb} = 25 °C		30	100	- nA	
I _{ib}	Input bias current, $T_{min} \le T_{amb} \le T_{max}$			300		
Δ.	Large signal voltage gain (V _o = ±10 V, R _L = 2 k Ω), T _{amb} = 25 °C	50	160		- V/mV	
A _{vd}	$ \begin{array}{l} \mbox{Large signal voltage gain (V_{0} = \pm 10 \ V, \ R_{L} = 2 \ k\Omega) , \\ \ T_{min} \leq T_{amb} \ \leq T_{max} \end{array} $	25			- v/mv	
SVR	Supply voltage rejection ratio (R _s \leq 10 kΩ), T _{amb} = 25 °C	77	100		dB	
OVIX	Supply voltage rejection ratio (R _s \leq 10 kΩ), T _{min} \leq T _{amb} \leq T _{max}				- dB	
	Supply current, all amp, no load, T _{amb} = 25 °C		2.1	3.6		
I _{cc}	Supply current, all amp, no load, $T_{min} \leq T_{amb} \leq T_{max}$			4.8	mA	
M	Input common mode voltage range, T _{amb} = 25 °C	10			v	
V _{icm}	Input common mode voltage range, $T_{min} \le T_{amb} \le T_{max}$	±12			V	
CMD	Common mode rejection ratio ($R_s \le 10 \text{ k}\Omega$), T _{amb} = 25 °C	70	110		dD	
CMR	Common mode rejection ratio ($R_s \le 10 \text{ k}\Omega$), $T_{min} \le T_{amb} \le T_{max}$	70			dB	
l _{os}	Output short-circuit current, T _{amb} = 25 °C	10	25	35	mA	
	Output voltage swing, T_{amb} = 25°C, $R_L \le 10 \text{ k}\Omega$	12 13				
+\/	Output voltage swing, $T_{amb} = 25^{\circ}C$, $R_{L} \leq 2 \ k\Omega$	10	12			
±V _{opp}	Output voltage swing, $T_{min} \le T_{amb} \le T_{max}$, $R_L \le 10 \ k\Omega$	12				
	Output voltage swing, $T_{min} \le T_{amb} \le T_{max}$, $R_L \le 2 \ k\Omega$	10			1	
SR	Slew rate (V _I = ±10 V, R _L = 10 kΩ, C _L = 100 pF, unity gain)	0.25	0.5		V/µs	



Symbol	Parameter	Min.	Тур.	Max.	Unit
t _r	Rise time (V _I = ±10 V, R _L = 10 kΩ, C _L = 100 pF, unity gain)		0.3		μs
K _{OV}	Overshoot (V _I = ±10 V, R _L = 10 kΩ, C _L = 100 pF, unity gain)		5		%
R _I	Input resistance	0.8	2.5		MΩ
GBP	Gain bandwidth product (V _I = 10 mV, R _L = 10 k Ω , C _L = 100 pF, f = 100 kHz)		1.3		MHz
THD	Total harmonic distortion (f = 1 kHz, A_v = 20 dB, R_L = 10 k Ω , C_L = 100pF, V_o = 2 V_{pp})		0.08		%
e _n	Equivalent Input noise voltage (f = 1 kHz, R _s = 100 Ω		40		$\frac{nV}{\sqrt{Hz}}$
V _{o1} /V _{o2}	Channel separation		120		dB

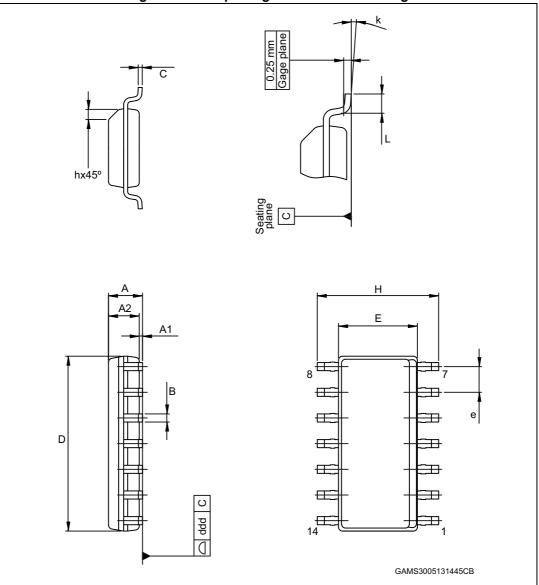
Table 3. Electrical performances at V _{CC} = \pm 15 V, T _{amb} = 25 ° C
(unless otherwise specified) (continued)



4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

4.1 SO14 package information







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	Dimensions						
Ref		Millimeters		Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
А	1.35		1.75	0.053		0.069	
A1	0.10		0.25	0.004		0.010	
A2	1.10		1.65	0.043		0.065	
В	0.33		0.51	0.013		0.020	
С	0.19		0.25	0.007		0.010	
D ⁽¹⁾	8.55		8.75	0.337		0.344	
E	3.80		4.00	0.150		0.157	
е		1.27	1	 	0.050		
Н	5.80		6.20	0.228		0.244	
h	0.25		0.50	0.010		0.020	
L	0.40		1.27	0.016		0.050	
k	0		8	0		0.315	
ddd			0.10			0.004	

Table 4. S	SO14	package	mechanical	data
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1. Dimension "D" does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions or gate burrs should not exceed 0.15 mm per side.



5 Ordering information

Table 5. Order codes

Order code	Temperature range	Package	Packaging	Marking	
LM248D	-40 °C to 105 °C	SO14	Tube	248	
LM248DT	-40 0 10 105 0	3014	Tape and reel	240	
LM348DT	0 ° C to 70° C	SO14	Tape and reel	348	

6 Revision history

Date	Revision	Changes
05-Jun-2013	4	Description: small text changes Table 1: Device summary: updated layout Replaced Figure 2: DIP14 package mechanical drawing, Figure 2: SO14 package mechanical drawing, Table 4: DIP14 package mechanical data, and Table 4: SO14 package mechanical data. Added Section 5: Ordering information
06-Dec-2013	5	Removed LM148 - product obsolete Removed DIP14 package (not recommended for new design) and order codes relating to it (LM148N, LM348N). <i>Table 2: Absolute maximum ratings</i> : added ESD data

Table 6. Document revision history



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