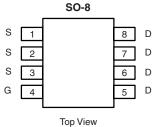


Vishay Siliconix

N-Channel Reduced Q_g , Fast Switching MOSFET

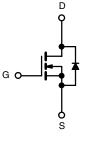
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
30	0.0185 at V _{GS} = 10 V	9			
	0.030 at V _{GS} = 4.5 V	7			





- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFET
- High-Efficient PWM Optimized
- 100 % UIS and R_g Tested •





N-Channel MOSFET

Ordering Information: Si4800BDY-T1-E3 (Lead (Pb)-free) Si4800BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	30		V	
Gate-Source Voltage		V _{GS}	± 25			
	T _A = 25 °C	- I _D	9	6.5		
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C		7.0	5.0		
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	40		А	
Continuous Source Current (Diode Conduction) ^{a, b}		۱ _S	2.3			
Avalanche Current	L = 0.1 mH	I _{AS} 15		15		
Single-Pulse Avalanche Energy	L = 0.1 mm	E _{AS}	11.25		mJ	
M. C. D. C a b	T _A = 25 °C T _A = 70 °C	- P _D	2.5	1.3	W	
Maximum Power Dissipation ^{a, b}			1.6	0.8	vv	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55	to 150	°C		

THERMAL RESISTANCE RATINGS						
			Limits			
Parameter		Symbol	Тур.	Max.	Unit	
Maximum handling to Angleing 18	t ≤ 10 s	R _{thJA}	40	50		
Maximum Junction-to-Ambient ^a	Steady State		70	95	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	24	30		

Notes:

a. Surface Mounted on FR4 board.

b. t ≤ 10 s.

rwise noted				
Conditions	Min.	Тур.	Max.	Unit
$I_{\rm S}, I_{\rm D} = 250 \ \mu {\rm A}$ $V, V_{\rm GS} = \pm 20 \ {\rm V}$	0.8		1.8	V
/, V _{GS} = ± 20 V			± 100	nA
0 V, V _{GS} = 0 V			1	

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.8		1.8	۷
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
	1	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	1		1	- μΑ
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$			5	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, V_{GS} = 10 V	30			Α
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 9 \text{ A}$			0.0185	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 7 \text{ A}$			0.030	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 9 \text{ A}$		16		S
Diode Forward Voltage ^a	V _{SD}	$I_{S} = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.2	V
Dynamic ^b						
Total Gate Charge	Qg			8.7	13	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 5.0 \text{ V}, I_{D} = 9 \text{ A}$		1.5		nC
Gate-Drain Charge	Q _{gd}			3.5		
Gate Resistance	Rg		0.5	1.4	2.2	Ω
Turn-On Delay Time	t _{d(on)}			7	15	
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		12	20	ns
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong$ 1 A, ${\rm V}_{\rm GEN}$ = 10 V, ${\rm R}_{\rm g}$ = 6 Ω		32	50	
Fall Time	t _f			14	25	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.3 A, dl/dt = 100 A/μs		30	60	

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

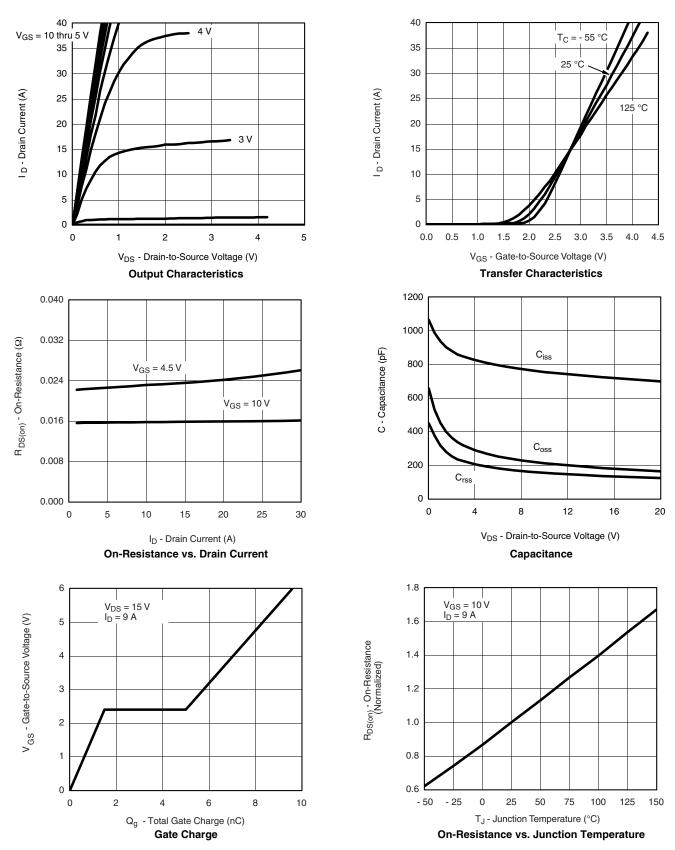
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Si4800BDY

Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



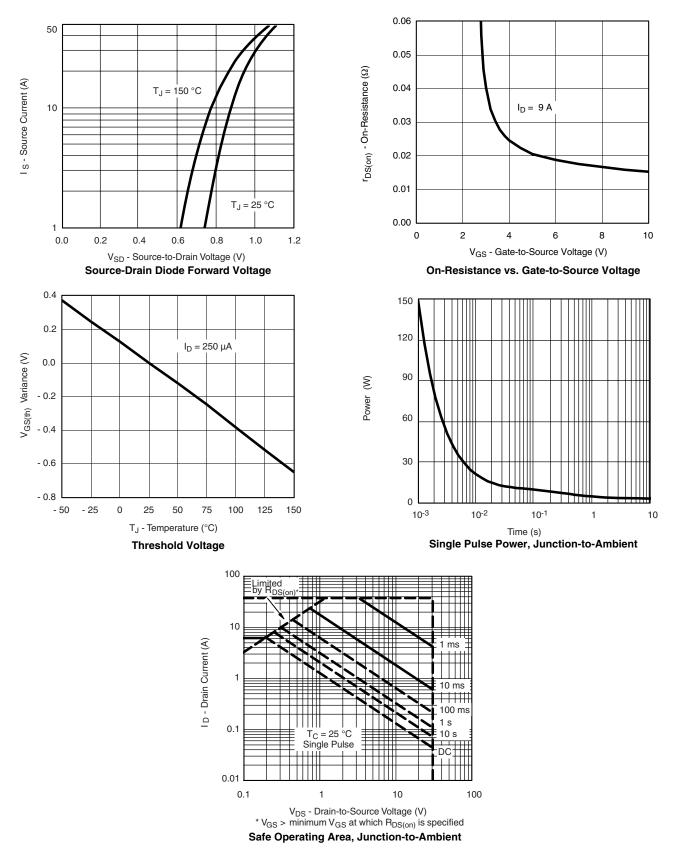
Document Number: 72124 S-83039-Rev. H, 29-Dec-08

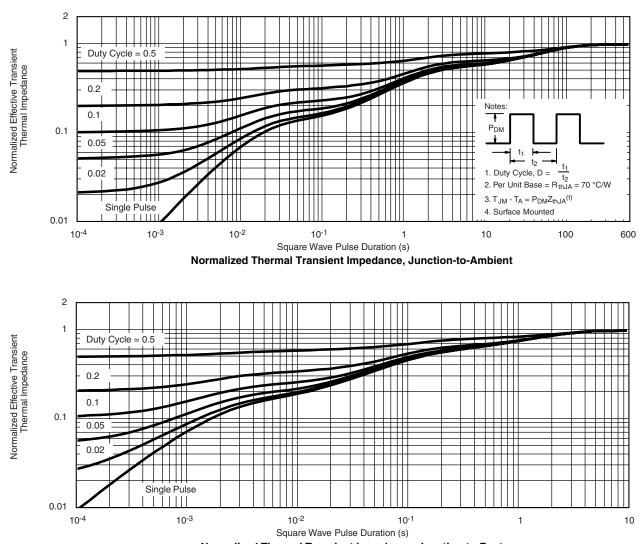
Si4800BDY

VISHAY.

Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?72124</u>.

VISHAY

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Si4800BDY

Vishay Siliconix



Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INC	HES		
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

Application Note 826

Vishay Siliconix



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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