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April 2013

FDMQ86530L

GreenBridgeTM Series of High-Efficiency Bridge Rectifiers N-Channel PowerTrench[®] MOSFET 60 V, 8 A, 17.5 m Ω

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Features

- Max $r_{DS(on)}$ = 17.5 m Ω at V_{GS} = 10 V, I_D = 8 A
- Max $r_{DS(on)}$ = 23 m Ω at V_{GS} = 6 V, I_D = 7 A
- Max $r_{DS(on)}$ = 25 m Ω at V_{GS} = 4.5 V, I_D = 6.5 A
- Substantial efficiency benefit in PD solutions
- RoHS Compliant

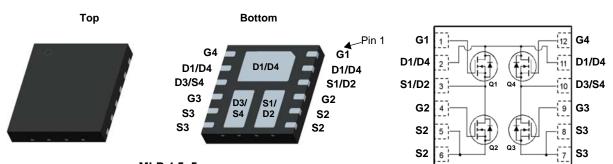


General Description

This Quad MOSFET solution provides ten-fold improvement in power dissipation over diode bridge.

Applications

- Active bridge
- Diode Bridge replacement in 24V & 48V AC systems



MLP 4.5x5

MOSFET Maximum Ratings T_A = 25 °C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DS}	Drain to Source Voltage			60	V	
V _{GS}	Gate to Source Voltage			±20	V	
I _D	Drain Current -Continuous	T _C = 25 °C		8		
	-Continuous	$T_A = 25 \ ^\circ C$ (Note 1a)		8	А	
	-Pulsed			50		
6	Power Dissipation	T _C = 25 °C		22	14/	
P _D	Power Dissipation	T _A = 25 °C	(Note 1a)	1.9	W	
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C	

Thermal Characteristics

R_{\thetaJA}	Thermal Resistance, Junction to Ambient	(Note 1a)	65	°C/W
$R_{ ext{ heta}JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	135	C/VV

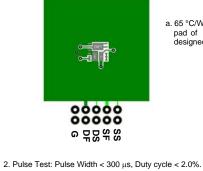
Package Marking and Ordering Information

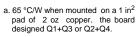
Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMQ86530L	FDMQ86530L	MLP 4.5x5	13 "	12 mm	3000 units

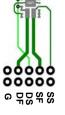
FDMQ86530L Quad
Quad N-Channel F
l PowerTrench [®]
³⁾ MOSFET

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units	
Off Chara	octeristics						
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \ \mu A, \ V_{GS} = 0 \ V$	60			V	
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$, referenced to 25 °C		27		mV/°C	
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±100	nA	
On Chara	cteristics						
V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \ \mu A$	1	1.8	3	V	
$\Delta V_{GS(th)}$ ΔT_J	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25 °C		-6		mV/°C	
		V _{GS} = 10 V, I _D = 8 A		12	17.5		
_	Statia Dania ta Causa On Danistanan	V _{GS} = 6 V, I _D = 7 A		15	23	mΩ	
r _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}$		20	25		
		V _{GS} = 10 V, I _D = 8 A, T _J = 125 °C		18	26		
9 _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D = 8 A		28		S	
C _{oss} C _{rss}	Output Capacitance Reverse Transfer Capacitance	— V _{DS} = 30 V, V _{GS} = 0 V, f = 1 MHz		299 10	400 15	pF pF	
				10	10	Pi	
	Turn-On Delay Time			8.8	18	ns	
t _r	Rise Time	V _{DD} = 30 V, I _D = 8 A,		3.8	10	ns	
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		22	35	ns	
<u>t_f</u>	Fall Time			2.8	10	ns	
Q _g	Total Gate Charge	V _{GS} = 0 V to 10 V		23	33	nC	
Q _a	Total Gate Charge	$V_{GS} = 0 V \text{ to } 4.5 V V_{DD} = 30 V,$		11	16	nC	
Q _{gs}	Gate to Source Charge	I _D = 8 A		5.1		nC	
Q _{gd}	Gate to Drain "Miller" Charge			2.3		nC	
Drain-Sou	urce Diode Characteristics						
V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_S = 8 A$ (Note 2)		0.8	1.3		
		$V_{GS} = 0 V, I_S = 1.6 A$ (Note 2)		0.7	1.2	V	
	Reverse Recovery Time	—I _F = 8 A, di/dt = 100 A/μs		27	43	ns	
t _{rr}							

1. R_{0JA} is determined with the device mounted on a 1in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0JC} is guaranteed by design while R_{0CA} is determined by the user's board design.

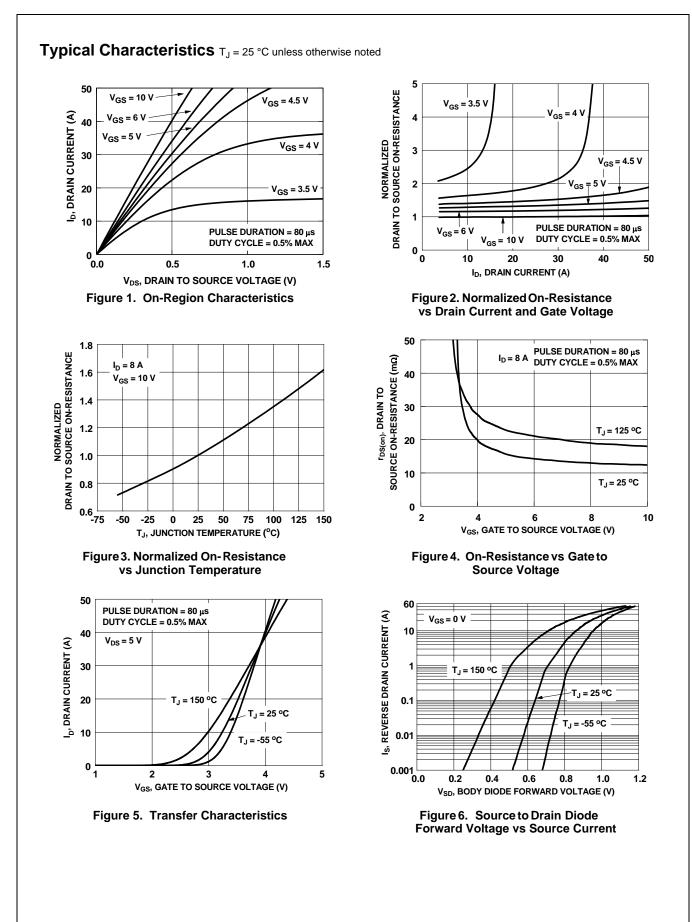


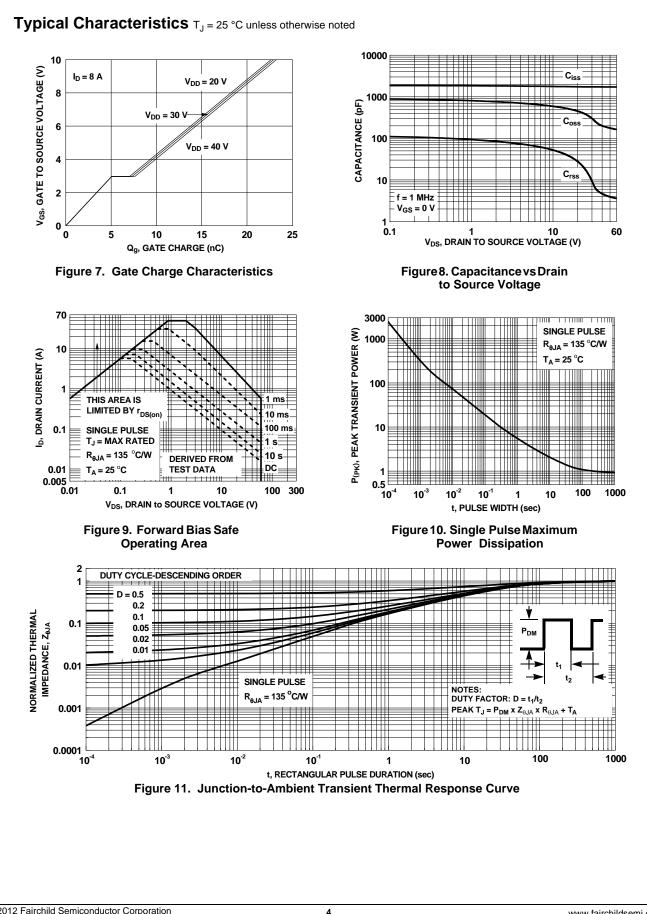




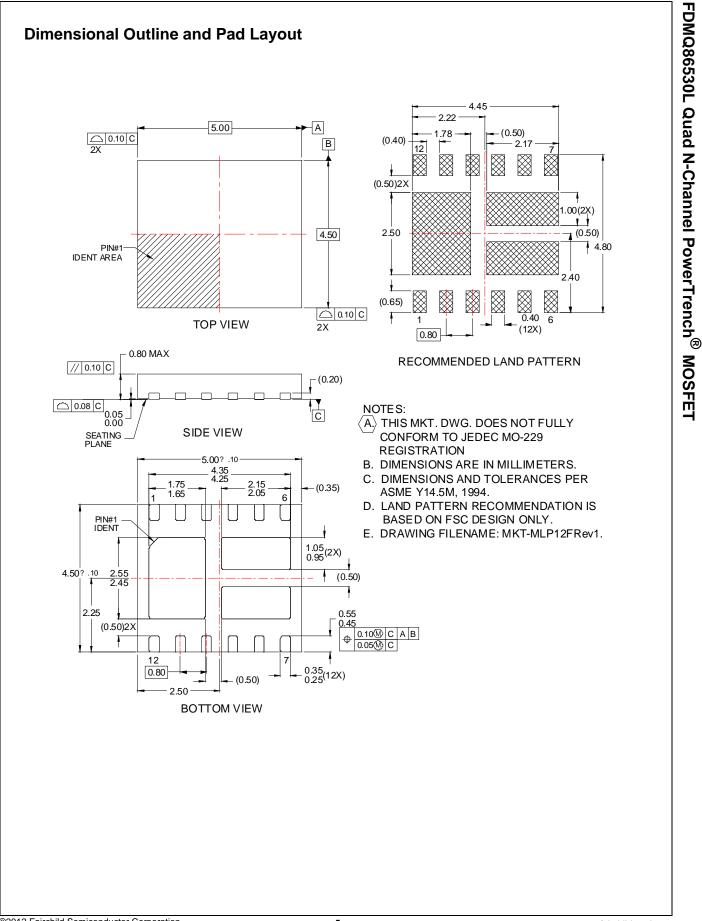
 b. 135 °C/W when mounted on a minimum pad of 2 oz copper. the board designed Q1+Q3 or Q2+Q4.







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