Vishay General Semiconductor

# **Dual Low-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.28$  V at  $I_F = 5.0$  A



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**DESIGN SUPPORT TOOLS** 



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
V <sub>RRM</sub>	45 V			
I <sub>FSM</sub>	240 A			
$V_F$ at $I_F = 20$ A	0.41 V			
T <sub>J</sub> max.	150 °C			
Package	D <sup>2</sup> PAK (TO-263AB)			
Circuit configuration	Common cathode			

### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation



- Meets MSL level 1, per J-STD-020, LF maximum RoHS peak of 245 °C COMPLIANT
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

### **MECHANICAL DATA**

Case: D<sup>2</sup>PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VBT4045C	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	45	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	40	^	
	per diode		20	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	240	A	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.41	-	V	
	I <sub>F</sub> = 10 A			0.44	-		
	I <sub>F</sub> = 20 A			0.50	0.58		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.28	-		
	I <sub>F</sub> = 10 A			0.33	-		
	I <sub>F</sub> = 20 A			0.41	0.50		
Reverse current per diode	V <sub>B</sub> = 45 V	$T_{A} = 25 \text{ °C}$	I <sub>R</sub> <sup>(2)</sup>	-	3000	μA	
	v <sub>R</sub> = 45 V	T <sub>A</sub> = 125 °C		18	50	mA	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER		SYMBOL	VBT4045C	UNIT
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	1.5	°C/W
	per device		0.8	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VBT4045C-E3/4W	1.38	4W	50/tube	Tube	
TO-263AB	VBT4045C-E3/8W	1.38	8W	800/reel	Tape and reel	

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

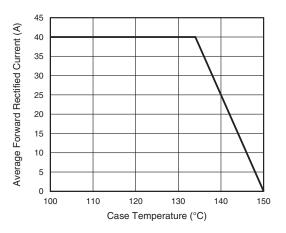


Fig. 1 - Maximum Forward Current Derating Curve

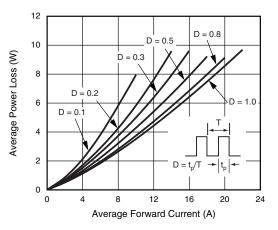
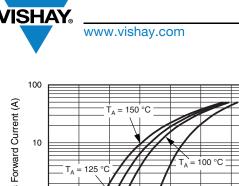


Fig. 2 - Forward Power Loss Characteristics Per Diode

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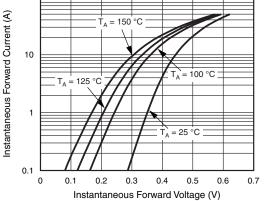


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

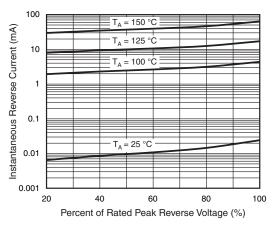


Fig. 4 - Typical Reverse Characteristics Per Diode



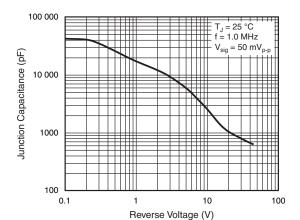


Fig. 5 - Typical Junction Capacitance Per Diode

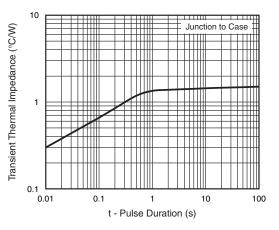
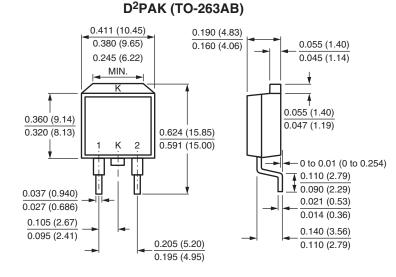
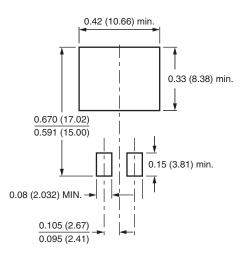


Fig. 6 - Typical Transient Thermal Impedance Per Diode



**Mounting Pad Layout** 



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