

# Differential Two-Wire Hall Effect Sensor-IC for Wheel Speed Applications

TLE4941-1  
TLE4941-1C

Sensors



Never stop thinking.

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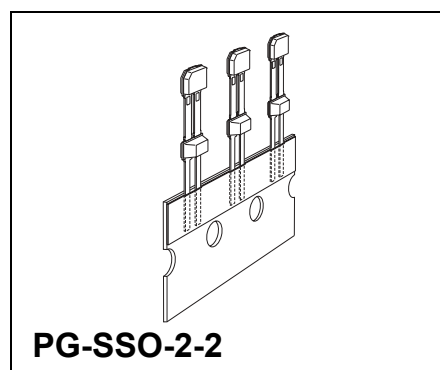
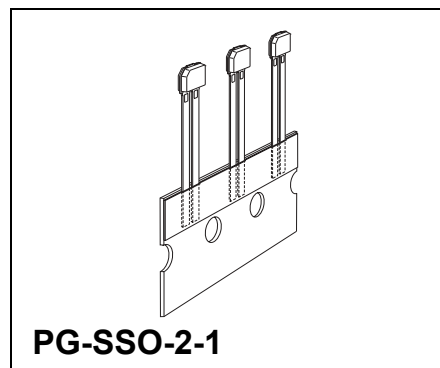
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## Differential Two-Wire Hall Effect Sensor IC

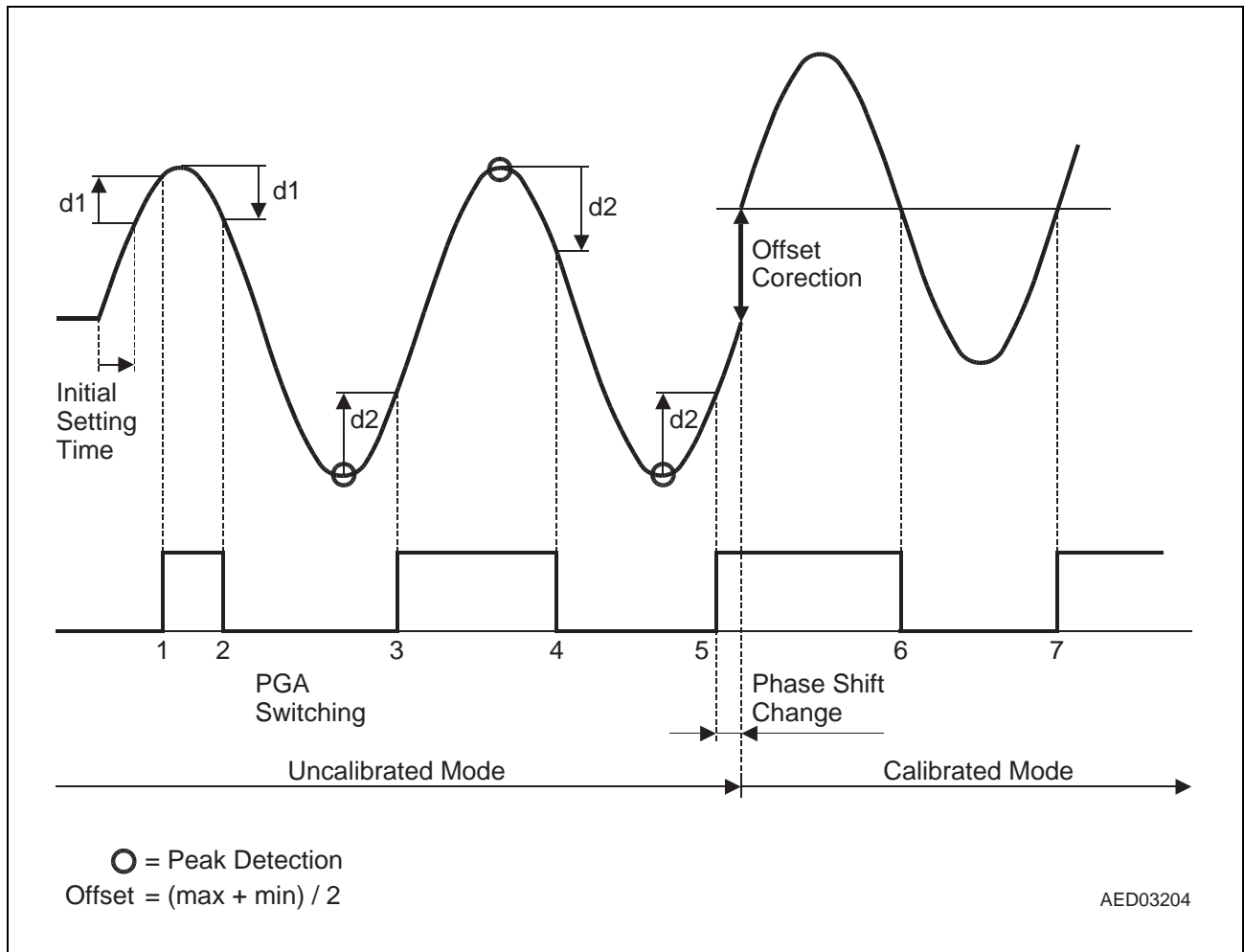
**TLE4941-1**  
**TLE4941-1C**

For all parameters not specified in this document the TLE4941 Data Sheet is valid.



Type	Marking	Ordering Code	Package
TLE4941-1	4101R	Q62705-K719	PG-SSO-2-1
TLE4941-1C	41C1R	Q62705-K712	PG-SSO-2-2

## Functional Description



**Figure 1 Example for Start-up Behavior**

### Uncalibrated Mode

Occasionally a short initial offset settling time  $t_{d,input}$  might delay the detection of the input signal (the sensor is “blind”).

The magnetic input signal is tracked by the speed ADC and monitored within the digital circuit. For detection the signal transient needs to exceed a threshold (digital noise constant  $d1$ ). When the signal slope is identified as a rising edge (or falling edge), a trigger pulse is issued to a comparator. A second trigger pulse is issued as soon as a falling edge (or rising edge respectively) is detected (and vice versa).

Between the start-up of the magnetic input signal and the time when its second extreme is reached, the PGA (programmable gain amplifier) will switch to its appropriate position. This value is determined by the signal amplitude and initial offset value. The digital noise constant value is changing accordingly ( $d1 \rightarrow d2$ , related to the corresponding PGA states), leading to a change in phase shift between magnetic input signal and output

signal. After that consecutive output edges should have a nominal delay of about 180°. In rare cases one further switching of PGA can occur (see Appendix B).

During the uncalibrated mode the offset value is calculated by the peak detection algorithm as described in the TLE4941 Data Sheet.

### **Transition to Calibrated Mode**

In the calibrated mode the output will switch at zero-crossing of the input signal. The phase shift between input and output signal is no longer determined by the ratio between digital noise constant and signal amplitude. Therefore a sudden change in the phase shift may occur during the transition from uncalibrated to calibrated mode.

### **Calibrated Mode**

See TLE4941 Data Sheet.

### **Additional Notes**

Unlike the TLE4941 the first output edge might occur before the first zero-crossing of the magnetic input signal. However, referring to the input signal the delay between start-up of the signal and first calibrated output signal is identical with TLE4941.

Typically the phase error due to PGA-transition reduces the error caused by switching the mode from uncalibrated to calibrated. PGA-transition usually takes place on the first full magnetic edge.

The summed up change in phase error from the first output edge issued to the output edges in calibrated mode will not exceed  $\pm 90^\circ$ .

### **Circuit Description**

See TLE4941 Data Sheet.

**Table 1 Additions/Changes for TLE4941-1 versus TLE4941**

(All values are valid for constant amplitude and offset of input signal,  
 $f < 2500$  Hz)

Parameter	Symbol	Limit Values			Unit	Conditions
		min.	typ.	max.		
Signal behavior after undervoltage or standstill $> t_{\text{Stop}}$ Number of magnetic edges where the first switching may occur <sup>1)</sup>	$n_{\text{DZ-Start}}$	–	–	1	edge	Magnetic edge amplitude according to $\Delta B_{\text{Limit, early startup}}$ $t_{\text{d,input}}$ has to be taken into account
Systematic phase error of output edges during start-up and uncalibrated mode		38	–	–	$\mu\text{s}$	Shortest time delay between input signal edge 1 and 2 $t_{\text{d,input}}$ has to be taken into account
		– 88	–	+ 88	°	Systematical phase error of “uncal” edge; $n^{\text{th}}$ vs. $n + 1^{\text{th}}$ edge (does not include random phase error)
Phase shift change during PGA switching		0	–	80	°	after the 2 <sup>nd</sup> edge
Phase shift change during transition from uncalibrated to calibrated mode	$\Delta\Phi_{\text{switch}}$	– 90	–	+ 90	°	
Number of edges in uncalibrated mode	$n_{\text{DZ-Startup}}$	–	–	6	edges	
In rare cases (see “ <b>Appendix B</b> ” on page 6)	$n_{\text{DZ-Startup}}$	–	–	8	edges	
Jitter during uncalibrated mode $1 \text{ Hz} < f < 2500 \text{ Hz}$	$S_{\text{JitClose}}$ (1 $\sigma$ -value)	–	–	$\pm 3$	%	$-40^{\circ}\text{C} \leq T_{\text{amb}} \leq 150^{\circ}\text{C}$
		–	–	$\pm 4$		$150^{\circ}\text{C} \leq T_{\text{amb}} \leq 170^{\circ}\text{C}$

**Table 1 Additions/Changes for TLE4941-1 versus TLE4941**

(All values are valid for constant amplitude and offset of input signal,  
 $f < 2500$  Hz) (cont'd)

Parameter	Symbol	Limit Values			Unit	Conditions
		min.	typ.	max.		
	$S_{\text{JitFar}}$ (1 $\sigma$ -value)	—	—	$\pm 5$	%	$-40^{\circ}\text{C} \leq T_{\text{amb}} \leq 150^{\circ}\text{C}$ $150^{\circ}\text{C} \leq T_{\text{amb}} \leq 170^{\circ}\text{C}$
		—	—	$\pm 7$		
	$S_{\text{JitAC}}$ (1 $\sigma$ -value)	—	—	$\pm 3$	%	See TLE4941 Data Sheet
Magnetic differential field change necessary for early startup of the - 1 Versions  $\Delta \dot{B}_{\text{Limit, early startup}}$ $1 \text{ Hz} < f < 2500 \text{ Hz}$ $2500 \text{ Hz} < f < 10000 \text{ Hz}$	$\Delta \dot{B}_{\text{Limit, early startup}}$	—	—	—	mT	Magnetic field (peak to peak value) change necessary for startup with the first edge
		0.7	1.76	3.3		
		—	—	3.9		
Permitted time for edges to exceed $\Delta \dot{B}_{\text{Limit, early startup}}$	$\Delta t_{\text{Limit, slow early startup}}$	—	—	590	ms	Necessary for startup with the second edge $f < 1 \text{ s}$

1) Due to defined power on state (low current) the IC can not provide the first edge if it would lead as well to a low state

### Behavior at Magnetic Input Signals Slower than $T_{\text{stop}}$ (self-calibration time period)

Unlike the TLE4941 magnetic changes exceeding  $\Delta \dot{B}_{\text{Limit, early startup}}$  can cause output switching of the TLE4941-1, even at  $f$  significantly lower than 1 Hz. Depending on their amplitude edges slower than  $\Delta t_{\text{Limit, slow early startup}}$  might be detected. If the digital noise constant ( $\Delta \dot{B}_{\text{Limit, early startup}}$ ) is not exceeded before a new initial self-calibration is started, the output of the corresponding edge will be inhibited. This depends on signal amplitude and initial phase.

### Additional Remarks

All additional parameters for TLE4941-1 are not subject to production test - verified by design/characterization. For series production additional to the parameters of TLE4941 (standard type) only  $n_{\text{DZ-start}}$  is tested.

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**Revision History: 2005-02, V2.1**

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Previous Version: 2004-01, V2.0

Page	Subjects (major changes since last revision)
3	Package name changed from P-... to PG-...
-	new format of data sheet

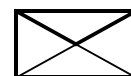
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