## Auxiliary Switch Diodes for Snubber <br> SARS01, SARS05

## Description

The SARS01/05 is an auxiliary switch diode especially designed for snubber circuits, which are used in the primary sides of flyback switched-mode power supplies.

Being capable of reducing the ringing voltage generated at power MOSFET turn-off, the SARS01/05-incorporated snubber circuits allow better cross regulation of multiple outputs.

The SARS01/05 can also improve power supply efficiency by partially transferring such ringing voltage into the secondary side of a power supply unit.

## Features

- Improves Cross Regulation
- Reduces Noise
- Improves Efficiency


## Applications

For switched-mode power supplies (SMPS) with flyback topology such as:

- White Goods
- Adaptor
- Industrial Equipment


## Typical Application



## Package

- SARS01

Axial $(\varphi 2.7 \times 5.0 \mathrm{~L} / \varphi 0.6)$
(1)

- SARS05

SJP ( $4.5 \mathrm{~mm} \times 2.6 \mathrm{~mm}$ )

(1)
(2) (1) Cathode
(2) Anode

Not to scale
Selection Guide

| Part Number | $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | $\mathrm{V}_{\mathrm{F}}$ (max.) | Package |
| :---: | :---: | :---: | :---: |
| SARS01 | 1.2 A | 0.92 V | Axial |
| SARS05 | 1.0 A | 1.05 V | SJP |

## Contents

Description ..... 1
Contents ..... - 2
Absolute Maximum Ratings ..... -3
Electrical Characteristics ..... -3
SARS01 Rating and Characteristic Curves ..... - 4
SARS05 Rating and Characteristic Curves ..... -5
SARS01 Physical Dimensions and Marking Diagram ..... -7
SARS05 Physical Dimensions and Marking Diagram ..... - 8
Operational Comparison of Clamp Snubber Circuits ..... 10
Power Dissipation and Junction Temperature Calculation ..... 11
Parameter Setting of Snubber Circuit using SARS01/05 ..... 11
Reference Design of Power Supply ..... 12
Important Notes ..... 14

SARS01, SARS05

## Absolute Maximum Ratings

Unless otherwise specified, $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

| Parameter | Symbol | Conditions | Rating | Unit | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Transient Peak Reverse Voltage | $\mathrm{V}_{\text {RSM }}$ |  | 800 | V |  |
| Peak Repetitive Reverse Voltage | $\mathrm{V}_{\text {RM }}$ |  | 800 | V |  |
| Average Forward Current ${ }^{(1)}$ | $\mathrm{I}_{\text {F(AV) }}$ |  | 1.2 | A | SARS01 |
|  |  |  | 1.0 |  | SARS05 |
| Surge Forward Current | $\mathrm{I}_{\text {FSM }}$ | Half cycle sine wave, positive side, $10 \mathrm{~ms}, 1$ shot | 110 | A | SARS01 |
|  |  |  | 30 |  | SARS05 |
| $\mathrm{I}^{2} \mathrm{t}$ Limiting Value | $\mathrm{I}^{2} \mathrm{t}$ | $1 \mathrm{~ms} \leq \mathrm{t} \leq 10 \mathrm{~ms}$ | 60.5 | $\mathrm{A}^{2} \mathrm{~s}$ | SARS01 |
|  |  |  | 4.5 |  | SARS05 |
| Junction Temperature | $\mathrm{T}_{\mathrm{J}}$ |  | -40 to 150 | ${ }^{\circ} \mathrm{C}$ |  |
| Storage Temperature | $\mathrm{T}_{\text {STG }}$ |  | -40 to 150 | ${ }^{\circ} \mathrm{C}$ |  |

## Electrical Characteristics

Unless otherwise specified, $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit | Remarks |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Forward Voltage Drop | $\mathrm{V}_{\mathrm{F}}$ | $\mathrm{I}_{\mathrm{F}}=1.2 \mathrm{~A}$ | - | - | 0.92 | V | SARS01 |
|  |  | $\mathrm{I}_{\mathrm{F}}=1.5 \mathrm{~A}$ | - | - | 1.05 |  | SARS05 |
| Reverse Leakage Current | $\mathrm{I}_{\mathrm{R}}$ | $\mathrm{V}_{\mathrm{R}}=\mathrm{V}_{\mathrm{RM}}$ | - | - | 10 | $\mu \mathrm{A}$ | SARS01 |
|  |  |  | - | - | 5 |  | SARS05 |
| Reverse Leakage Current under High Temperature | $\mathrm{H} \cdot \mathrm{I}_{\mathrm{R}}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{R}}=\mathrm{V}_{\mathrm{RM}}, \\ & \mathrm{~T}_{\mathrm{J}}=100^{\circ} \mathrm{C} \end{aligned}$ | - | - | 50 | $\mu \mathrm{A}$ |  |
| Reverse Recovery Time | $\mathrm{t}_{\mathrm{rr}}$ | $\begin{aligned} & \mathrm{I}_{\mathrm{F}}=\mathrm{I}_{\mathrm{RP}}=100 \mathrm{~mA}, \\ & \mathrm{~T}_{\mathrm{J}}=25{ }^{\circ} \mathrm{C}, \\ & 90 \% \text { recovery point } \end{aligned}$ | 2 | - | 18 | $\mu \mathrm{s}$ | SARS01 |
|  |  |  | 2 | - | 19 |  | SARS05 |
| Thermal Resistance | $\mathrm{R}_{\text {th }(\mathrm{J}-\mathrm{L})}$ | (2) | - | - | 20 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ | SARS01 |
|  |  |  | - | - | 20 |  | SARS05 |

[^0]SARS01 Rating and Characteristic Curves


Figure 1. SARS01 $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ vs. $\mathrm{P}_{\mathrm{F}}$ Power Dissipation Curves ( $\mathrm{T}_{\mathrm{J}}=150{ }^{\circ} \mathrm{C}$ )


Figure 3. SARS01 $\mathrm{T}_{\mathrm{L}}$ vs. $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ Derating Curves
$\left(\mathrm{V}_{\mathrm{R}}=0 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=150^{\circ} \mathrm{C}\right)$


Figure 2. SARS01 $\mathrm{V}_{\mathrm{R}}$ vs. $\mathrm{P}_{\mathrm{R}}$ Power Dissipation Curves $\left(\mathrm{T}_{\mathrm{J}}=150{ }^{\circ} \mathrm{C}\right)$


Figure 4. SARS01 $\mathrm{T}_{\mathrm{L}}$ vs. $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ Derating Curves
$\left(\mathrm{V}_{\mathrm{R}}=800 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=150{ }^{\circ} \mathrm{C}\right)$


Figure 5. SARS01 $\mathrm{V}_{\mathrm{F}}$ Vs. $\mathrm{I}_{\mathrm{F}}$ Typical Characteristics

## SARS05 Rating and Characteristic Curves



Figure 7. SARS05 $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ vs. $\mathrm{P}_{\mathrm{F}}$ Power Dissipation Curves ( $\mathrm{T}_{\mathrm{J}}=150{ }^{\circ} \mathrm{C}$ )


Figure 6. SARS01 $V_{R}$ vs. $I_{R}$ Typical Characteristics


Figure 8. SARS05 V $\mathrm{V}_{\mathrm{R}}$ vs. $\mathrm{P}_{\mathrm{R}}$ Power Dissipation Curves ( $\mathrm{T}_{\mathrm{J}}=150{ }^{\circ} \mathrm{C}$ )


Figure 9. SARS05 $\mathrm{T}_{\mathrm{L}}$ vs. $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ Derating Curves $\left(\mathrm{V}_{\mathrm{R}}=0 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=150^{\circ} \mathrm{C}\right)$


Figure 11. SARS05 $\mathrm{V}_{\mathrm{F}}$ vs. $\mathrm{I}_{\mathrm{F}}$ Typical Characteristics


Figure 10. SARS05 $\mathrm{T}_{\mathrm{L}}$ vs. $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ Derating Curves $\left(\mathrm{V}_{\mathrm{R}}=800 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=150^{\circ} \mathrm{C}\right)$


Figure 12. $\quad$ SARS05 $\mathrm{V}_{\mathrm{R}}$ vs. $\mathrm{I}_{\mathrm{R}}$ Typical Characteristics

## SARS01 Physical Dimensions and Marking Diagram

- SARS01 Physical Dimensions

Axial ( $\varphi 2.7 \times 5.0 \mathrm{~L} / \varphi 0.6)$


## NOTES:

- Dimensions in millimeters
- Bare leads: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time, within the following limits:

Flow: $260 \pm 5^{\circ} \mathrm{C} / 10 \pm 1 \mathrm{~s}, 2$ times
Soldering Iron: $380 \pm 10^{\circ} \mathrm{C} / 3.5 \pm 0.5 \mathrm{~s}, 1$ time (Soldering should be at a distance of at least 1.5 mm from the body of the product.)

- SARS01 Marking Diagram


Lot Number:
Y is the last digit of the year of manufacture (0 to 9)
M is the month of the year ( 1 to $9, \mathrm{O}, \mathrm{N}$, or D )
D is a period of days:
"." is the first 10 days of the month ( 1 st to 10 th)
".." is the second 10 days of the month (11th to 20th)
". . ." is the last $10-11$ days of the month (21st to 31 st)

## SARS05 Physical Dimensions and Marking Diagram

- SARS05 Physical Dimensions
- SJP Physical Dimensions



## NOTES:

- Dimensions in millimeters
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time, within the following limits: Reflow (MSL 3)

Preheat: $180^{\circ} \mathrm{C} / 90 \pm 30 \mathrm{~s}$
Solder heating: $250^{\circ} \mathrm{C} / 10 \pm 1 \mathrm{~s}, 2$ times $\left(260^{\circ} \mathrm{C}\right.$ peak)
Soldering iron: $380 \pm 10^{\circ} \mathrm{C} / 3.5 \pm 0.5 \mathrm{~s}$, 1 time

- SARS05 Land Pattern Example




## SARS01, SARS05

## Operational Comparison of Clamp Snubber Circuits

Figure 13 shows a general clamp snubber circuit. In the circuit, the surge voltage at tuning off a power MOSFET is charged to $\mathrm{C}_{\mathrm{S}}$ through the surge absorb loop, and is consumed by $\mathrm{R}_{\mathrm{S} 1}$ through the energy discharge loop. All the consumed energy becomes loss in $\mathrm{R}_{\mathrm{S} 1}$. In addition, the ringing of surge voltage results in poor cross regulation of multi-outputs.


Figure 13. General Clamp Snubber Circuit


Figure 14. Waveforms of General Clamp Snubber Circuit


Figure 15. Enlarged View of Figure 14

Figure 16 shows the clamp snubber circuit using the SARS01/05. The surge voltage at tuning off a power MOSFET is charged to $\mathrm{C}_{\mathrm{s}}$ through the surge absorb loop. Since the reverse recovery time, trr, of the SARS01/05 is a relatively long period, the energy charged to $\mathrm{C}_{\mathrm{S}}$ is discharged to the reverse direction of the surge absorb loop until $\mathrm{C}_{\mathrm{S}}$ voltage is equal to the flyback voltage. Some discharged energy is transferred to secondary side. Thus, the power supply efficiency improves.

In addition, the power supply using the SARS01/05 reduces the ringing voltage. Thus, the cross regulation of multi-outputs can be improved.


Figure 16. Clamp Snubber Circuit using SARS01/05


Figure 17. Waveforms of Clamp Snubber Circuit using SARS01


Figure 18. Enlarged View of Figure 17

## Power Dissipation and Junction Temperature Calculation

Figure 19 shows a typical application using the SARS01/05. Figure 20 shows the operating waveforms of the SARS01/05. The power dissipation of the SARS01/05 is calculated as follows:

1) The waveforms of the SARS $01 / 05$ voltage, $\mathrm{V}_{\text {SARS }}$, and the SARS01/05 current, $\mathrm{I}_{\text {SARS }}$, are measured in actual application operation. $\mathrm{V}_{\text {SARS }} \times \mathrm{I}_{\text {SARS }}$ is calculated by the math function of oscilloscope.
2) The each average energy $\left(P_{1}, P_{2} \cdots P_{k}\right)$ is measured at period of each polarity of $V_{\text {SARS }} \times I_{\text {SARS }}\left(t_{1}, t_{2}, \cdots t_{k}\right)$ as shown in Figure 19 by the automatic measurement function of the oscilloscope.
3) The power dissipation of the SARS01/05, $\mathrm{P}_{\text {SARS }}$, is calucultaed by Equation (1):

$$
\begin{equation*}
P_{\text {SARS }}=\frac{1}{T}\left(\left|P_{1} \times t_{1}\right|+\left|P_{2} \times t_{2}\right|+\cdots\left|P_{k} \times t_{k}\right|\right) \tag{1}
\end{equation*}
$$

where:
$\mathrm{P}_{\text {SARS }}$ is power dissipation of the SARS01/05,
T is switching cycle of power MOSFET (s), and
$\mathrm{P}_{\mathrm{k}}$ is average energy of period $\mathrm{t}_{\mathrm{k}}(\mathrm{W})$.
A differential probe is recommended to use for the measurement of $\mathrm{V}_{\text {SARS }}$. Please conform to the oscilloscope manual about power dissipation measurement including the delay compensation of probe. In addition, by using the temperature of the SARS01/05 in actual application operation, the estimated junction temperature of the SARS01/05 is calculated by Equation (2). It should be enough lower than $\mathrm{T}_{\mathrm{J}}$ of the absolute maximum rating.

$$
\begin{equation*}
\mathrm{T}_{\mathrm{J}(\mathrm{SARS})}=\mathrm{T}_{\mathrm{L}}+\theta_{\mathrm{J}-\mathrm{L}} \times \mathrm{P}_{\mathrm{SARS}}\left({ }^{\circ} \mathrm{C}\right) \tag{2}
\end{equation*}
$$

## where:

$\mathrm{T}_{\mathrm{J}(\mathrm{SARS})}$ is junction temperature of the SARS01/05,
$\mathrm{T}_{\mathrm{L}}$ is lead temperature of the SARS01/05, and $\theta_{\mathrm{J}-\mathrm{L}}$ is thermal resistance between junction to lead.



Figure 20. SARS01/05 Current

## Parameter Setting of Snubber Circuit using SARS01/05

The temperature of the SARS01/05 and peripheral components should be measured in actual application operation.

The reference values of snubber circuit using the SARS01/05 are as follows:

- $\mathrm{C}_{\mathrm{S}}$

680 pF to $0.01 \mu \mathrm{~F}$.
The voltage rating is selected according to the voltage subtraced the input voltage from the peak of $\mathrm{V}_{\mathrm{DS}}$.

## - $\mathbf{R}_{\mathrm{S} 1}$

$\mathrm{R}_{\mathrm{S} 1}$ is the bias resistance to turn off the SARS01/05, and is $100 \mathrm{k} \Omega$ to $1 \mathrm{M} \Omega$.

Since a high voltage is applied to $\mathrm{R}_{\mathrm{S} 1}$ that has high resistance, the following should be considered according to the requirement of the application:

- Select a resistor designed for electromigration, or
- Connect more resistors in series so that the applied voltages of individual resistors can be reduced.
The power rating of resistor should be selected from the measurement of the effective current of $\mathrm{R}_{\mathrm{S} 1}$ based on actual operation in the application.


## - $\mathbf{R}_{\mathrm{S} 2}$

$\mathrm{R}_{\mathrm{S} 2}$ is the limited resistance in the energy discharging. The value of $22 \Omega$ to $220 \Omega$ is connected to the SARS01/05 in series.
The power rating of resistor should be selected from the measurement of the effective current of $\mathrm{R}_{\mathrm{S} 2}$ based on actual operation in the application.

Figure 19. Typical Application

## SARS01, SARS05

## Reference Design of Power Supply

This section provides the information on a reference design, including power supply specifications, a circuit diagram, the bill of materials, and transformer specifications.

- Power Supply Specifications

| Item | Specification |
| :--- | :--- |
| Input Voltage | 85 VAC to 265 VAC |
| Output Power | $34.8 \mathrm{~W}(40.4 \mathrm{~W}$ peak $)$ |
| Output 1 | $8 \mathrm{~V} / 0.5 \mathrm{~A}$ |
| Output 2 | $14 \mathrm{~V} / 2.2 \mathrm{~A}(2.6 \mathrm{~A}$ peak $)$ |

- Circuit Schematic

- Bill of Materials

| Symbol | Ratings ${ }^{(1)}$ | Recommended Part No. | Symbol | Ratings ${ }^{(1)}$ | Recommended Part No. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{C} 1^{(2)}$ | Film, $0.1 \mu \mathrm{~F}, 275 \mathrm{~V}$ |  | D52 | Schottky, $100 \mathrm{~V}, 10 \mathrm{~A}$ | SPEN-210A |
| $\mathrm{C} 2^{(2)}$ | Electrolytic, $150 \mu \mathrm{~F}, 400 \mathrm{~V}$ |  | F1 | Fuse, $250 \mathrm{~V} \mathrm{AC}$, |  |
| C3 | Ceramic, $1000 \mathrm{pF}, 1 \mathrm{kV}$ |  | L1 ${ }^{(2)}$ | CM inductor, 3.3 mH |  |
| C4 | Ceramic, $0.01 \mu \mathrm{~F}$ |  | PC1 | Optocoupler, PC123 or equiv. |  |
| C5 | Electrolytic, $22 \mu \mathrm{~F}, 50 \mathrm{~V}$ |  | $\mathrm{R1}{ }^{(3)}$ | Metal oxide, $330 \mathrm{k} \Omega$, 1 W |  |
| $\mathrm{C} 6^{(2)}$ | Ceramic, $15 \mathrm{pF} / 2 \mathrm{kV}$ |  | R2 | $47 \Omega$, 1 W |  |
| $\mathrm{C} 7^{(2)}$ | Ceramic, $2200 \mathrm{pF}, 250 \mathrm{~V}$ |  | R3 | $10 \Omega$ |  |
| C51 ${ }^{(2)}$ | Electrolytic, $680 \mu \mathrm{~F}, 25 \mathrm{~V}$ |  | $\mathrm{R} 4^{(2)}$ | $0.47 \Omega, 1 / 2 \mathrm{~W}$ |  |
| C52 | Electrolytic, $680 \mu \mathrm{~F}, 25 \mathrm{~V}$ |  | R51 | $1 \mathrm{k} \Omega$ |  |
| C53 | Electrolytic, $470 \mu \mathrm{~F}, 16 \mathrm{~V}$ |  | R52 | $1.5 \mathrm{k} \Omega$ |  |
| C54 ${ }^{(2)}$ | Ceramic, $0.1 \mu \mathrm{~F}, 50 \mathrm{~V}$ |  | R53 ${ }^{(2)}$ | $100 \mathrm{k} \Omega$ |  |
| D1 | $600 \mathrm{~V}, 1 \mathrm{~A}$ | EM01A | R54 ${ }^{(2)}$ | $6.8 \mathrm{k} \Omega$ |  |
| D2 | $600 \mathrm{~V}, 1 \mathrm{~A}$ | EM01A | R55 | $\pm 1 \%, 39 \mathrm{k} \Omega$ |  |
| D3 | $600 \mathrm{~V}, 1 \mathrm{~A}$ | EM01A | R56 | $\pm 1 \%, 10 \mathrm{k} \Omega$ |  |
| D4 | $600 \mathrm{~V}, 1 \mathrm{~A}$ | EM01A | T1 | See the Transformer Specification |  |
| D5 | $800 \mathrm{~V}, 1.0 \mathrm{~A}$ | SARS05 | U1 | IC | STR3A453D |
| D6 | Fast recovery, $200 \mathrm{~V}, 1.5 \mathrm{~A}$ | SJPX-F2 | U51 | Shunt regulator, $\mathrm{V}_{\text {REF }}=2.5 \mathrm{~V}$ | (TL431 or equiv.) |
| D51 | Schottky, $60 \mathrm{~V}, 1.5 \mathrm{~A}$ | SJPW-F6 |  |  |  |

[^1]SARS01, SARS05

- Transformer Specifications

| Item | Specification |
| :--- | :--- |
| Primary Inductance, $\mathrm{L}_{\mathrm{P}}$ | $518 \mu \mathrm{H}$ |
| Core Size | EER-28 |
| AL Value | $245 \mathrm{nH} / \mathrm{N}^{2}$ (with a center gap of about 0.56 mm ) |
| Winding Specification | See Table 1 |
| Winding Structure | See Figure 21 |

Table 1. Winding Specification

| Winding | Symbol | Number of Turns <br> (turns) | Wire Diameter (mm) | Structure |
| :---: | :---: | :---: | :---: | :--- |
| Primary Winding | P1 | 18 | $\varphi 0.23 \times 2$ | Single-layer, solenoid winding |
| Primary Winding | P2 | 28 | $\varphi 0.30$ | Single-layer, solenoid winding |
| Auxiliary Winding | D | 12 | $\varphi 0.30 \times 2$ | Solenoid winding |
| Output 1 Winding | S1-1 | 6 | $\varphi 0.4 \times 2$ | Solenoid winding |
| Output 1 Winding | S1-2 | 6 | $\varphi 0.4 \times 2$ | Solenoid winding |
| Output 2 Winding | S2-1 | 4 | $\varphi 0.4 \times 2$ | Solenoid winding |
| Output 2 Winding | S2-2 | 4 | $\varphi 0.4 \times 2$ | Solenoid winding |



Figure 21. Winding Structure

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[^0]:    ${ }^{(1)}$ See the derating curves of each product.
    ${ }^{(2)} \mathrm{R}_{\mathrm{th}(J-\mathrm{L})}$ is thermal resistance between junction and lead.

[^1]:    ${ }^{(1)}$ Unless otherwise specified, the voltage rating of capacitor is 50 V or less and the power rating of resistor is $1 / 8 \mathrm{~W}$ or less
    ${ }^{(2)}$ Refers to a part that requires adjustment based on operation performance in an actual application.
    ${ }^{(3)}$ High voltage is applied to this resistor that has high resistance. To meet your application requirements, it is required to select resistors designed for electromigration, or to connect more resistors in series so that the applied voltages of individual resistors can be reduced.

