

Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild <a href="general-regarding-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-numbers-n

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



November 2013

FDB5800

N-Channel Logic Level PowerTrench® MOSFET 60 V, 80 A, 6 m Ω

Features

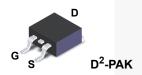
- $R_{DS(on)}$ = 4.6 m Ω (Typ.), V_{GS} = 10 V, I_D = 80 A
- High Performance Trench Technology for Extermly Low $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}$
- · Low Gate Charge
- · High Power and Current Handing Capability
- · RoHs Compliant

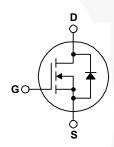
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- Power tools
- · Motor drives and Uninterruptible Power Supplies





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

| Symbol | Parameter | | FDB5800 | Unit |
|-----------------------------------|---------------------------------------------------------------------------------|--------|------------|------|
| V _{DSS} | Drain to Source Voltage | | 60 | V |
| V_{GS} | Gate to Source Voltage | | ±20 | V |
| | Drain Current - Continuous (T _C < 102°C, V _{GS} = 10 V) | | 80 | А |
| I _D | - Continuous ($T_C < 90^{\circ}C$, $V_{GS} = 5 \text{ V}$) | | 80 | Α |
| | - Continuous (T_{amb} = 25°C, V_{GS} = 10V, with $R_{\theta JA}$ = 43°C/W) | | 14 | Α |
| | - Pulsed | | Figure 4 | Α |
| E _{AS} | Single Pulse Avalanche Energy (No | ote 1) | 652 | mJ |
| | - Power Dissipation | | 242 | W |
| P_{D} | - Derate above 25°C | | 1.61 | W/°C |
| T _J , T _{STG} | - Operating and Storage Temperature | | -55 to 175 | οС |

Thermal Characteristics

| $R_{\theta JC}$ | Thermal Resistance Junction to Case TO-263, Max. | 0.62 | °C/W |
|-----------------|---------------------------------------------------------------------------------|------|------|
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient TO-263, Max. (Note 2) | 62.5 | °C/W |
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient TO-263, 1in ² copper pad area | 43 | °C/W |

Package Marking and Ordering Information

| Part Number | Top Mark | Package | Packing Method | Reel Size | Tape Width | Quantity |
|-------------|----------|---------------------|----------------|-----------|------------|-----------|
| FDB5800 | FDB5800 | D ² -PAK | Tape and Reel | 330 mm | 24 mm | 800 units |

Electrical Characteristics T_C = 25°C unless otherwise noted.

| Symbol | Parameter | Parameter Test Conditions | | Тур. | Max. | Unit |
|-------------------|--------------------------------------------------|---------------------------------------|----|------|------|------|
| Off Char | racteristics | | | | | |
| B _{VDSS} | Drain to Source Breakdown Voltage | $I_D = 250 \mu A, V_{GS} = 0 V$ | 60 | - | - | V |
| 1 | I _{DSS} Zero Gate Voltage Drain Current | V _{DS} = 48 V | - | - | 1 | μА |
| DSS | Zelo Gale Vollage Dialii Culient | $V_{GS} = 0 V$ $T_{C} = 150^{\circ}C$ | - | - | 250 | μΑ |
| I _{GSS} | Gate to Source Leakage Current | $V_{GS} = \pm 20 \text{ V}$ | - | - | ±100 | nA |
| | | | | | | |

On Characteristics

| $V_{GS(TH)}$ | Gate to Source Threshold Voltage | $V_{GS} = V_{DS}, I_{D} = 250 \mu A$ | 1.0 | - | 2.5 | V |
|---------------------|----------------------------------|-----------------------------------------------------------------------------|-----|-----|------|----|
| r _{DS(ON)} | Drain to Source On Resistance | I _D = 80 A, V _{GS} = 10 V | - | 4.6 | 6.0 | - |
| | | $I_D = 80 \text{ A}, V_{GS} = 4.5 \text{ V}$ | - | 5.8 | 7.2 | |
| | | I _D = 80 A, V _{GS} = 5 V | • | 5.5 | 7.0 | mΩ |
| | | $I_D = 80 \text{ A}, V_{GS} = 10 \text{ V},$ $T_1 = 175^{\circ}\text{C}$ | - | 10 | 12.6 | |
| | | 1j = 170 O | | | | |

Dynamic Characteristics

| C _{ISS} | Input Capacitance | V -15 V V -0 V | - | 6625 | - | pF |
|------------------|----------------------------------|---------------------------------------------------------------------------------------|-----|------|-----|----|
| C _{OSS} | Output Capacitance | V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz | - \ | 628 | - | pF |
| C _{RSS} | Reverse Transfer Capacitance | 1 - 1 1011 12 | - | 262 | - | pF |
| R_G | Gate Resistance | $V_{GS} = 0.5 V, f = 1 MHz$ | - | 1.4 | ı | Ω |
| $Q_{g(TOT)}$ | Total Gate Charge at 10V | V _{GS} = 0 V to 10 V | - | 104 | 135 | nC |
| $Q_{g(5)}$ | Total Gate Charge at 5V | V _{GS} = 0 V to 5 V | - | 55 | 72 | nC |
| $Q_{g(TH)}$ | Threshold Gate Charge | $V_{GS} = 0 \text{ V to } 1 \text{ V}$ $V_{DD} = 30 \text{ V}$ $I_{D} = 80 \text{ A}$ | - | 6.0 | - | nC |
| Q_{gs} | Gate to Source Gate Charge | $I_0 = 30 \text{ A}$ | - | 18.4 | - | nC |
| Q_{gs2} | Gate Charge Threshold to Plateau | .g | - | 12.5 | - | nC |
| Q_{gd} | Gate to Drain "Miller" Charge | | - | 20.1 | - | nC |

Switching Characteristics $(V_{GS} = 5V)$

| t _{ON} | Turn-On Time | | - / | - | 62.1 | ns |
|---------------------|---------------------|-----------------------------------------------|-----|------|------|----|
| t _{d(ON)} | Turn-On Delay Time | | -/ | 20.3 | - | ns |
| t _r | Rise Time | V _{DD} = 30 V, I _D = 80 A | - | 22.0 | - | ns |
| t _{d(OFF)} | Turn-Off Delay Time | V_{GS} = 5 V, R_{GS} = 2 Ω | - | 27.1 | - / | ns |
| t _f | Fall Time | | - | 12.1 | - / | ns |
| t _{OFF} | Turn-Off Time | | - | - | 59.0 | ns |

Drain-Source Diode Characteristics

| V _{SD} | Source to Drain Diode Voltage | I _{SD} = 80 A | - | - | 1.25 | V |
|-----------------|-------------------------------|-----------------------------------------------------------------|---|---|------|----|
| | Source to Drain Diode Voltage | I _{SD} = 40 A | - | - | 1.0 | V |
| t _r | Reverse Recovery Time | $I_{SD} = 60 \text{ A}, dI_{SD}/dt = 100 \text{ A}/\mu\text{s}$ | - | - | 44 | ns |
| $Q_{_{\rm SD}}$ | Reverse Recovered Charge | $I_{SD} = 60 \text{ A}, dI_{SD}/dt = 100 \text{ A}/\mu\text{s}$ | ı | | 57 | nC |

Notes: 1: Starting T_J = 25°C, L = 1mH, I_{AS} = 36A, V_{DD} = 54V, V_{GS} = 10V. 2: Pulse width = 100s.

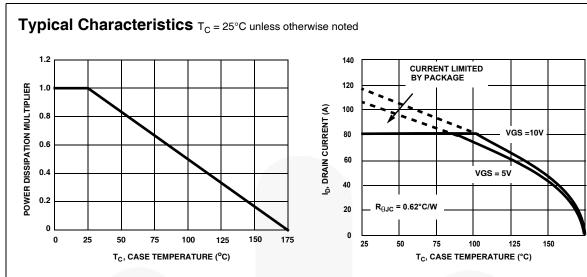


Figure 1. Normalized Power Dissipation vs Case Temperature

Figure 2. Maximum Continuous Drain Current vs Case Temperature

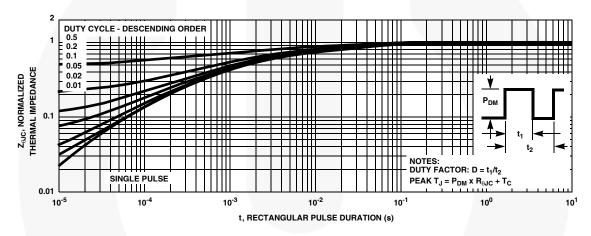


Figure 3. Normalized Maximum Transient Thermal Impedance

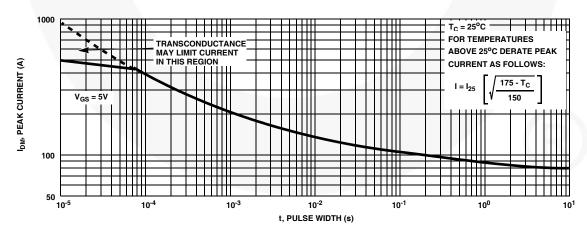
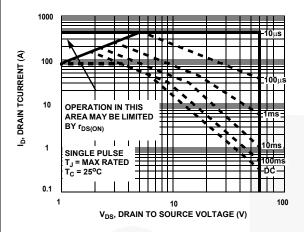


Figure 4. Peak Current Capability

Typical Characteristics $T_C = 25$ °C unless otherwise noted



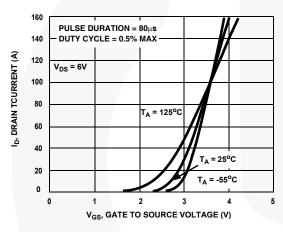
The state of the s

Figure 5. Forward Bias Safe Operating Area

NOTE: Refer to Fairchild Application Notes AN7514 and AN7515

Figure 6. Unclamped Inductive Switching

Capability



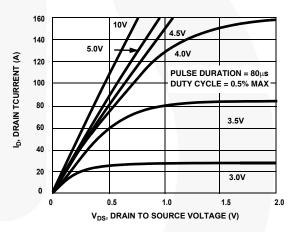
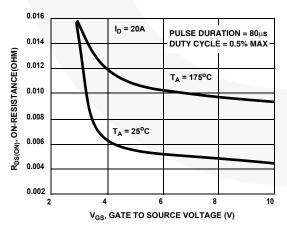


Figure 7. Transfer Characteristics

Figure 8. Saturation Characteristics



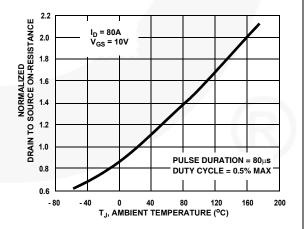


Figure 9. On-Resistance Variation vs Gate-to-

Figure 10. Normalized Drain to Source On

Typical Characteristics T_C = 25°C unless otherwise noted

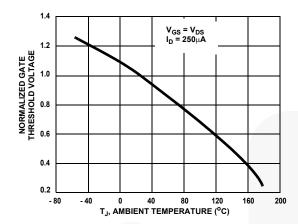


Figure 11. Normalized Gate Threshold Voltage vs
Junction Temperature

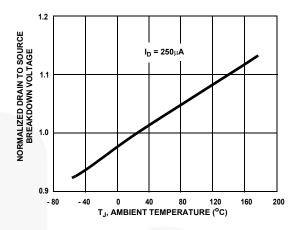


Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

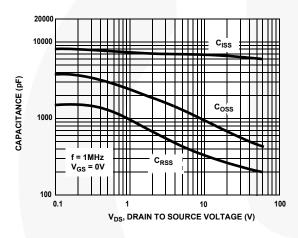


Figure 13. Capacitance vs Drain to Source Voltage

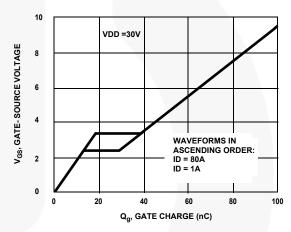


Figure 14. Gate Charge Waveforms for Constant Gate Current

Mechanical Dimensions

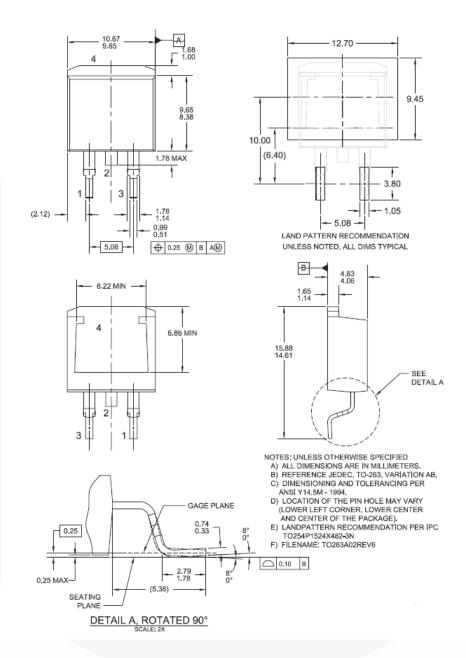


Figure 15. TO263 (D²PAK), Molded, 2-Lead, Surface Mount

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/package/packageDetails.html?id=PN_TT263-002





TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™ AX-CAP® BitSiC™ Build it Now™ CorePLUS™ CorePOWER™

 $CROSSVOLT^{\text{TM}}$ CTI ™ Current Transfer Logic™

DEUXPEED® Dual Cool™ EcoSPARK® EfficentMax™ ESBC™

Fairchild[®] Fairchild Semiconductor® FACT Quiet Series™ FACT® FAST® FastvCore™

Global Power ResourceSM GreenBridge™ Green FPS™ Green FPS™ e-Series™ G*max*™ GTO™

IntelliMAX™

F-PFS™

FRFET®

ISOPLANAR™ Marking Small Speakers Sound Louder

and Better™ MegaBuck™ MICROCOUPLER™ MicroFET™ MicroPak™ MicroPak2™

MillerDrive™ MotionMax™ mWSaver® OptoHiT™ OPTOLOGIC® OPTOPLANAR® PowerTrench® PowerXS™

Programmable Active Droop™

QFET QS™ Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™ SignalWise™

SmartMax™ SMART START™

Solutions for Your Success™

STEALTH™ SuperFET® SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SvncFET™

Sync-Lock™ SYSTEM®* TinyBoost[®] TinyBuck[®] TinyCalc™ TinyLogic[®] TINYOPTO™ TinvPower™ TinyPWM™ TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®* μSerDes™

UHC[®] Ultra FRFET™ UniFFT™ VCX™ VisualMax™ VoltagePlus™ XSTM

*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FETBench™

FPS™

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY
FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE
EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS Definition of Terms

| Datasheet Identification Product Status | | Definition |
|-------------------------------------------|------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Advance Information Formative / In Design | | Datasheet contains the design specifications for product development. Specifications may change in any manner without notice. |
| Preliminary | First Production | Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. |
| No Identification Needed Full Production | | Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design. |
| Obsolete Not In Production | | Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only. |

Rev. 166

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdt/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: FDB5800 FDB5800_F085