

74AHC138

3-TO-8 LINE DECODER DEMULTIPLEXER

Description

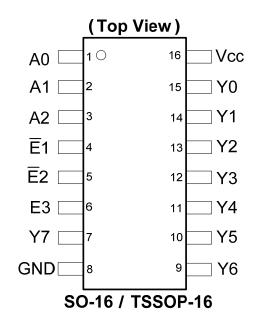
The 74AHC138 is an advanced high speed CMOS device.

The device accepts a three bit binary weighted address on input pins A0, A1 and A2 and when enabled will produce one active low output with the remaining seven being high.

There are two active LOW enable inputs $\overline{E}1$ and $\overline{E}2$, and one active HIGH enable input E3. The disabled device state results in all outputs being high. The enable state occurs with $\overline{E}1$ and $\overline{E}2$ asserted low and E3 asserted high.

The multiple enable lines allow for the parallel expansion of decoders to create 4-to-16 line versions with no additional parts and 5-to-32 versions with the addition of a single inverter.

Pin Assignments



Features

- Wide Supply Voltage Range from 2.0 V to 5.5 V
- Sinks or sources 8mA at Vcc = 4.5V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs
- Inputs accept up to 5.5 V
- ESD Protection Tested per JESD 22
 Exceeds 200-V Machine Model (A115-A)
 Exceeds 2000-V Human Body Model (A114-A)
 Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 250mA per JESD 78, Class II
- SO16 Available in "Green" Molding Compound (no Br, Sb)
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)

Applications

- Memory chip select decoding
- Demultiplexing
- Single line peripheral control
- Allow simple serial bit streams from a microcontroller to control as many peripheral lines as needed.

- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
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Pin Descriptions

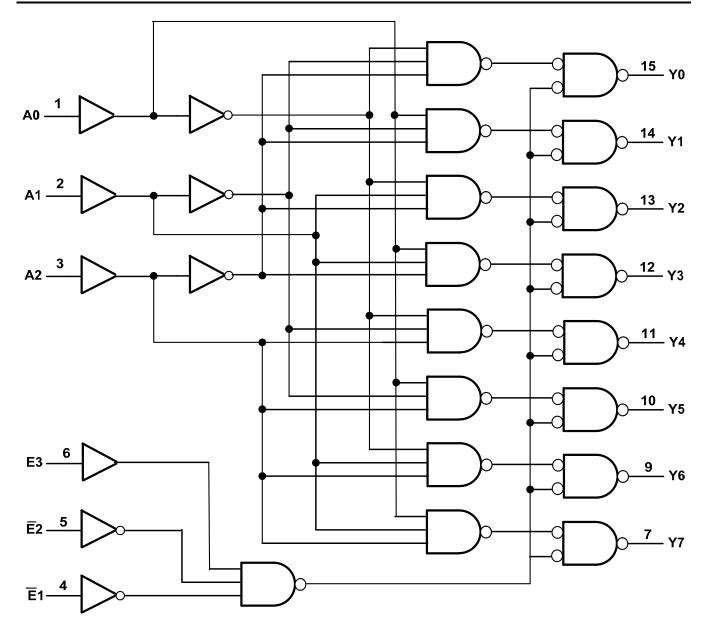
Pin Number	Pin Name	Description
1	A0	Address Input 0
2	A1	Address Input 1
3	A2	Address Input 2
4	Ē1	Enable Input 1 (active LOW)
5	Ē2	Enable Input 2 (active LOW)
6	E3	Enable Input 3 (active HIGH)
7	Y7	Output 7 (active LOW)
8	GND	Ground
9	Y6	Output 6 (active LOW)
10	Y5	Output 5 (active LOW)
11	Y4	Output 4 (active LOW)
12	Y3	Output 3 (active LOW)
13	Y2	Output 2 (active LOW)
14	Y1	Output 1 (active LOW)
15	Y0	Output o (active LOW)
16	V _{cc}	Supply Voltage

Function Table Diagram

	Control			Input					Out	put			
Ē1	E2	E3	A2	A1	A0	<u>¥</u> 7	¥6	¥5	<u>¥</u> 4	¥3	<u>¥</u> 2	<u></u> <u> </u>	¥0
Н	х	х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н
х	Н	х											
х	х	L											
L	L	Н											
			L	L	L	Н	Н	Н	Н	Н	Н	Н	L
			L	L	Н	Н	Н	Н	Н	Н	Н	L	Н
			L	Н	L	Н	Н	Н	Н	Н	L	Н	Н
			L	Н	н	Н	Н	н	Н	L	Н	Н	н
			н	L	L	Н	Н	Н	L	Н	Н	Н	н
			н	L	н	Н	Н	L	Н	Н	Н	Н	н
			н	Н	L	Н	L	Н	Н	Н	Н	Н	н
			Н	Н	н	L	Н	Н	Н	Н	Н	Н	н



Logic Diagram





Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to 7.0	V
VI	Input Voltage Range	-0.5 to 7.0	V
Vo	Voltage applied to output in high or low state	-0.3 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current VI < -0.5V	-20	mA
Ι _{ΟΚ}	Output Clamp Current V _O < -0.5V	-20	mA
loк	Output Clamp Current $V_O > V_{CC} + 0.5 V$	20	mA
Ι _Ο	Continuous output current	+/- 25	mA
Icc	Continuous current through V _{CC}	75	mA
I _{GND}	Continuous current through GND	-75	mA
T _J Operating Junction Temperature		-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT} Total Power Dissipation		500	mW

Notes: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

Recommended Operating Conditions (Note 5) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage	-	2.0	5.5	V
VI	Input Voltage	-	0	5.5	V
Vo	Output Voltage	Active Mode	0	V _{cc}	V
	Input transition rise or fall rate	$V_{CC} = 3.0 \text{ V} \pm 0.3 \text{ V}$	_	100	20/1
$\Delta t / \Delta V$	Input transition rise or fall rate	$V_{CC} = 5.0 V \pm 0.5 V$	-	20	ns/V
T _A	Operating free-air temperature	-	-40	+125	°C

Notes: 5. Unused inputs should be held at V_{CC} or Ground.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

		-		Т	′ _A = +25°	С	-40°C t	o +85°C	-40°C t	o +125°C	<u> </u>
Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit
		-	2.0V	1.5	_	_	1.5	-	1.5	-	
VIH	High-level Input Voltage	-	3.0V	2.1	-	-	2.1	-	2.1	_	V
	i chugo	-	5.5V	3.85	-	-	3.85	-	3.85	-	
		-	2.0V	-	-	0.5	_	0.5	_	0.5	
VIL	Low-level input voltage	-	3.0V	-	-	0.9	-	0.9	-	0.9	V
	, enage	-	5.5V	-	-	1.65	-	1.65	-	1.65	
		I _{он} = -50 µА	2.0V	1.9	2.0	-	1.9	-	1.9	-	
	High Level OH Output Voltage	I _{OH} = -50 μA	3.0V	2.9	3.0	-	2.9	-	2.9	-	
VOH		I _{OH} = -50 μA	4.5V	4.4	4.5	-	4.4	-	4.4	_	V
		I _{OH} = -4 mA	3.0V	2.58	-	-	2.48	-	2.40	-	
		I _{он} = -8 mA	4.5V	3.94	-	-	3.80	-	3.70	-	
		I _{OL} = 50 μA	2.0V	-	-	0.1	_	0.1	_	0.1	
		I _{OL} = 50 μA	3.0V	-	-	0.1	-	0.1	-	0.1	
V _{OL}	Low-level Output Voltage	I _{OL} = 50 μA	4.5V	-	-	0.1	_	0.1	_	0.1	V
		I _{OL} = 4 mA	3.0V	-	-	0.36	_	0.44	-	0.55	7
		I _{OL} = 8 mA	4.5V	-	-	0.36	_	0.44	-	0.55	
h	Input Current	V ₁ =GND or 5.5 V	5.5V	-	-	0.1	-	± 1	-	± 2	μA
Icc	Supply Current	$V_1 = GND \text{ or } V_{CC}$ $I_0=0 \text{ A}$	5.5V	-	-	4.0	-	40	-	80	μA
Ci	Input Capacitance	$V_i = V_{CC}$ or GND	5.5V	-	4.0	10	-	10	-	10	pF

Switching Characteristics

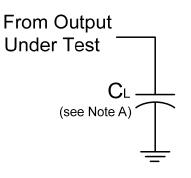
	-	Test	N.		T _A = +25°C	;	-40°C t	o +85°C	-40°C to	• +125°C	
Symbol	Parameter	Conditions	Vcc	Min	Тур.	Max	Min	Max	Min	Max	Unit
		Figure 2	3.0 V to 3.6V	0.5	4.4	8.0	0.5	9.5	0.5	11.5	
	Propagation	C _L =15 pF	4.5 V to 5.5 V	0.5	3.0	5.5	0.5	6.5	0.5	7.0	
	Delay An to Yn	Figure 2	3.0 V to 3.6V	0.5	6.2	11.5	0.5	13.0	0.5	14.5	ns
		C _L =50pF	4.5 V to 5.5 V	0.5	4.3	7.5	0.5	8.5	0.5	9.5	
	. Propagation	Figure 2	3.0 V to 3.6V	0.5	4.7	8.0	0.5	9.5	0.5	11.5	
		C _L =15 pF	4.5 V to 5.5 V	0.5	3.3	5.1	0.5	6.0	0.5	7.5	
t _{PD}	Delay E3 to Yn	Figure 2	3.0 V to 3.6V	0.5	6.8	11.5	0.5	13.0	0.5	14.5	ns
		C _L =50pF	4.5 V to 5.5 V	0.5	4.7	7.1	0.5	8.0	0.5	9.0	
		Figure 2	3.0 V to 3.6V	0.5	6.7	9.7	0.5	11.5	0.5	12.5	
	Propagation	C _L =15 pF	4.5 V to 5.5 V	0.5	4.8	6.8	0.5	8.0	0.5	8.5	
	Delay $\overline{E}n$ to $\overline{Y}n$	Figure 2	3.0 V to 3.6V	0.5	9.6	13.2	0.5	15.0	0.5	16.5	ns
		C _L =50pF	4.5 V to 5.5 V	0.5	6.8	8.8	0.5	10.0	0.5	11.0	



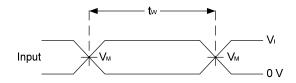
Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

	Parameter	Test Conditions	V _{CC} = 5 V TYP	Unit
C _{pd}	Power dissipation capacitance	f = 1 MHz all outputs switching-no load	18	pF

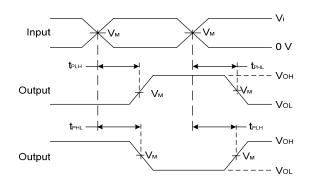
Parameter Measurement Information



Vee	Inj	outs	V	6
Vcc	VI	t _r /t _f	V _M	C _L
2.0V -6.0V	V _{CC}	6ns	V _{CC} /2	50pF
5.0V	V _{CC}	6ns	V _{CC} /2	15pF used for 5V typical test



Voltage Waveform Pulse Duration



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

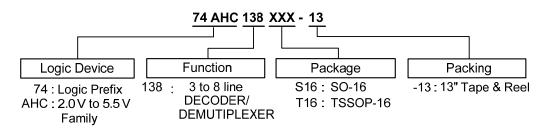
Notes: A . Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz
- C. Inputs are measured separately one transition per measurement
- D. t_{PLH} and t_{PHL} are the same as t_{PD}

Figure 1. Load Circuit and Voltage Waveforms



Ordering Information

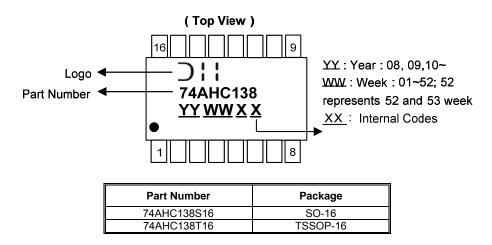


Device	Deekere Cede	Packaging	7" Tape and	Reel (Note 7
Device	Package Code	(Note 6)	Quantity	Part Number Suffix
74AHC138S16-13	S16	SO-16	2500/Tape & Reel	-13
74AHC138T16-13	T16	TSSOP-16	2500/Tape & Reel	-13

6. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf
7. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf Notes:

Marking Information

(1) SO-16, TSSOP-16

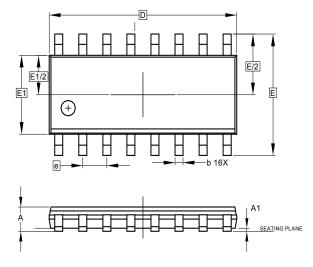


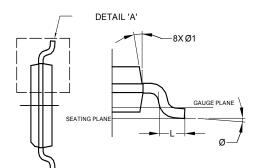


Package Outline Dimensions (All Dimensions in mm)

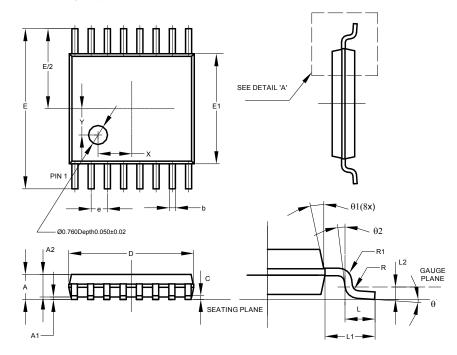
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

(1) Package Type: SO-16





(2) Package Type: TSSOP-16



	SOI	C-16	
Dim	Min	Max	Тур
Α	-	1.75	-
A1	0.10	0.25	-
b	0.31	0.51	-
с	0.10	0.25	-
D	9.80	10.00	-
Е	5.80	6.20	-
E1	3.80	4.00	-
е	-	-	1.27
L	0.40	1.27	-
Ø	0 °	8°	-
Ø1	5°	15°	-
All	Dimens	ions in r	nm

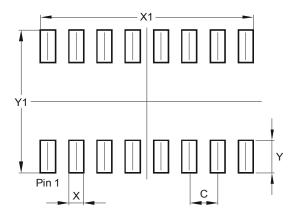
	TSSC	DP-16			
Dim	Min	Max	Тур		
Α	-	1.08	-		
A1	0.05	0.15	-		
A2	0.80	0.93	-		
b	0.19	0.30	-		
С	0.09	0.20	-		
D	4.90	5.10	-		
Е	e	6.40 BSC	2		
E1	4.30 4.50 -				
е	(0.65 BSC			
L	0.45	0.75	-		
L1	1	.00 RE	F		
L2	0).25 BS(0		
R	0.09	-	-		
R1	0.09	-	-		
Х	-	-	1.350		
Y	-	-	1.050		
Θ	0°	8°	-		
01	5°	15°	-		
02	0°	-	-		
All	Dimensi	ions in I	mm		



Suggested Pad Layout

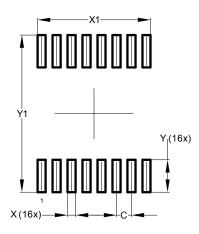
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

(1) Package Type: SO-16



Dimensions	Value (in mm)
С	1.270
Х	0.670
X1	9.560
Y	1.450
Y1	6.400

(2) Package Type: TSSOP-16



Dimensions	Value (in mm)
С	0.650
х	0.350
X1	4.900
Y	1.400
Y1	6.800



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