



| Parameter | Rating | Units |
|-----------------------|--------|----------------------|
| Blocking Voltage | 350 | V_P |
| Load Current | 120 | mA_{rms} / mA_{DC} |
| On-Resistance (max) | 35 | Ω |
| Input Control Current | 0.25 | mA |

Features

- Very Low Input Control Current (0.25mA)
- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Small 6-Pin Package
- Machine Insertable, Wave Solderable
- Surface Mount, Tape & Reel Version Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, PocketSize)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment Patient/Equipment Isolation
- Aerospace
- Industrial Controls

Description

LCA182 is a normally open (1-Form-A) solid state relay that uses optically coupled MOSFET technology to provide 3750V_{rms} of input to output isolation. It features an extremely low input control current of only 0.25mA, which is the lowest available in IXYS Integrated Circuits Division's Solid State Relay family.

Its optically coupled outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient GaAIAs infrared LED.

The LCA182 can be used to replace mechanical relays, and offers the superior reliability associated with semiconductor devices. Because it has no moving parts, it offers faster, bounce-free switching in a more compact surface mount or through-hole package.

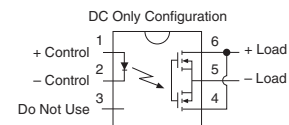
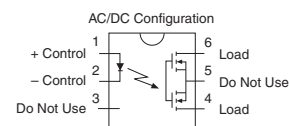
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component: TUV Certificate B 09 07 49410 004

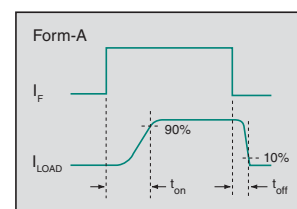
Ordering Information

| Part # | Description |
|-----------|---------------------------------|
| LCA182 | 6 Pin DIP (50/Tube) |
| LCA182S | 6 Pin Surface Mount (50/Tube) |
| LCA182STR | 6 Pin Surface Mount (1000/Reel) |

Pin Configuration



Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings @ 25°C

| Parameter | Ratings | Units |
|--------------------------------------|-------------|------------------|
| Blocking Voltage | 350 | V _P |
| Reverse Input Voltage | 5 | V |
| Input Control Current | 50 | mA |
| Peak (10ms) | 1 | A |
| Input Power Dissipation ¹ | 150 | mW |
| Total Power Dissipation ² | 800 | mW |
| Isolation Voltage, Input to Output | 3750 | V _{rms} |
| Operational Temperature | -40 to +85 | °C |
| Storage Temperature | -40 to +125 | °C |

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

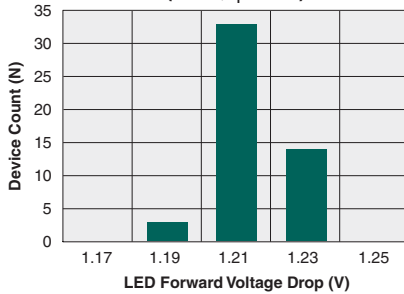
Electrical Characteristics @ 25°C

| Parameter | Conditions | Symbol | Min | Typ | Max | Units |
|------------------------------------------------|------------------------------------------|-------------------|------|-----|------|--------------------------------------|
| Output Characteristics | | | | | | |
| Load Current, Continuous | | | | | | |
| AC/DC Configuration | - | I _L | - | - | 120 | mA _{rms} / mA _{DC} |
| DC Configuration | | | | | 200 | |
| Peak Load Current | t=10ms | I _{LPK} | - | - | 350 | mA |
| On-Resistance | | | | | | |
| AC/DC Configuration | I _L =120mA | R _{ON} | - | 23 | 35 | Ω |
| DC Configuration | I _L =200mA | | | 7 | 10 | |
| Off-State Leakage Current | V _L =350V _P | I _{LEAK} | - | - | 1 | μA |
| Switching Speeds | | | | | | |
| Turn-On | I _F =1mA, V _L =10V | t _{on} | - | - | 3 | ms |
| Turn-Off | | t _{off} | - | - | 3 | |
| Output Capacitance | V _L =50V, f=1MHz | C _{OUT} | - | 25 | - | pF |
| Input Characteristics | | | | | | |
| Input Control Current to Activate ¹ | I _L =120mA | I _F | - | - | 0.25 | mA |
| Input Control Current to Deactivate | - | I _F | 0.05 | - | - | mA |
| Input Voltage Drop | I _F =1mA | V _F | 0.9 | 1.2 | 1.4 | V |
| Reverse Input Current | V _R =5V | I _R | - | - | 10 | μA |
| Common Characteristics | | | | | | |
| Input to Output Capacitance | - | C _{I/O} | - | 3 | - | pF |

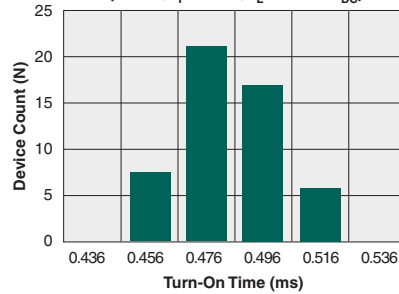
¹ It is recommended that the input control current be increased to 1mA in high temperature (>55°C) operation.

PERFORMANCE DATA @25°C (Unless Otherwise Noted)*

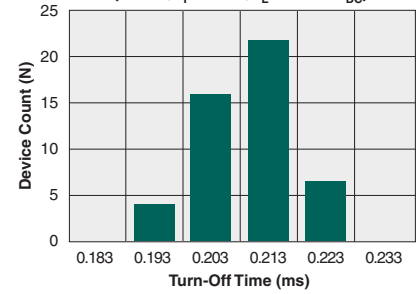
Typical LED Forward Voltage Drop
(N=50, I_F=5mA)



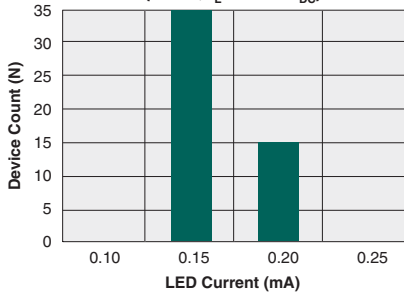
Typical Turn-On Time
(N=50, I_F=2mA, I_L=120mA_{DC})



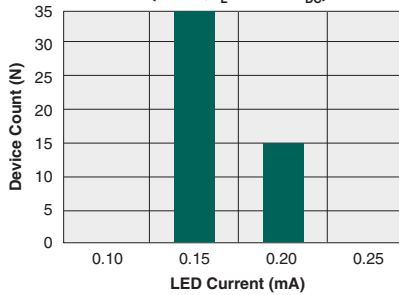
Typical Turn-Off Time
(N=50, I_F=2mA, I_L=120mA_{DC})



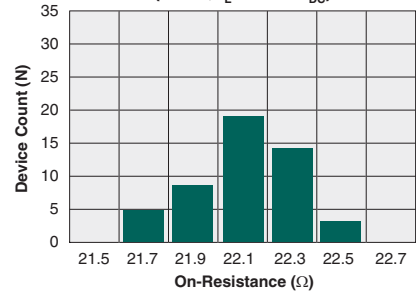
Typical I_F for Switch Operation
(N=50, I_L=120mA_{DC})



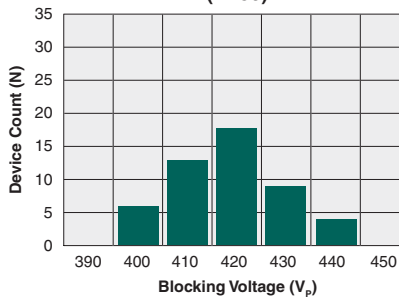
Typical I_F for Switch Dropout
(N=50, I_L=120mA_{DC})



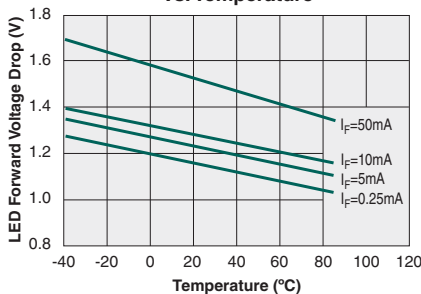
Typical On-Resistance Distribution
(N=50, I_L=120mA_{DC})



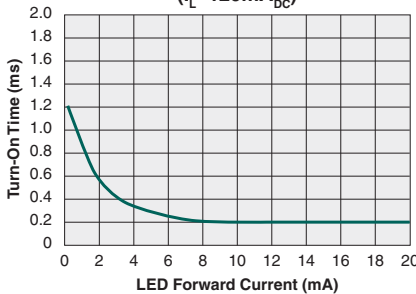
Typical Blocking Voltage Distribution
(N=50)



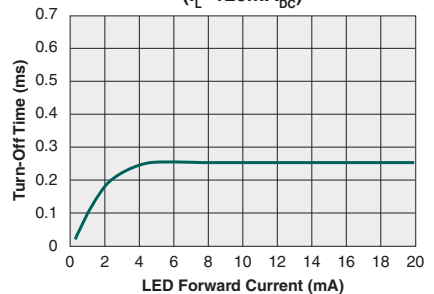
Typical LED Forward Voltage Drop vs. Temperature



Typical Turn-On Time vs. LED Forward Current
(I_L=120mA_{DC})



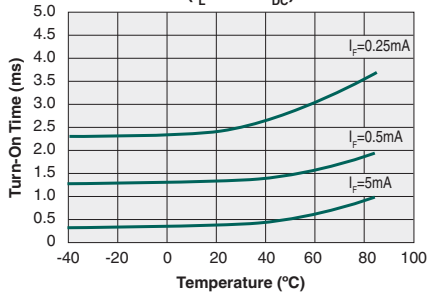
Typical Turn-Off Time vs. LED Forward Current
(I_L=120mA_{DC})



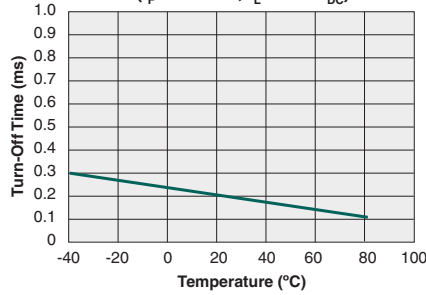
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA @25°C (Unless Otherwise Noted)*

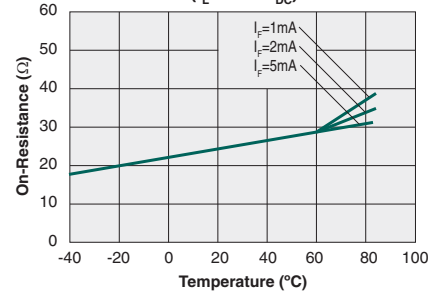
Typical Turn-On time vs. Temperature
($I_L=70mA_{DC}$)



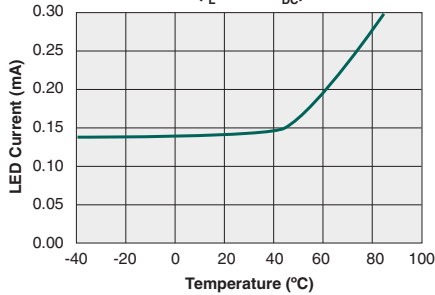
Typical Turn-Off Time vs. Temperature
($I_F=0.25mA, I_L=70mA_{DC}$)



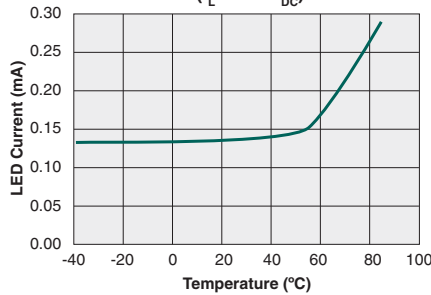
Typical On-Resistance vs. Temperature
($I_L=70mA_{DC}$)



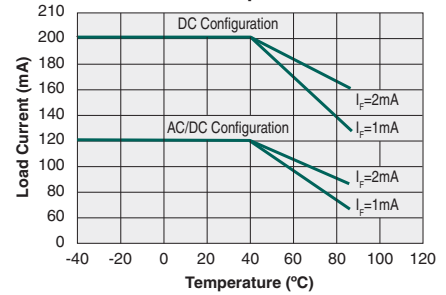
Typical I_F for Switch Operation vs. Temperature
($I_L=70mA_{DC}$)



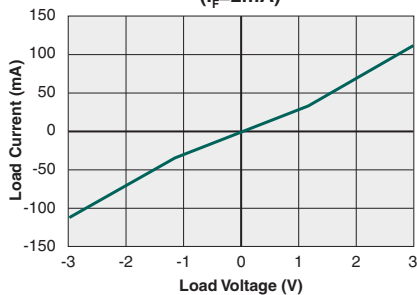
Typical I_F for Switch Dropout vs. Temperature
($I_L=70mA_{DC}$)



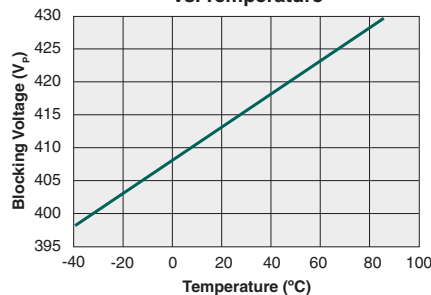
Maximum Load Current vs. Temperature



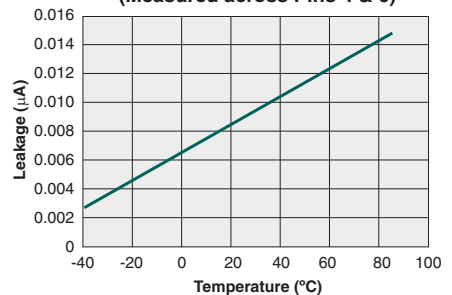
Typical Load Current vs. Load Voltage
($I_F=2mA$)



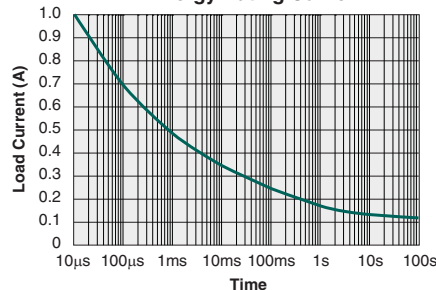
Typical Blocking Voltage vs. Temperature



Typical Leakage vs. Temperature
(Measured across Pins 4 & 6)



Energy Rating Curve



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

| Device | Moisture Sensitivity Level (MSL) Rating |
|------------------|-----------------------------------------|
| LCA182 / LCA182S | MSL 1 |

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

| Device | Maximum Temperature x Time |
|------------------|----------------------------|
| LCA182 / LCA182S | 250°C for 30 seconds |

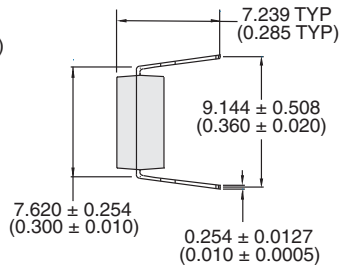
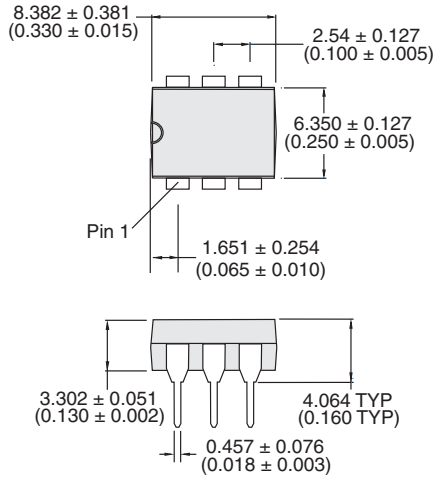
Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

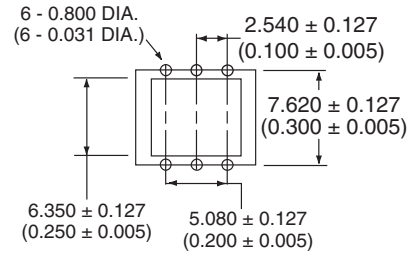


MECHANICAL DIMENSIONS

LCA182

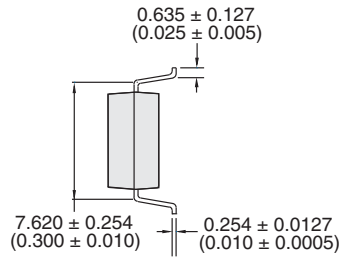
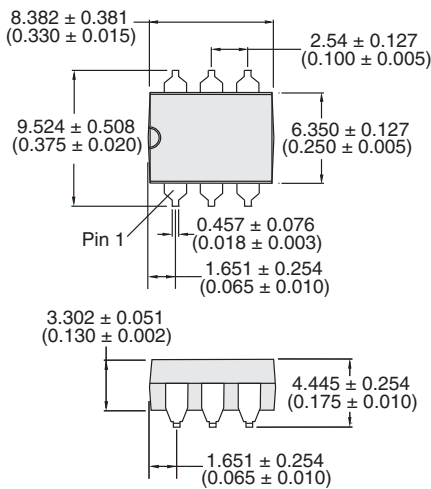


PCB Hole Pattern

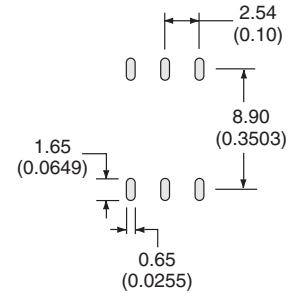


Dimensions
mm
(inches)

LCA182S

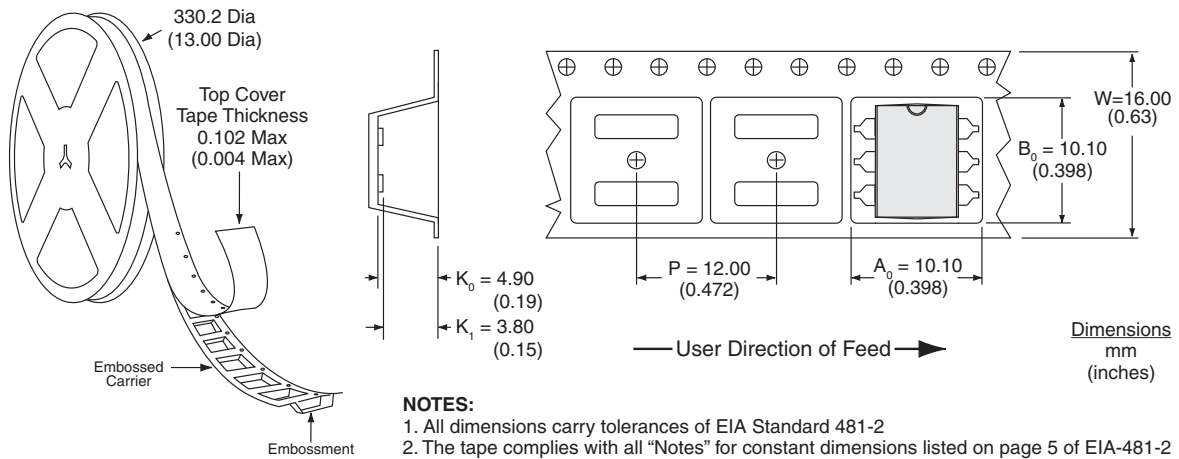


PCB Land Pattern



Dimensions
mm
(inches)

LCA182STR Tape & Reel



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