## **Switching Transistor**

## **PNP Silicon**

#### **Features**

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

| Rating                         | Symbol           | Value | Unit |
|--------------------------------|------------------|-------|------|
| Collector – Emitter Voltage    | $V_{CEO}$        | -40   | Vdc  |
| Collector - Base Voltage       | V <sub>CBO</sub> | -40   | Vdc  |
| Emitter-Base Voltage           | V <sub>EBO</sub> | -5.0  | Vdc  |
| Collector Current – Continuous | Ic               | -600  | mAdc |
| Collector Current – Peak       | I <sub>CM</sub>  | -900  | mAdc |

### THERMAL CHARACTERISTICS

| Characteristic  | Symbol                            | Max         | Unit        |
|---|-----------------------------------|-------------|-------------|
| Total Device Dissipation FR-5 Board<br>(Note 1) @T <sub>A</sub> = 25°C<br>Derate above 25°C         | P <sub>D</sub>                    | 225<br>1.8  | mW<br>mW/°C |
| Thermal Resistance, Junction-to-Ambient   | $R_{\theta JA}$                   | 556         | °C/W        |
| Total Device Dissipation Alumina<br>Substrate, (Note 2) @T <sub>A</sub> = 25°C<br>Derate above 25°C | P <sub>D</sub>                    | 300<br>2.4  | mW<br>mW/°C |
| Thermal Resistance, Junction-to-Ambient   | $R_{\theta JA}$                   | 417         | °C/W        |
| Junction and Storage Temperature  | T <sub>J</sub> , T <sub>stg</sub> | -55 to +150 | °C          |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

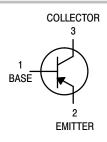
\*Transient pulses must not cause the junction temperature to be exceeded.

- 1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
- 2. Alumina =  $0.4 \times 0.3 \times 0.024$  in. 99.5% alumina.



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SOT-23 (TO-236) CASE 318 STYLE 6

#### **MARKING DIAGRAM**



2T = Specific Device Code\*

M = Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)

\*Specific Device Code, Date Code or overbar orientation and/or location may vary depending upon manufacturing location. This is a representation only and actual devices may not match this drawing exactly.

### **ORDERING INFORMATION**

| Device        | Package             | Shipping <sup>†</sup>   |
|---------------|---------------------|-------------------------|
| MMBT4403LT1G  | SOT-23<br>(Pb-Free) | 3000 / Tape & Reel      |
| SMMBT4403LT1G | SOT-23<br>(Pb-Free) | 3000 / Tape & Reel      |
| MMBT4403LT3G  | SOT-23<br>(Pb-Free) | 10,000 / Tape &<br>Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

| (                                      | Symbol  | Min                  | Max                          | Unit                    |                    |  |
|--|---|----------------------|------------------------------|-------------------------|--------------------|--|
| OFF CHARACTERISTICS                    |   |                      | •                            |                         |                    |  |
| Collector - Emitter Breakdown Voltage  | V <sub>(BR)CEO</sub>  | -40                  | _                            | Vdc                     |                    |  |
| Collector - Base Breakdown Voltage     | $(I_C = -0.1 \text{ mAdc}, I_E = 0)$  | V <sub>(BR)CBO</sub> | -40                          | -                       | Vdc                |  |
| Emitter-Base Breakdown Voltage         | $(I_E = -0.1 \text{ mAdc}, I_C = 0)$  | V <sub>(BR)EBO</sub> | -5.0                         | -                       | Vdc                |  |
| Base Cutoff Current                    | $(V_{CE} = -35 \text{ Vdc}, V_{EB} = -0.4 \text{ Vdc})$   | I <sub>BEV</sub>     | _                            | -0.1                    | μAdc               |  |
| Collector Cutoff Current               | $(V_{CE} = -35 \text{ Vdc}, V_{EB} = -0.4 \text{ Vdc})$   | I <sub>CEX</sub>     | _                            | -0.1                    | μAdc               |  |
| ON CHARACTERISTICS                     |   |                      |                              |                         | •                  |  |
| DC Current Gain (Note 3) (Note 3)      | $ \begin{array}{l} (I_{C} = -0.1 \text{ mAdc},  V_{CE} = -1.0  \text{Vdc}) \\ (I_{C} = -1.0  \text{mAdc},  V_{CE} = -1.0  \text{Vdc}) \\ (I_{C} = -10  \text{mAdc},  V_{CE} = -1.0  \text{Vdc}) \\ (I_{C} = -150  \text{mAdc},  V_{CE} = -2.0  \text{Vdc}) \\ (I_{C} = -500  \text{mAdc},  V_{CE} = -2.0  \text{Vdc}) \end{array} $ | h <sub>FE</sub>      | 30<br>60<br>100<br>100<br>20 | -<br>-<br>-<br>300<br>- | -                  |  |
| Collector – Emitter Saturation Voltage | (Note 3) $ \begin{aligned} (I_C = -150 \text{ mAdc}, I_B = -15 \text{ mAdc}) \\ (I_C = -500 \text{ mAdc}, I_B = -50 \text{ mAdc}) \end{aligned} $   | V <sub>CE(sat)</sub> | -<br>-                       | -0.4<br>-0.75           | Vdc                |  |
| Base-Emitter Saturation Voltage (Not   | e 3) $ \begin{aligned} \text{(I}_{\text{C}} &= -150 \text{ mAdc, I}_{\text{B}} = -15 \text{ mAdc)} \\ \text{(I}_{\text{C}} &= -500 \text{ mAdc, I}_{\text{B}} = -50 \text{ mAdc)} \end{aligned} $   | V <sub>BE(sat)</sub> | -0.75<br>-                   | -0.95<br>-1.3           | Vdc                |  |
| SMALL-SIGNAL CHARACTERISTIC            | s   |                      |                              |                         |                    |  |
| Current-Gain - Bandwidth Product       | rrent-Gain – Bandwidth Product (I <sub>C</sub> = -20 mAdc, V <sub>CE</sub> = -10 Vdc, f = 100 MHz)  |                      | 200                          | _                       | MHz                |  |
| Collector-Base Capacitance             | $(V_{CB} = -10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$  | C <sub>cb</sub>      | _                            | 8.5                     | pF                 |  |
| Emitter-Base Capacitance               | Base Capacitance $(V_{BE} = -0.5 \text{ Vdc}, I_{C} = 0, f = 1.0 \text{ MHz})$  |                      | _                            | 30                      | pF                 |  |
| Input Impedance                        | $(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz})$  | h <sub>ie</sub>      | 1.5                          | 15                      | kΩ                 |  |
| Voltage Feedback Ratio                 | $(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz})$  | h <sub>re</sub>      | 0.1                          | 8.0                     | X 10 <sup>-4</sup> |  |
| Small – Signal Current Gain            | $(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz})$  | h <sub>fe</sub>      | 60                           | 500                     | -                  |  |
| Output Admittance                      | $(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz})$  | h <sub>oe</sub>      | 1.0                          | 100                     | μMhos              |  |
| SWITCHING CHARACTERISTICS              |   |                      |                              |                         |                    |  |
| Delay Time                             | $(V_{CC} = -30 \text{ Vdc}, V_{EB} = -2.0 \text{ Vdc},$   | t <sub>d</sub>       | _                            | 15                      |                    |  |
| Rise Time                              | $I_C = -150 \text{ mAdc}, I_{B1} = -15 \text{ mAdc})$   | t <sub>r</sub>       | _                            | 20                      | ns                 |  |
| Storage Time                           | $(V_{CC} = -30 \text{ Vdc}, I_C = -150 \text{ mAdc},$   | t <sub>s</sub>       | _                            | 225                     | ns                 |  |
| Fall Time                              | $I_{B1} = I_{B2} = -15 \text{ mAdc}$  | t <sub>f</sub>       | _                            | 30                      | 110                |  |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width  $\leq 300 \,\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

#### SWITCHING TIME EQUIVALENT TEST CIRCUIT

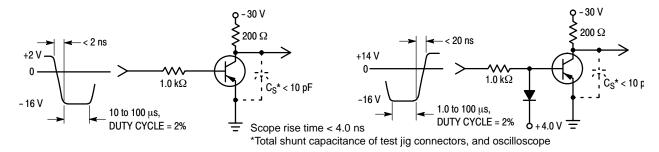
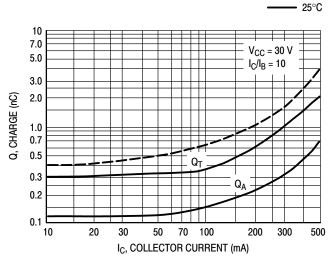


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

#### TRANSIENT CHARACTERISTICS

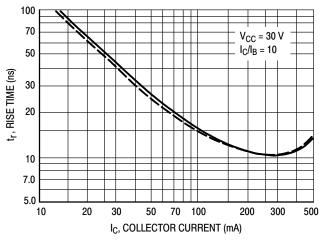
—— 100°C



100  $I_{C}/I_{B} = 10$ 70 50 @ V<sub>CC</sub> = 30 V @  $V_{CC} = 10 V$ 30 t, TIME (ns)  $t_d @ V_{BE(off)} = 2 V$ 20  $t_d @ V_{BE(off)} = 0$ 10 7.0 10 20 70 200 300 500 I<sub>C</sub>, COLLECTOR CURRENT (mA)

Figure 3. Charge Data

Figure 4. Turn-On Time



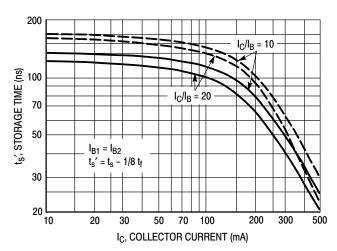
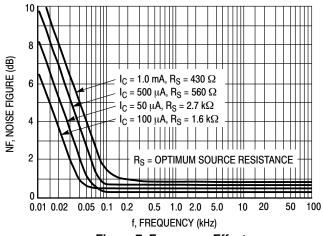


Figure 5. Rise Time

Figure 6. Storage Time

## SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE

 $V_{CE} = -10 \text{ Vdc}$ ,  $T_A = 25^{\circ}\text{C}$ ; Bandwidth = 1.0 Hz



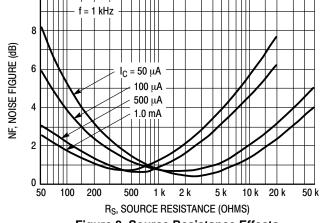


Figure 7. Frequency Effects

Figure 8. Source Resistance Effects

#### **h PARAMETERS**

$$V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz}, T_A = 25^{\circ}\text{C}$$

This group of graphs illustrates the relationship between  $h_{fe}$  and other "h" parameters for this series of transistors. To obtain these curves, a high–gain and a low–gain unit were selected from the MMBT4403LT1 lines, and the same units were used to develop the correspondingly numbered curves on each graph.

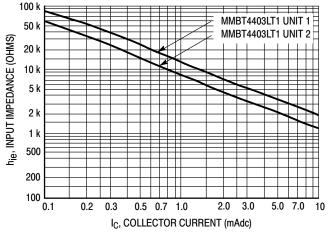


Figure 9. Input Impedance

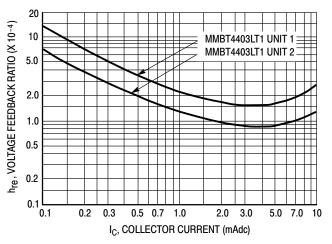


Figure 10. Voltage Feedback Ratio

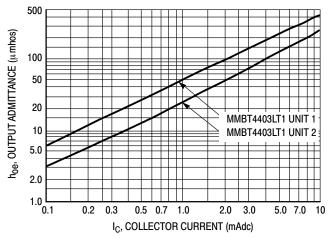


Figure 11. Output Admittance

#### STATIC CHARACTERISTICS

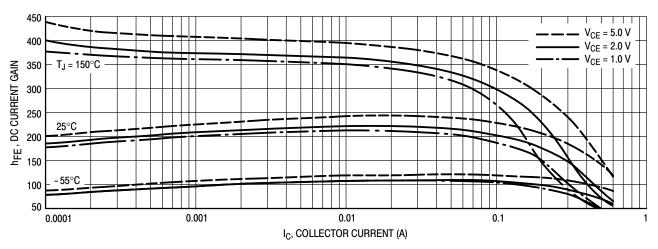


Figure 12. DC Current Gain

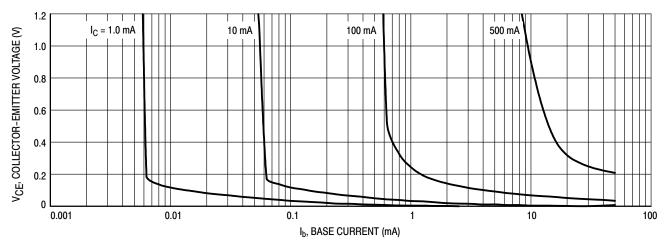


Figure 13. Collector Saturation Region

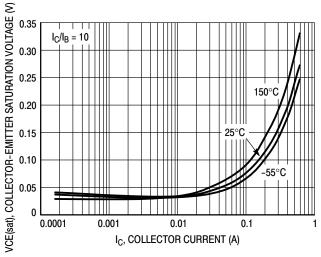


Figure 14. Collector–Emitter Saturation Voltage vs. Collector Current

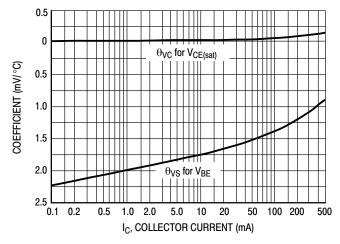


Figure 15. Temperature Coefficients

#### STATIC CHARACTERISTICS

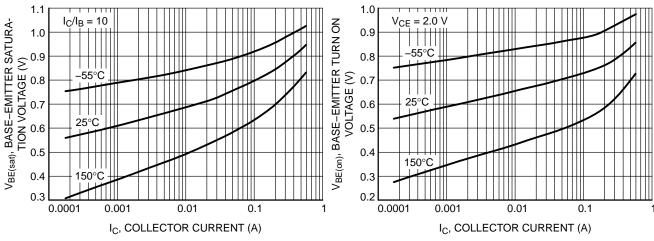


Figure 16. Base-Emitter Saturation Voltage vs. **Collector Current** 

Figure 17. Base-Emitter Turn On Voltage vs. **Collector Current** 

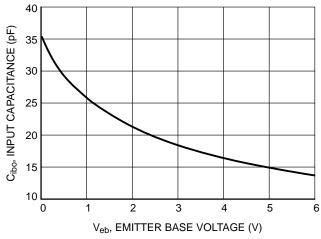


Figure 18. Input Capacitance vs. Emitter Base Voltage

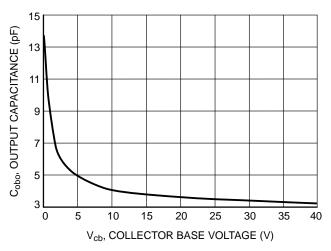


Figure 19. Output Capacitance vs. Collector Base Voltage

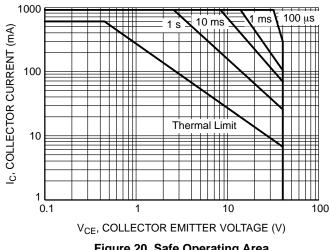


Figure 20. Safe Operating Area

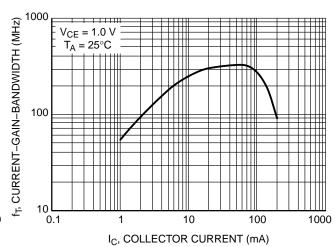
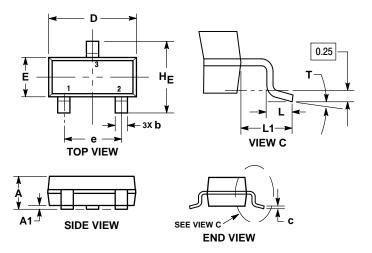


Figure 21. Current-Gain-Bandwidth Product

#### PACKAGE DIMENSIONS

#### SOT-23 (TO-236) CASE 318-08 **ISSUE AR**



#### NOTES:

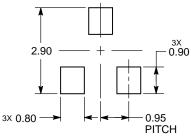
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
  MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH.
- PROTRUSIONS, OR GATE BURRS.

|     | MILLIMETERS |      |      | INCHES |       |       |
|-----|-------------|------|------|--------|-------|-------|
| DIM | MIN         | NOM  | MAX  | MIN    | NOM   | MAX   |
| Α   | 0.89        | 1.00 | 1.11 | 0.035  | 0.039 | 0.044 |
| A1  | 0.01        | 0.06 | 0.10 | 0.000  | 0.002 | 0.004 |
| b   | 0.37        | 0.44 | 0.50 | 0.015  | 0.017 | 0.020 |
| С   | 0.08        | 0.14 | 0.20 | 0.003  | 0.006 | 0.008 |
| D   | 2.80        | 2.90 | 3.04 | 0.110  | 0.114 | 0.120 |
| E   | 1.20        | 1.30 | 1.40 | 0.047  | 0.051 | 0.055 |
| е   | 1.78        | 1.90 | 2.04 | 0.070  | 0.075 | 0.080 |
| L   | 0.30        | 0.43 | 0.55 | 0.012  | 0.017 | 0.022 |
| L1  | 0.35        | 0.54 | 0.69 | 0.014  | 0.021 | 0.027 |
| HE  | 2.10        | 2.40 | 2.64 | 0.083  | 0.094 | 0.104 |
| Т   | 0°          | -    | 10°  | 0°     | -     | 10°   |

#### STYLE 6:

- PIN 1. 2. BASE
  - EMITTER
  - COLLECTOR

#### **RECOMMENDED** SOLDERING FOOTPRINT\*



**DIMENSIONS: MILLIMETERS** 

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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