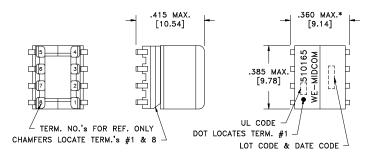
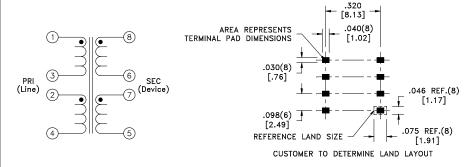
**Vidcom** 



## \* DIMENSION MAY BE EXCEEDED WITH SOLDER ONLY





## ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	1-3	@20°C	0.500 ohms max.
D.C. RESISTANCE	2-4	@20°C	0.500 ohms max.
D.C. RESISTANCE	8-6	@20°C	0.250 ohms max.
D.C. RESISTANCE	7-5	@20°C	0.250 ohms max.
INDUCTANCE	1-4	tie(2+3), 10kHz, 100mVAC, Ls	1.20mH ±10%
LEAKAGE INDUCTANCE	1-4	tie(2+3, 6+7, 5+8), 1MHz, 100mVAC, Ls	3uH max.
INTERWINDING CAPACITANCE	1-8	tie(2+3, 6+7), 100kHz, 100mVAC, Cs	60pF max.
DIELECTRIC	1-8	tie(2+3, 6+7), 1875VAC, 1 second	1500VAC, 1 minute
TURNS RATIO		(1-4):(8-5), tie(2+3, 6+7)	3:1, ±1.5%
TOTAL HARMONIC DISTORTION		40kHz, 2Vrms across PRI, 11 ohm load, 100 ohm input, tie(2+3, 6+7)	-80dB max.
LONGITUDINAL BALANCE 1		20k - 2MHz	50dB min.
10 100 100 100 100			

¹Per ITU Method (L->M).

## **GENERAL SPECIFICATIONS:**

OPERATING TEMPERATURE RANGE: -40°C to +85°C.

Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:

- Supplementary insulation for a primary circuit at a working voltage of 250Vrms.

DETAILS SUBJECT TO CHANGE

DRAWING TITLE

**TRANSFORMER** 

750510165

PART NO.

in brackets are in millimeters.

eiSos p/n: **750510165** 

Packaging Specifications REV. DATE Method: Tape & Reel PKG-0410 www.midcom-inc.com 12/08

10/08

CONVENTION PLACEMENT SEE REVISION SHEET FOR REVISION LEVEL

Tolerances unless otherwise specified:

Decimals: ±.005 [.13] Footprint: ±.005 [.13] Angles: ±1° Fractions: ±1/64 This drawing is dual dimensioned.

AGENCY NUMBER

UL CODE

FILE# E205930

## **Mouser Electronics**

**Authorized Distributor** 

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Wurth Electronics: 750510165