PI5C3257

**Quad 2:1 Mux/DeMux Bus Switch**

Features

→ Near-Zero propagation delay

→ 5Ω switches connect inputs to outputs

→ Direct bus connection when switches are ON

→ Ultra Low Quiescent Power (0.2µA typical)
  – Ideally suited for notebook applications

→ Pin compatible with 74 series 257 logic devices

→ Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

→ Halogen and Antimony Free. "Green" Device (Note 3)

→ Packaging (Pb-free & Green available):
  – 16-pin, QSOP (Q)
  – 16-pin, TSSOP (L)
  – 16-pin, UQFN (ZHD)

Pin Configuration (QSOP, TSSOP)
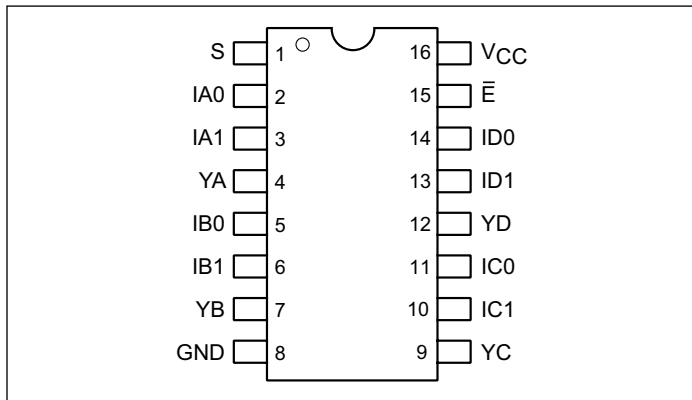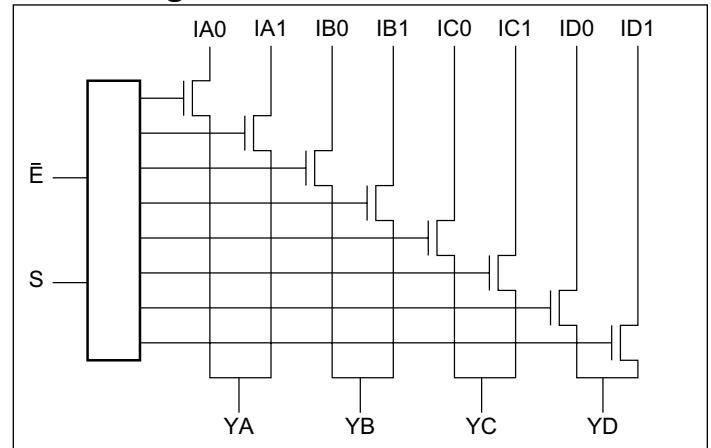

Pin Configuration (UQFN)
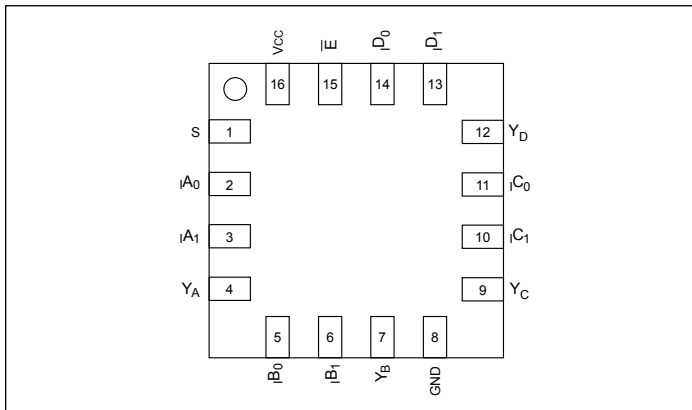
Description

The PI5C3257 is a Quad 2:1 multiplexer/demultiplexer with three-state outputs that is pinout and function compatible with the PI74FCT257T, 74F257, and 74ALS/AS/LS257. Inputs can be connected to outputs with low On-Resistance (5Ω) with no additional ground bounce noise or propagation delay.

Block Diagram

Truth Table[1]

$\overline{E}$	S	YA	YB	YC	YD	Function
H	X	Hi-Z	Hi-Z	Hi-Z	Hi-Z	Disable
L	L	IA0	IB0	IC0	ID0	S = 0
L	H	IA1	IB1	IC1	ID1	S = 1

Note:
1.   H = High Voltage Level, L = Low Voltage Level

Pin Description

Pin Name	Description
IAn-IDn	Data Inputs
S	Select Inputs
$\overline{E}$	Enable
YA-YD	Data Outputs
GND	Ground
V_{CC}	Power

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature ....–65°C to +150°C

Ambient Temperature with Power Applied ....–40°C to +85°C

Supply Voltage to Ground Potential (Inputs & V_{CC} Only)....–0.5V to +7.0V

Supply Voltage to Ground Potential (Outputs & D/O Only) .–0.5V to +7.0V

DC Input Voltage ....–0.5V to +7.0V

DC Output Current... 120mA

Power Dissipation 0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±5%)

Parameters	Description	Test Conditions[1]	Min.	Typ.[2]	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}			±1	µA
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND			±1	
I_{OZH}	High Impedance Output Current	0 ≤ I, Y ≤ V_{CC}			±1	
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = –18 mA		–0.7	–1.2	V
I_{OS}	Short Circuit Current[3]	I (Y) = 0V, Y (I) = V_{CC}	100			mA
V_H	Input Hysteresis at Control Pins			150		mV
R_{ON}	Switch On-Resistance[4]	V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA		5	7	Ω
		V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA		10	15	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.

3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

4. Measured by the voltage drop between I and Y pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I,Y) pins.

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters[1]	Description	Test Conditions	Typ.	Max.	Units
C_{IN}	Input Capacitance			6	pF
C_{OFF}	In/Yn Capacitance, Switch Off	V_{IN} = 0V		6	
C_{ON}	In/Yn Capacitance, Switch On			14	

Notes:

1. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions[1]		Min.	Typ.[2]	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max.	V_{IN} = GND or V_{CC}		0.1	3.0	μA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V_{CC} = Max.	V_{IN} = 3.4V[3]			2.5	mA
I_{CCD}	Supply Current per Input per MHz[4]	V_{CC} = Max., I and Y Pins Open $\overline{BE}$ = GND Control Input Toggling 50% Duty Cycle				0.25	mA/MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.

2. Typical values are at V_{CC} = 5.0V, +25°C ambient.

3. Per TTL driven input (V_{IN} = 3.4V, control inputs only); I and Y pins do not contribute to I_{CC}.

4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

| Parameters	Description	Conditions	Com.		Units
			Min.	Max.	
t_{IY}	Propagation Delay[1,2] In to Yn	C_L = 50pF R_L = 500Ω		0.25	ns
t_{SY}	Bus Select Time Sn to Yn		0.5	5.2	
t_{PZH} t_{PZL}	Bus Enable Time $\overline{E}$ to Yn		0.5	4.8	
t_{PHZ} t_{PLZ}	Bus Disable Time $\overline{E}$ to Yn		0.5	5.0	

Notes:

1. This parameter is guaranteed but not tested on Propagation Delays.

2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.
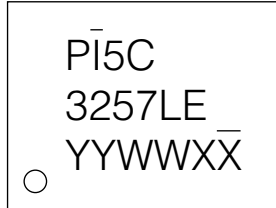
Part marking

Q Package
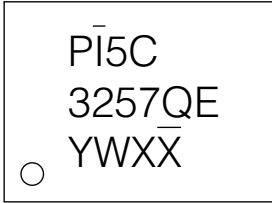
PĪ5C
3257QE
YWXX̄

Y: Year
W: Workweek
1st X: Assembly Site Code
2nd X: Fab Site Code
Bar above "I" means Fab3 of MGN
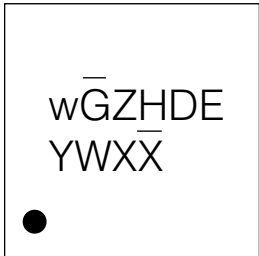Bar above fab code means Cu wire

L Package

PĪ5C
3257LE ̄
YYWWXX̄

YY: Year
WW: Workweek
1st X: Assembly Site Code
2nd X: Fab Site Code
Bar above "I" means Fab3 of MGN
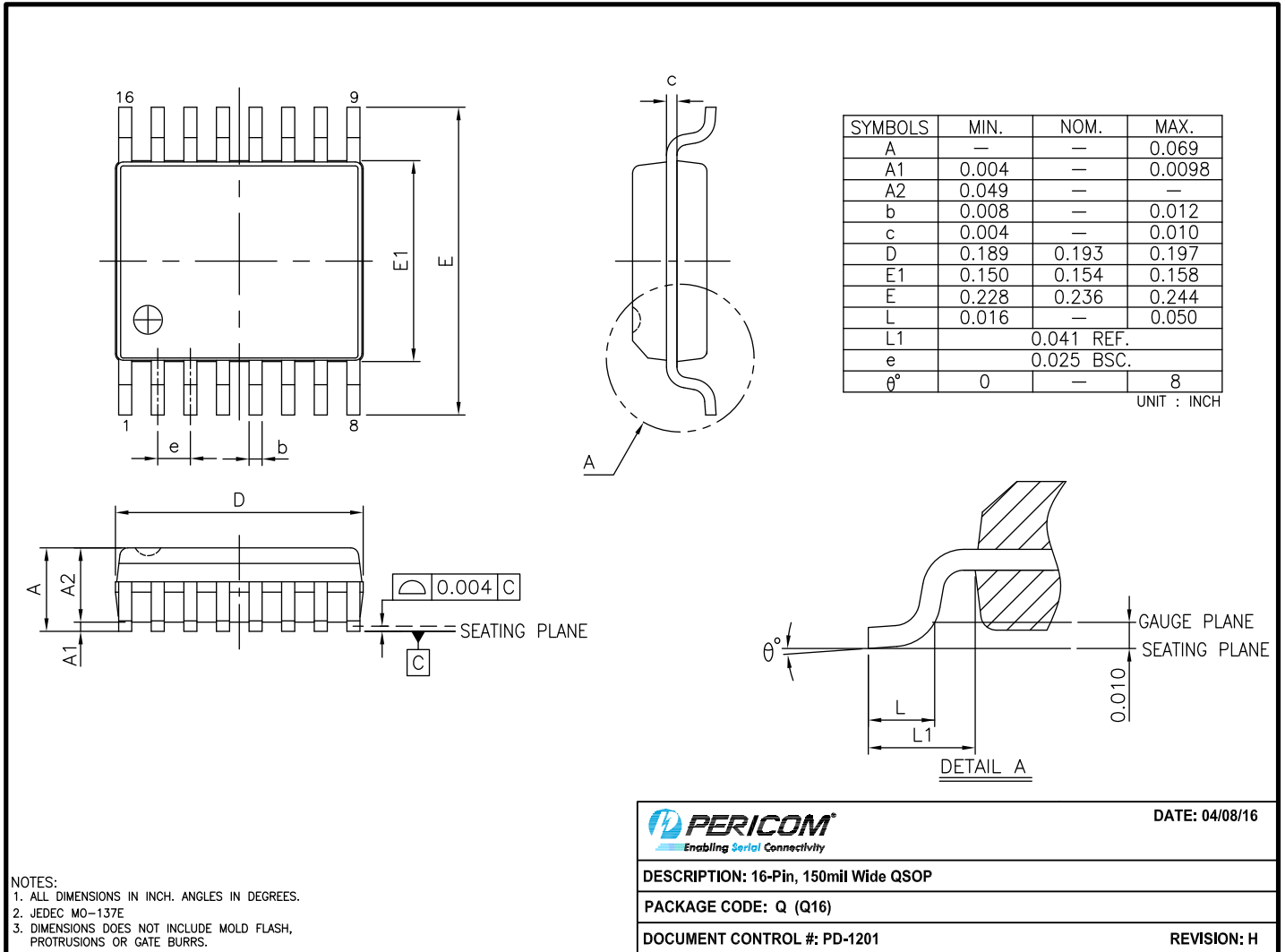Bar above fab code means Cu wire

ZHD Package

wG = PI5C3257ZHDE

wḠZHDE
YWXX̄

Y: Year
W: Workweek
1st X: Assembly Site Code
2nd X: Fab Site Code
Bar above "G" means Fab3 of MGN
Bar above 2nd "X" means Cu wire

Packaging Mechanical: 16-QSOP (Q)

SYMBOLS	MIN.	NOM.	MAX.
A	—	—	0.069
A1	0.004	—	0.0098
A2	0.049	—	—
b	0.008	—	0.012
c	0.004	—	0.010
D	0.189	0.193	0.197
E1	0.150	0.154	0.158
E	0.228	0.236	0.244
L	0.016	—	0.050
L1	0.041 REF.		
e	0.025 BSC.		
θ°	0	—	8

UNIT : INCH

SEATING PLANE

DETAIL A

GAUGE PLANE
SEATING PLANE

PERICOM®
Enabling Serial Connectivity

	DATE: 04/08/16
DESCRIPTION: 16-Pin, 150mil Wide QSOP	
PACKAGE CODE: Q (Q16)	
DOCUMENT CONTROL #: PD-1201	REVISION: H

NOTES:
1. ALL DIMENSIONS IN INCH. ANGLES IN DEGREES.
2. JEDEC MO−137E
3. DIMENSIONS DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

16-0056

Packaging Mechanical: 16-TSSOP (L)

SYMBOLS	MIN.	NOM.	MAX.
A	–	–	1.20
A1	0.05	–	0.15
A2	0.80	1.00	1.05
b	0.19	–	0.30
c	0.09	–	0.20
D	4.90	5.00	5.10
E1	4.30	4.40	4.50
E	6.20	6.40	6.60
e	0.65 BSC		
L1	1.00 REF		
L	0.45	0.60	0.75
S	0.20	–	–
θ	0°	–	8°

NOTES:
1. ALL DIMENSIONS IN MILLIMETERS. ANGLES IN DEGREES.
2. JEDEC MO−153F
3. DIMENSIONS DOES NOT INCLUDE MOLD FLASH,
   PROTRUSIONS OR GATE BURRS.

16-0061

PERICOM Enabling Serial Connectivity	DATE: 03/24/16
DESCRIPTION: 16-Pin, 173mil Wide TSSOP	
PACKAGE CODE: L (L16)	
DOCUMENT CONTROL #: PD-1310	REVISION: G

Packaging Mechanical: 16-UQFN (ZHD)

Top View

Bottom View

RECOMMENDED LAND PATTERN(unit:mm)

0.70x16

1.60

1.60

16X0.27

2.90

0.50BSC

2.90

PKG. DIMENSIONS(MM)		
SYMBOL	Min	Max
A	0.50	0.65
A1	0.00	0.05
A3	0.15 REF	
D	2.90	3.10
E	2.90	3.10
D1	1.60	1.90
E1	1.60	1.90
b	0.18	0.30
e	0.50 BSC	
L	0.25	0.55

Note:
1. Comply with MO-248E, except 'L' MIN and 'L' 'D1' 'E1' MAX

	DATE: 07/27/16
DESCRIPTION: 16-Pin, UQFN, 3X3	
PACKAGE CODE: ZHD(ZHD16)	
DOCUMENT CONTROL#: PD-2209	**REVISION: --**

16-0092

For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Packaging Code	Package Description
PI5C3257QEX	Q	16-pin, 150-mil Wide (QSOP)
PI5C3257LEX	L	16-pin, 173-mil Wide (TSSOP)
PI5C3257ZHDEX	ZHD	16-pin, 3x3 (UQFN)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. E = Pb-free and Green
5. X suffix = Tape/Reel

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