

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$ Max	I_D Max $T_A = +25^\circ\text{C}$
60V	$3\Omega @ V_{GS} = 5V$	0.3A

Description

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

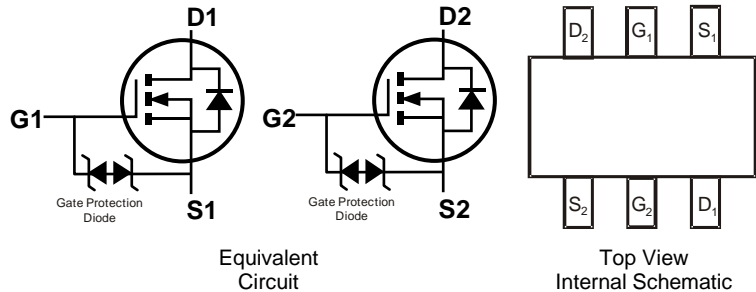
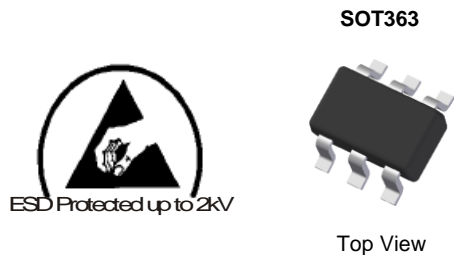
- Motor Control
- Power Management Functions

Features

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **ESD Protected Up To 2kV**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208 (E)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

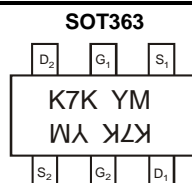


Ordering Information (Note 4)

Part Number	Case	Packaging
DMN601DWK-7	SOT363	3,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



K7K = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: C = 2015)
 M = Month (ex: 9 = September)

Date Code Key

Year	2005	2006	---	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024
Code	S	T	---	C	D	E	F	G	H	I	J	K	L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
Code	1	2	3	4	5	6	7	8	9	O	N	D	

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	60	V
Gate-Source Voltage	V _{GSS}	±20	V
Drain Current (Note 5)	Continuous	305	mA
	Pulsed (Note 6)	800	

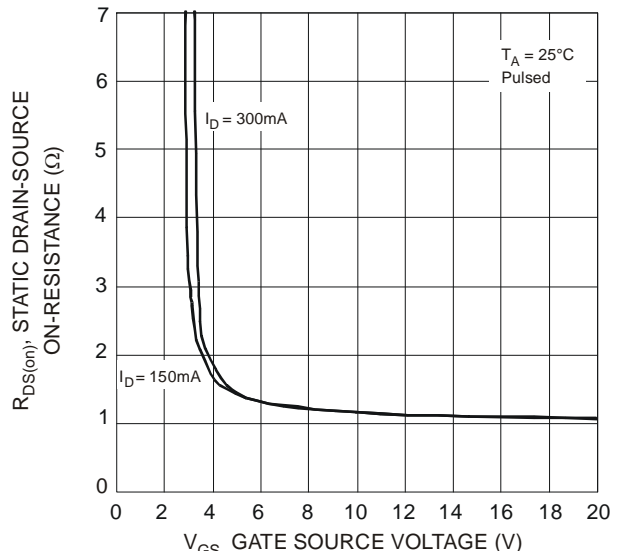
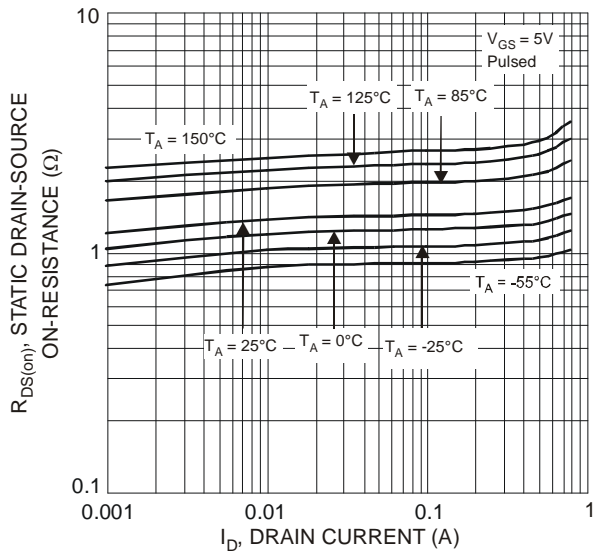
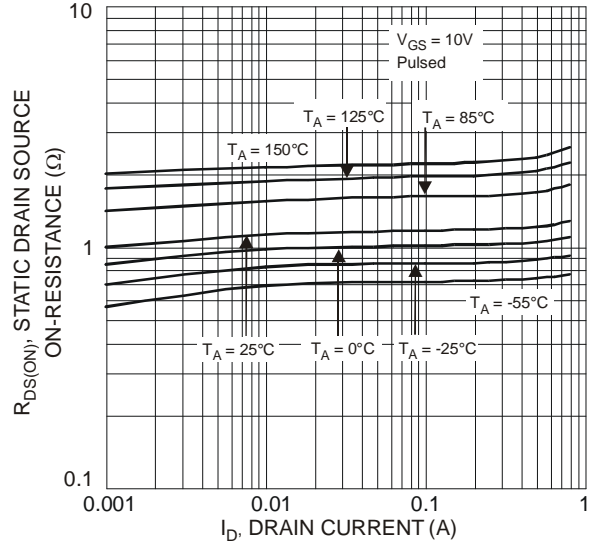
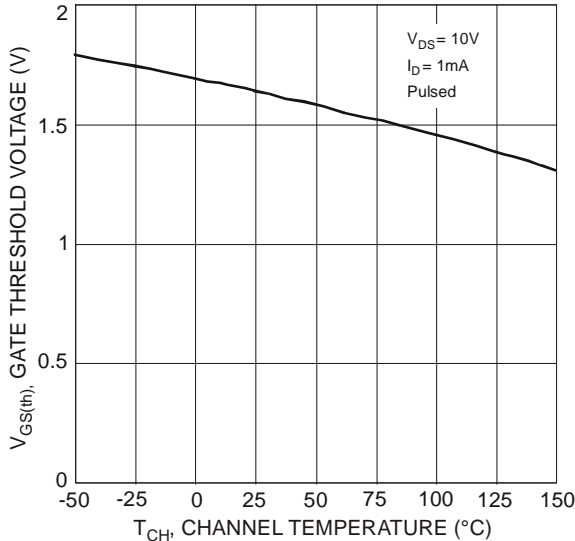
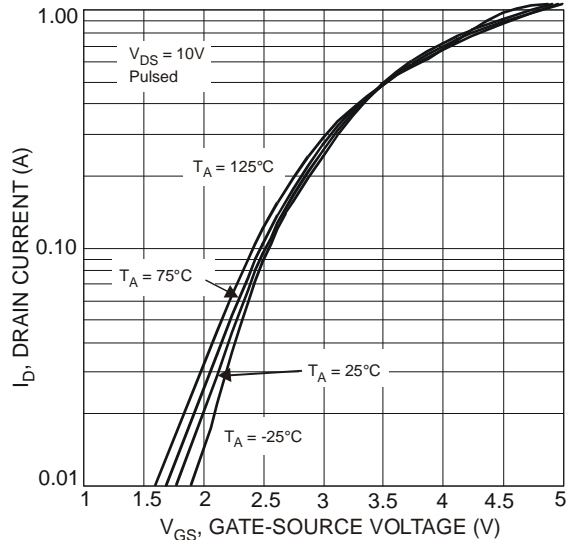
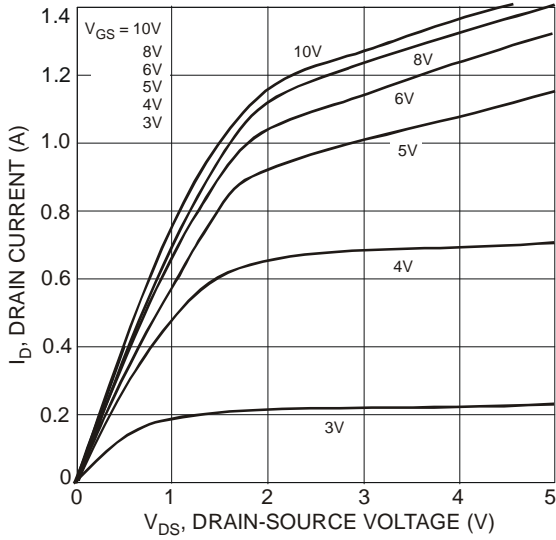
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	200	mW
Thermal Resistance, Junction to Ambient	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	—	V	V _{GS} = 0V, I _D = 10μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	μA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	1.0	1.6	2.5	V	V _{DS} = 10V, I _D = 1mA
Static Drain-Source On-Resistance	R _{DS(on)}	—	—	2.0 3.0	Ω	V _{GS} = 10V, I _D = 0.5A V _{GS} = 5V, I _D = 0.05A
Forward Transfer Admittance	Y _{fs}	80	—	—	ms	V _{DS} = 10V, I _D = 0.2A
Diode Forward Voltage (Note 8)	V _{SD}	0.5	—	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	30	50	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	4.2	25	pF	
Reverse Transfer Capacitance	C _{rss}	—	2.9	5.0	pF	
Gate Resistance	R _g	—	133	—	Ω	f = 1MHz, V _{GS} = 0V, V _{DS} = 0V
Total Gate Charge	Q _g	—	304	—	pC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 250mA
Gate-Source Charge	Q _{gs}	—	203	—	pC	
Gate-Drain Charge	Q _{gd}	—	84	—	pC	
Turn-On Delay Time	t _{D(on)}	—	3.9	—	nS	
Turn-On Rise Time	t _r	—	3.4	—	nS	V _{DD} = 30V, V _{GS} = 10V, R _G = 25Ω, I _D = 200mA
Turn-Off Delay Time	t _{D(off)}	—	15.7	—	nS	
Turn-Off Fall Time	t _f	—	9.9	—	nS	

- Notes:
5. Device mounted on FR-4 PCB.
 6. Pulse width ≤10μS, duty cycle ≤1%.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.



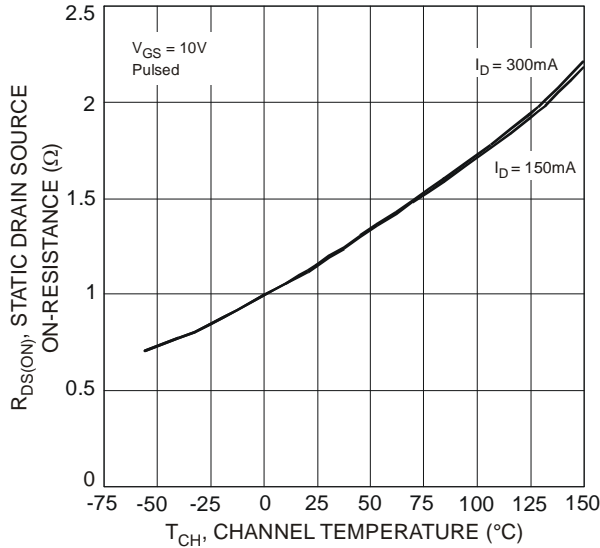


Figure 7 Static Drain-Source On-State Resistance vs. Channel Temperature

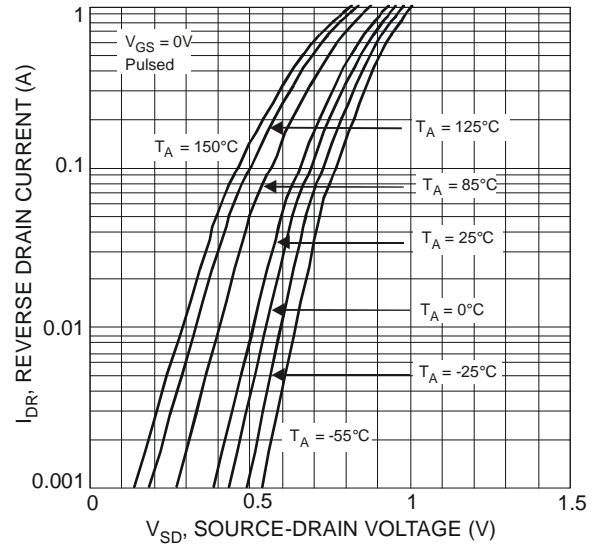


Figure 8 Reverse Drain Current vs. Source-Drain Voltage

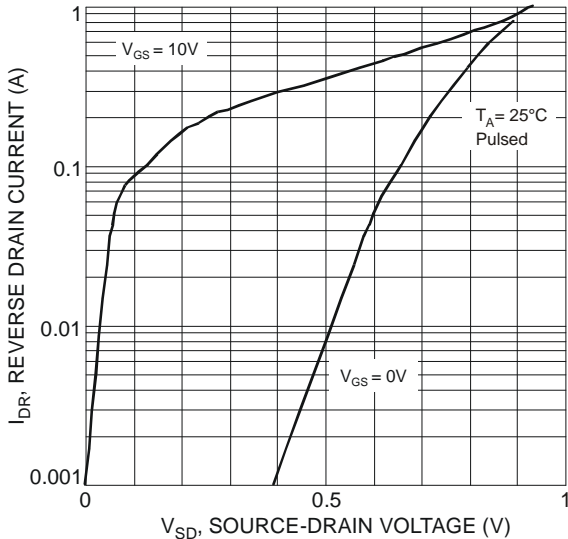


Figure 9 Reverse Drain Current vs. Source-Drain Voltage

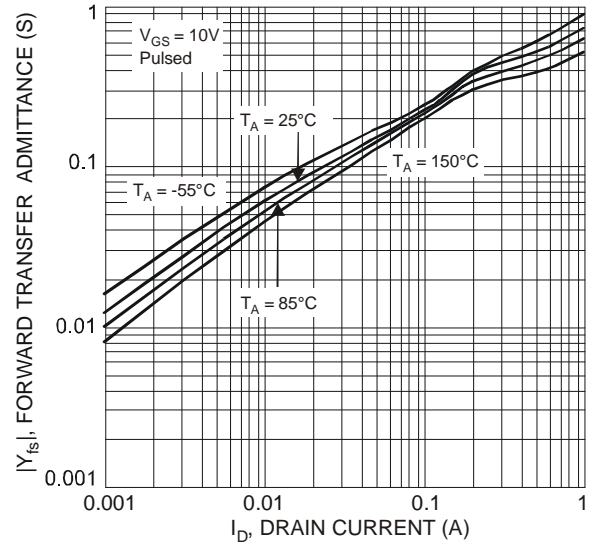


Figure 10 Forward Transfer Admittance vs. Drain Current

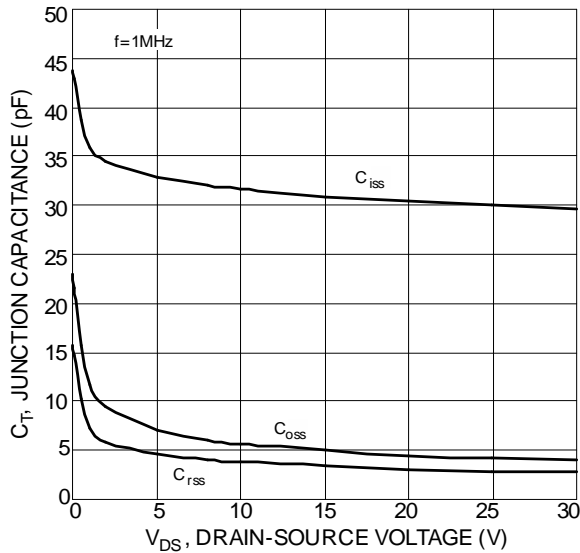


Figure 11 Typical Junction Capacitance

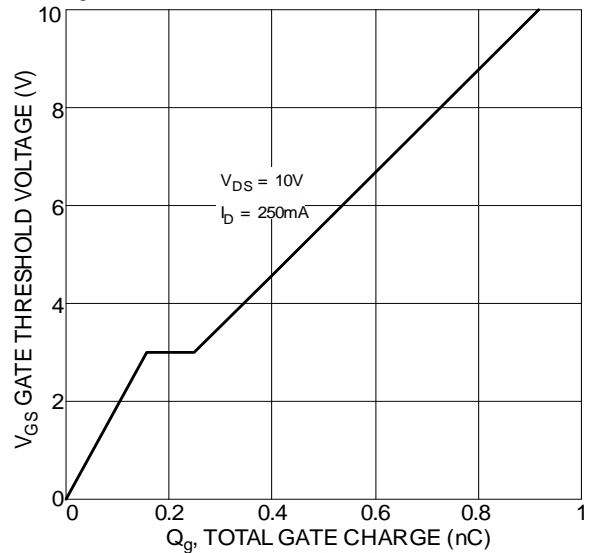


Figure 12 Gate Charge

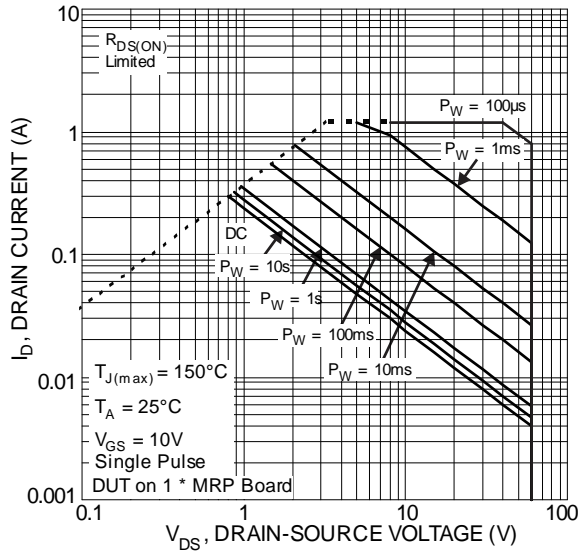


Figure 13 SOA, Safe Operation Area

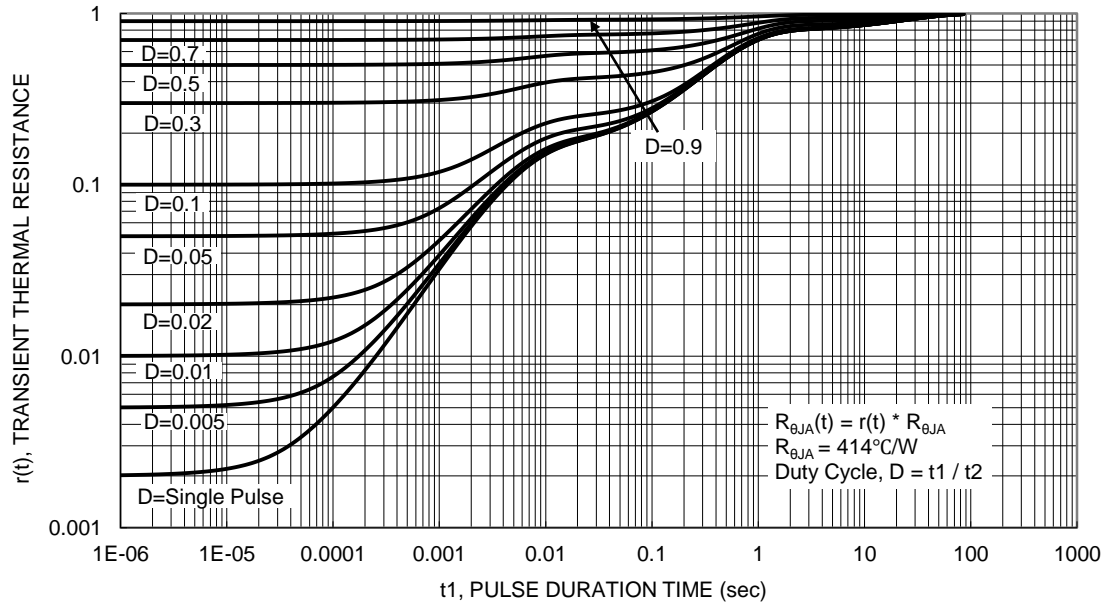
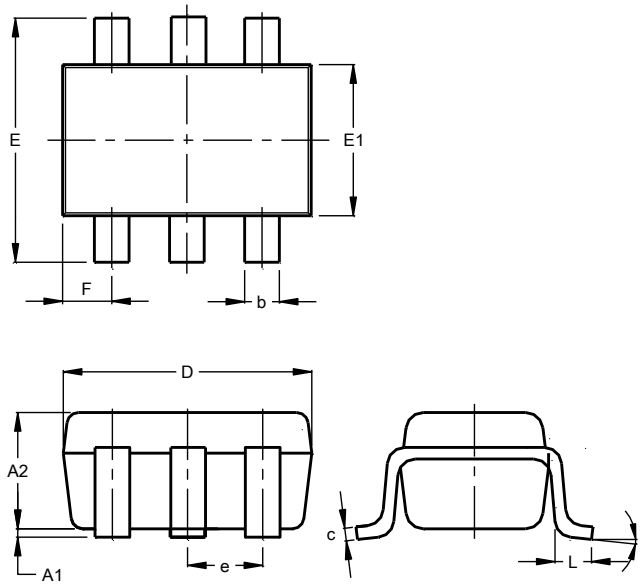


Figure 14 Transient Thermal Resistance

Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

SOT363

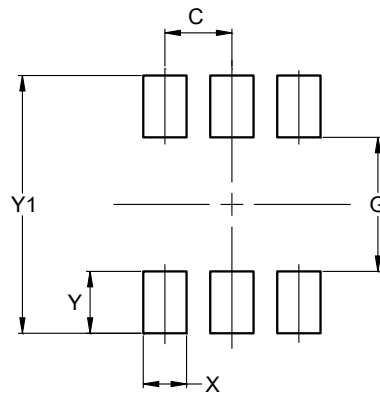


SOT363			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	1.00
b	0.10	0.30	0.25
c	0.10	0.22	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
F	0.40	0.45	0.425
L	0.25	0.40	0.30
a	8°		
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

SOT363



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500

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