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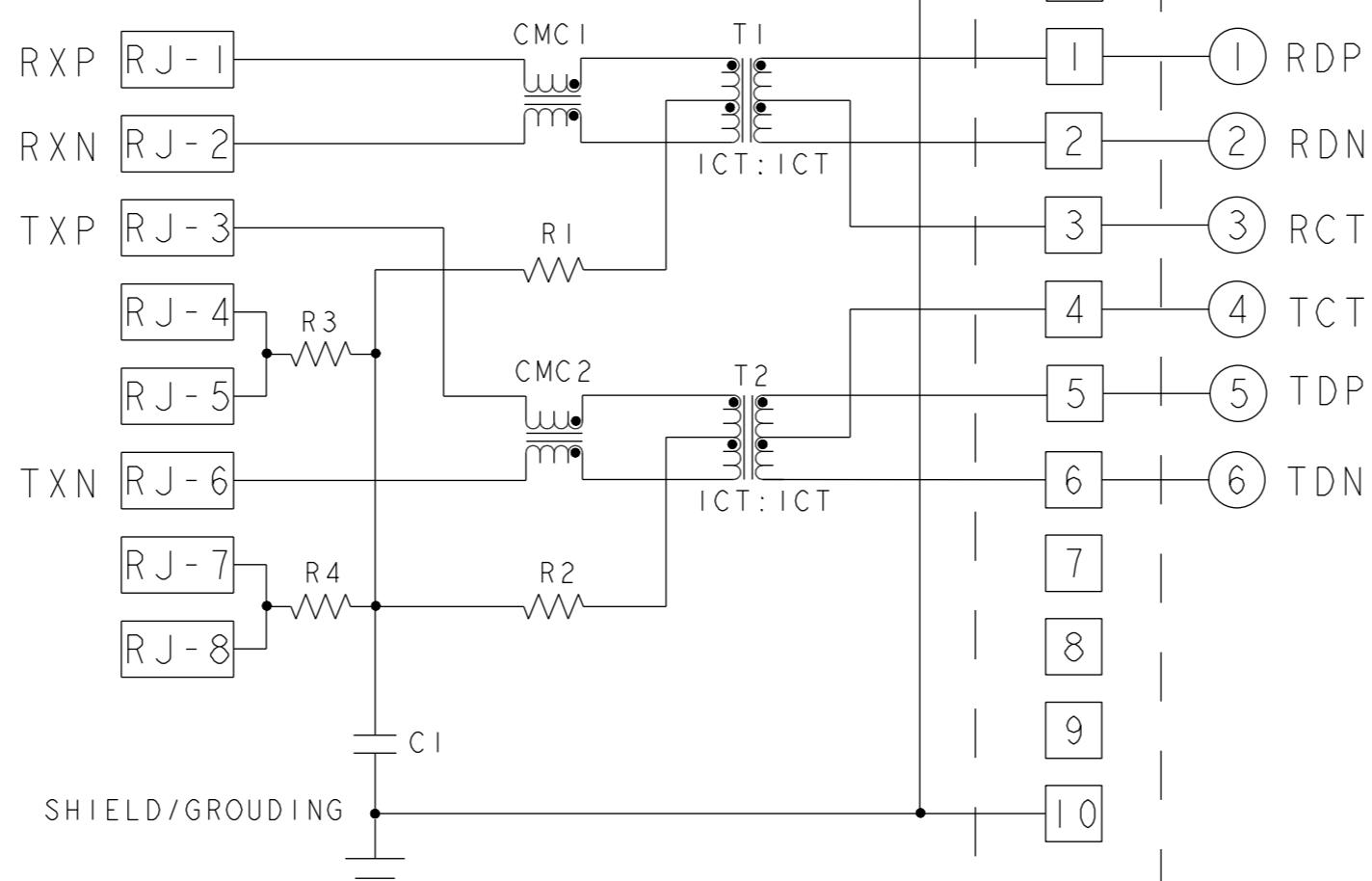
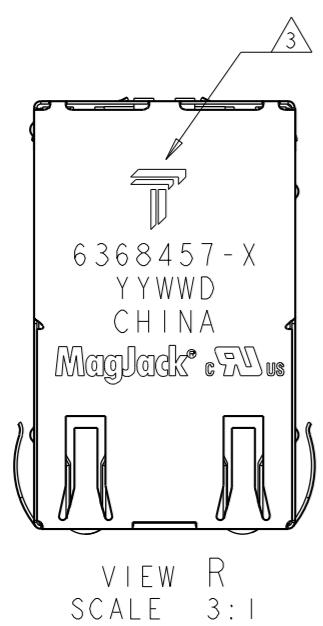
PRODUCT MAY BE PROTECTED BY ONE OR MORE OF THE FOLLOWING US PATENTS:
5736910 5939955 6425781 6428361 6554638 6840817 7123117
7429195 7717749 7808751 6217391 6149050 7924130

REVISIONS	
P	LTR
F	EC-1411035 COMPANY LOGO CHANGE

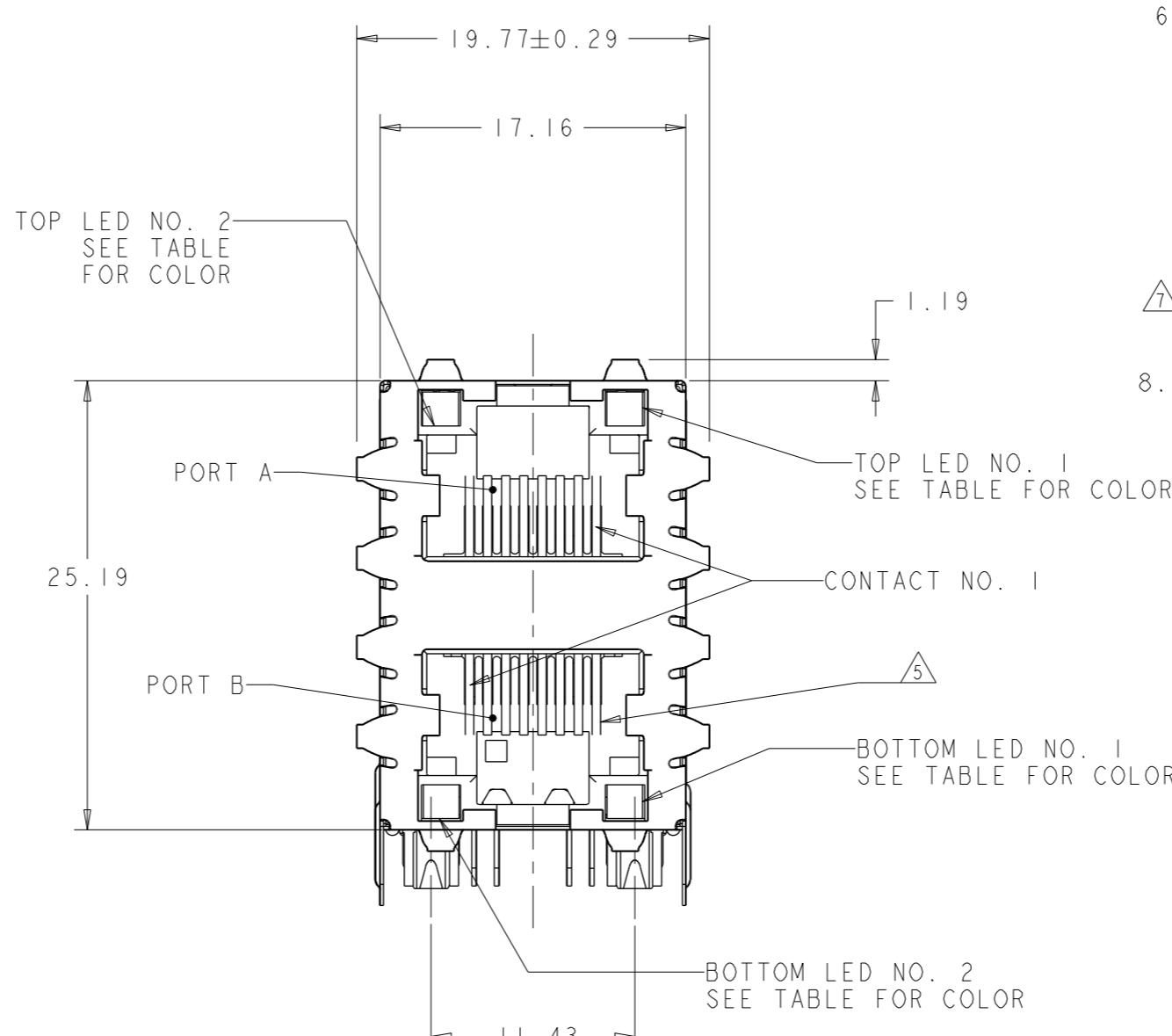
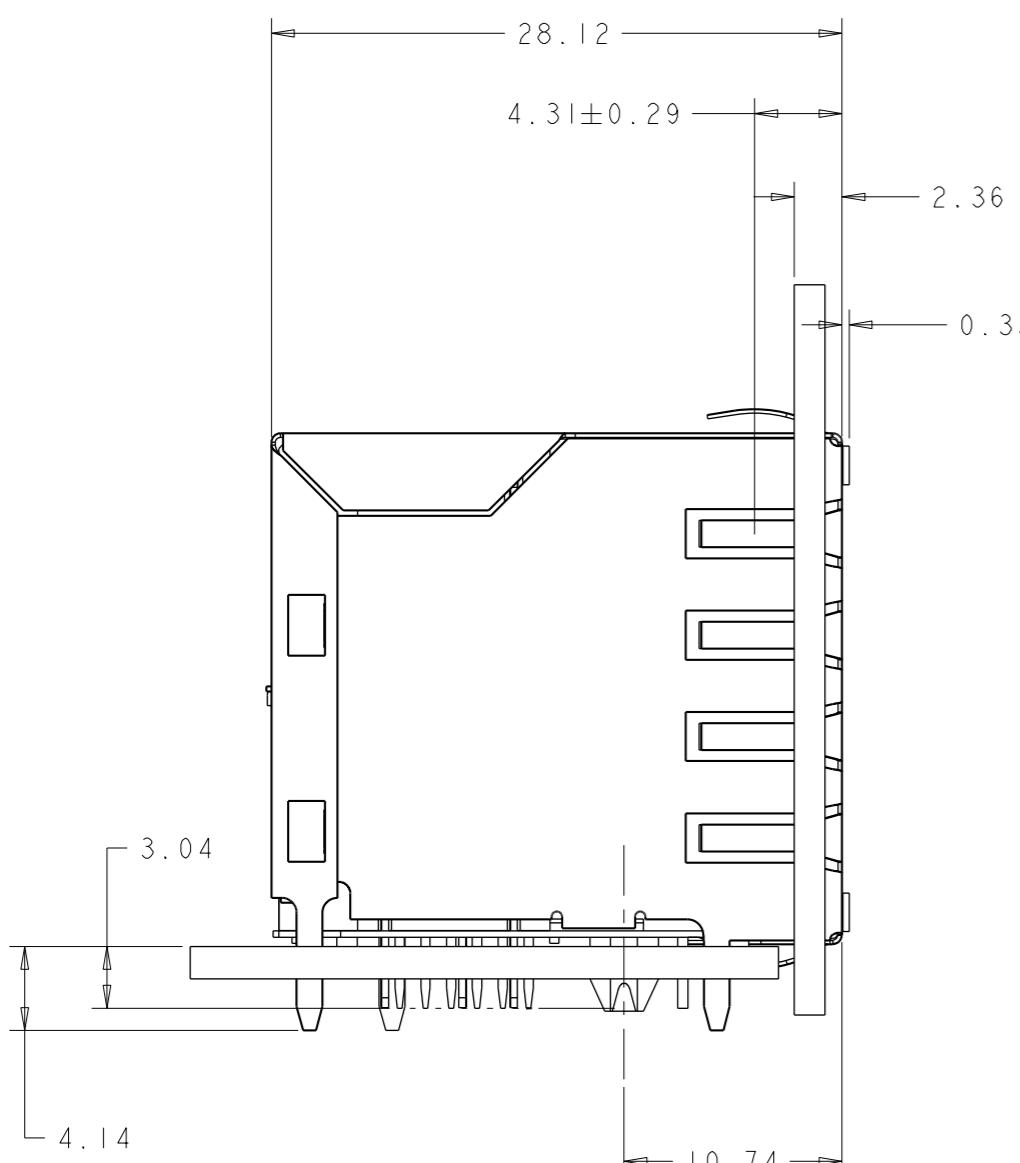
DATE 19JAN2015
DWN APVD
RL TY

S866P9 MAGNETIC CIRCUIT STACKED

TOP AND BOTTOM PORTS



RI - R4 = 75 Ohms, 1/16W, 5% RESISTORS
CI = 1000pF, 2KV DECOUPLING CAP



1. MATERIALS:
PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, PLATED WITH 0.76 μ m SEMI-BRIGHT NICKEL, POST DIPPED WITH 2.54 μ m MIN SAC SOLDER ON SOLDER TAILS
CONTACTS: PHOSPHER BRONZE, 1.27 μ m MIN OVERALL NICKEL UNDER PLATE WITH SELECT 1.27 μ m MIN GOLD AT MATING INTERFACE AND 2.54 μ m MIN MATTE TIN ON SOLDER TAILS.
LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME LEADS PREPLATED WITH 2.03 μ m MIN SILVER OVER 1.02 μ m MIN NICKEL UNDERPLATE OVER 1.02 μ m MIN COPPER UNDERPLATE. POST-PLATED WITH 2.54 μ m MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP

2. MAGNETICS
APPLICATION: 10/100 BASE-T
IMPEDANCE: 100 OHMS
URNS RATIO (CHIP:CABLE): TX = 1:1, RX = 1:1
OPEN CIRCUIT INDUCTANCE (OCL): 350 μ H MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0°C TO 70°C, TX AND RX PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 30MHz
18-20LOG(f/30)dB MIN FROM 30.1MHz TO 60MHz
12dB MIN FROM 60.1MHz TO 80MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA 23.5.1.1, ITEM B.

3. TRP LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN, AGENCY APPROVAL MARKING LOGO LOCATED IN THE APPROXIMATE AREA SHOWN. DATE CODE YY IS YEAR, WW IS WORK WEEK, D IS DAY OF WEEK, WITH SUNDAY=1

4. OPERATING TEMPERATURE: FROM 0° C TO + 70° C.

5. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

6. LED'S COMPLIANT WITH IEC60825-1 SAFETY OF LASER PRODUCTS WHEN OPERATED AT CURRENT OF 20 mA MAX.
LED IS DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA.
LED COLOR:

DOMINANT WAVELENGTH (λ D): GREEN 568 nm TYP @ IF=20mA
FORWARD VOLTAGE (VF): GREEN 2.2V TYP @ IF=20mA

DOMINANT WAVELENGTH (λ D): YELLOW 588 nm TYP @ IF=20mA
FORWARD VOLTAGE (VF): YELLOW 2.1V TYP @ IF=20mA

7. INDICATED CONNECTIONS ARE FOR HUB CONFIGURATION.
THE MAGNETICS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

8. THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK SOLDERING TEMPERATURE IS 260°C MAX, 10 SECONDS MAX.

GRN/YEL	GRN/YEL	GRN/YEL	GRN/YEL	6368457-4
GREEN	YELLOW	GREEN	YELLOW	6368457-3
OBsolete	YELLOW	GREEN	GREEN	6368457-2
GREEN	GREEN	GREEN	GREEN	6368457-1
BOTTOM LED NO. 2	BOTTOM LED NO. 1	TOP LED NO. 1	TOP LED NO. 2	PART NO.

THIS DRAWING IS A CONTROLLED DOCUMENT.		DRW C GILLETTE 30DEC2005	CHK C P D'AMATO 30DEC2005	DONGGUAN CHINA	
DIMENSIONS:	mm	TOLERANCES UNLESS OTHERWISE SPECIFIED:		APR S FLICKINGER 30DEC2005	
0 PLC	± 0.25	1 PLC	± 0.25	MODEL NAME	
1 PLC	± 0.25	2 PLC	± 0.25	MAGJACK	
3 PLC	± 0.25	4 PLC	± 0.25	STACK NON-PoE	
PRODUCT SPEC	108-2100	APPLICATION SPEC		SIZE A1	CAGE CODE C-6368457
				DRAWING NO.	RESTRICTED TO
				CUSTOMER DRAWING	
				SCALE NTS	SHEET 1 OF 3 REV F

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17.63
CONNECTOR ENVELO

SEE DETAIL

RECOMMENDED PC BOARD LAYOUT
VIEWED FROM COMPONENT SIDE

15.49

11.78

5.43
2 PLC

4.7
4 PLC

4.57
2 PLC

2.89

2.54
2 PLC

2.79
2 PLC

1.27
2 PLC

1.27
18 PLC

2.54

11.43

11.43

Ø 1.57 ± 0.08
4 PLC

Ø 3.25^{+0.08}
-0.05

- A -

Ø 1.02 ± 0.08
28 PLC

Ø 3.25^{+0.08}
-0.05

- B -

1.25
TYP

5.08 MAX

25.72
CONNECTOR ENVELOPE

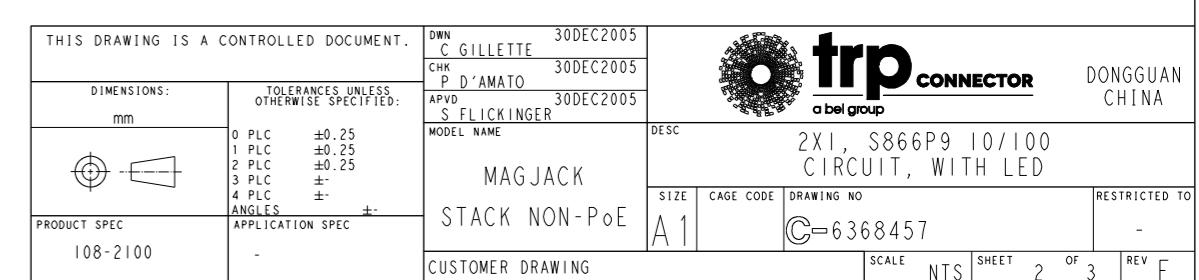
7.62

RECOMMENDED PC BOARD LAYOUT
VIEWED FROM COMPONENT SIDE

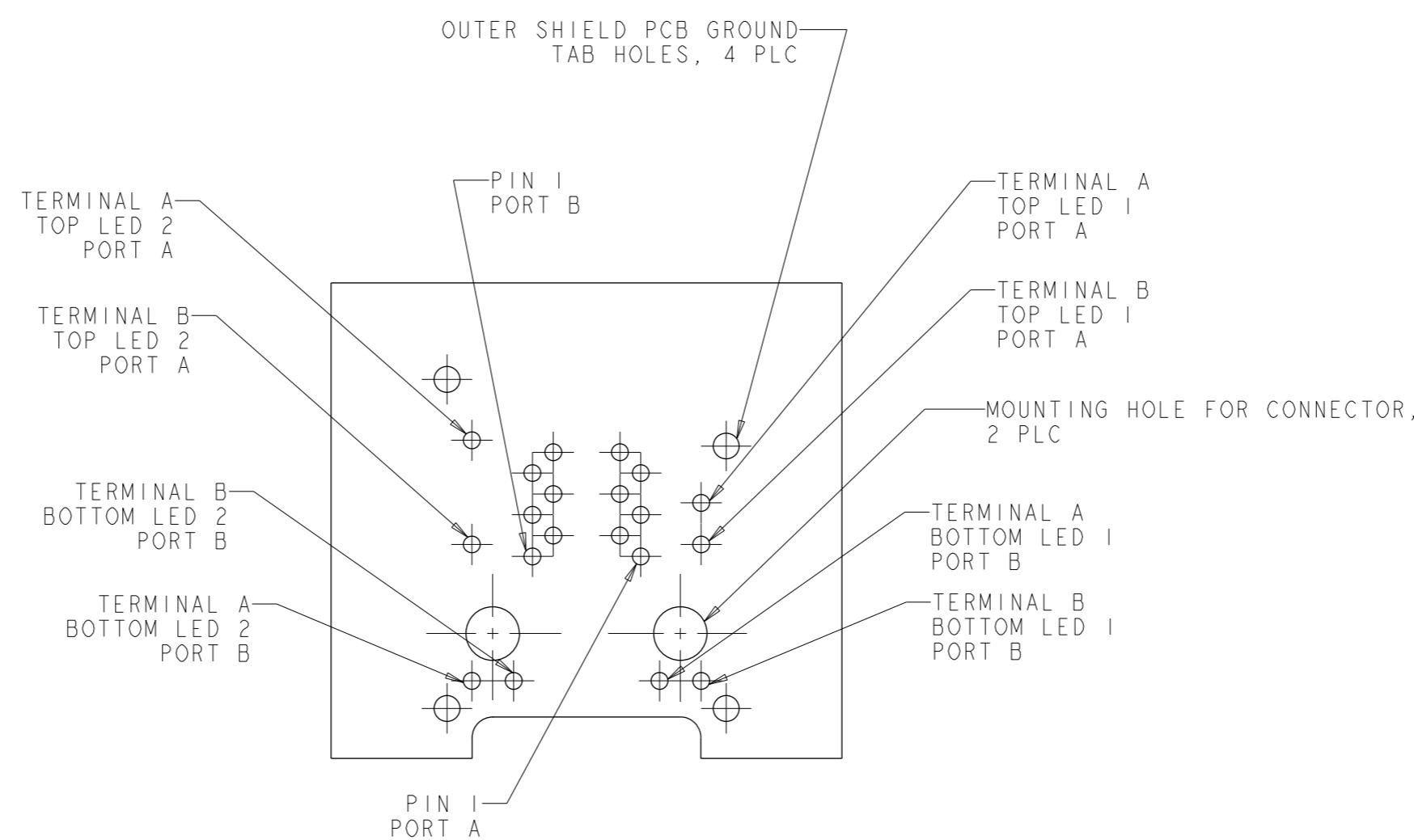
PIN #0 & PIN #10 ON MOD JACK ARE STUB PINS WITH

DETAIL A
SCALE 8:

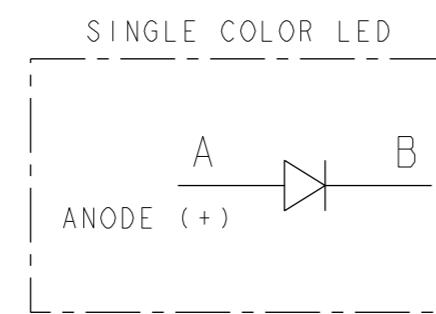
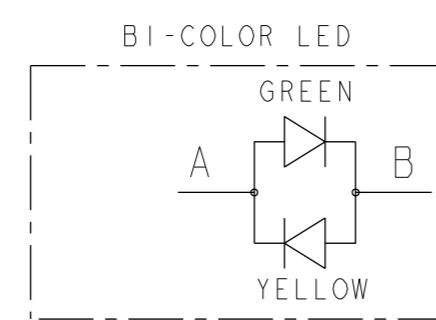
PIN #0 & PIN #10 ON MOD JACK ARE STUB PINS WITH LENGTH SIMILAR TO PCB STANDOFF HEIGHT. PRECAUTIONS SHOULD BE TAKEN IN PCB DESIGN TO GUARD AGAINST SHORTING TRACES TO STUB PINS.



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LED HOLE DESIGNATIONS VIEWED FROM COMPONENT SIDE
SCALE 4:1



THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN C GILLETTE 30DEC2005	CHK P D'AMATO 30DEC2005	APVD S FLICKINGER 30DEC2005	 trp CONNECTOR a bel group	DONGGUAN CHINA				
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:								
		0 PLC ± 0.25	1 PLC ± 0.25	2 PLC ± 0.25	3 PLC \pm	4 PLC \pm	ANGLES \pm			
PRODUCT SPEC 108-2100		APPLICATION SPEC -		MODEL NAME MAGJACK STACK NON-PoE		DESC 2XI, S866P9 10/100 CIRCUIT, WITH LED	SIZE A1	CAGE CODE -	DRAWING NO C-6368457	RESTRICTED TO -
CUSTOMER DRAWING				SCALE NTS	SHEET 3 OF 3	REV F				

Mouser Electronics

Authorized Distributor

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Bel:

6368457-3