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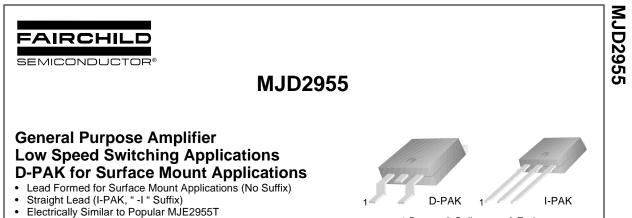


## **ON Semiconductor**®

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1.Base 2.Collector 3.Emitter

- DC Current Gain Specified to 10A
- High Current Gain Bandwidth Product:
- $f_{\rm T} = 2$ MHz (MIN),  $I_{\rm C} = -500$ mA

### PNP Epitaxial Silicon Transistor

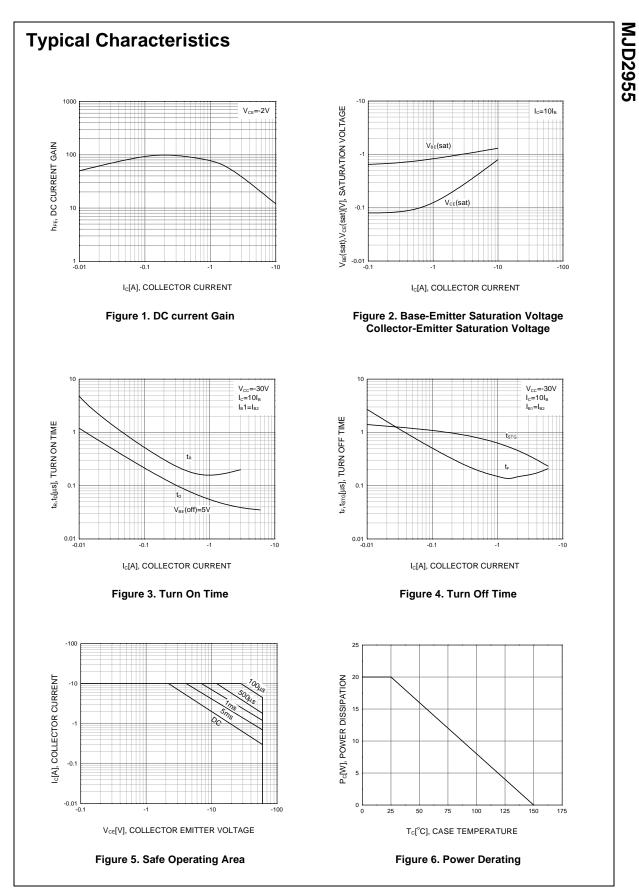
#### Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	- 70	V
V <sub>CEO</sub>	Collector-Emitter Voltage	- 60	V
V <sub>EBO</sub>	Emitter-Base Voltage	- 5	V
I <sub>C</sub>	Collector Current	- 10	А
I <sub>B</sub>	Base Current	- 6	А
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	20	W
	Collector Dissipation (T <sub>a</sub> =25°C)	1.75	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

#### Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

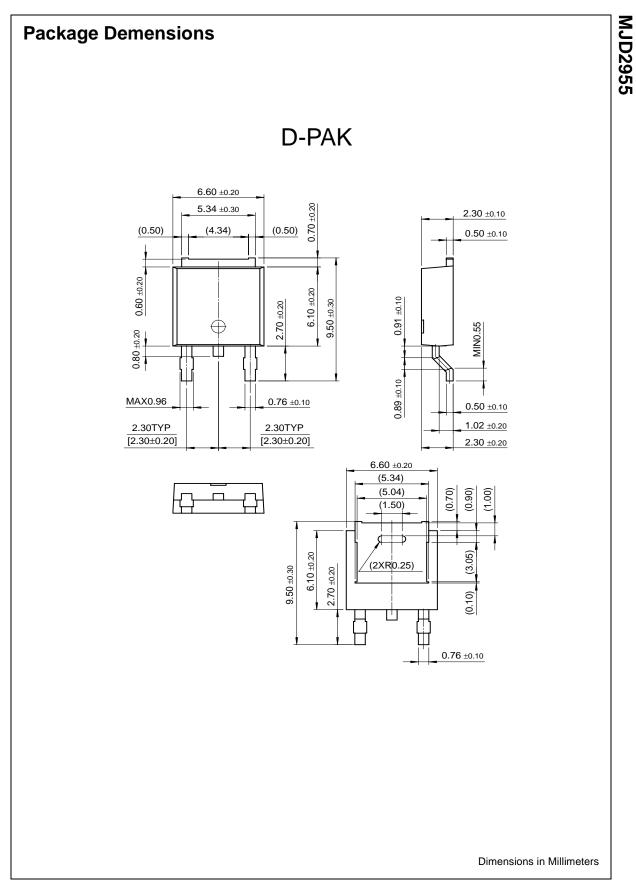
Symbol	Parameter	Test Condition	Min.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining Voltage	I <sub>C</sub> = - 30mA, I <sub>B</sub> = 0	-60		V
I <sub>CEO</sub>	Collector Cut-off Current	$V_{CE} = -30V, I_{E} = 0$		- 50	μΑ
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = -70V, I_E = 0$		- 2	mA
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		- 0.5	mA
h <sub>FE</sub>	* DC Current Gain	$V_{CE} = -4V, I_{C} = -4A$	20	100	
		$V_{CE} = -4V, I_{C} = -10A$	5		
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	$I_{\rm C} = -4A, I_{\rm B} = -0.4A$		- 1.1	V
		I <sub>C</sub> = - 10A, I <sub>B</sub> = - 3.3A		- 8	V
V <sub>BE</sub> (on)	* Base-Emitter ON Voltage	V <sub>CE</sub> = - 4V, I <sub>C</sub> = - 4A		-1.8	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = - 10V, I <sub>C</sub> = - 500mA	2		MHz

\* Pulse Test: PW≤300ms, Duty Cycle≤2%



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