



**PDS3200** 

#### 3A HIGH VOLTAGE SCHOTTKY BARRIER RECTIFIER PowerDI5

#### **Features**

- Guard Ring Die Construction for Transient Protection
- Low Forward Voltage Drop
- Very Low Leakage Current
- Highly Stable Oxide Passivated Junction
- High Forward Surge Current Capability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### **Mechanical Data**

- Case: PowerDI<sup>®</sup>5
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208(63)
- Polarity: See Diagram
- Weight: 0.096 grams (Approximate)

#### PowerDI5





LEFT PIN o BOTTOMSIDE RIGHT PIN o

Note: Pins Left & Right must be electrically connected at the printed circuit board.

### Ordering Information (Note 5)

Part Number	Qualification	Case	Packaging
PDS3200-13	Commercial	PowerDI5	5,000/Tape & Reel
PDS3200Q-13	Automotive	PowerDI5	5,000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**

PowerDI5



S3200 = Product Type Marking Code );; = Manufacturer's Code Marking YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 18 for 2018) WW = Week Code (01 to 53) K = Factory Designator



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>R</sub> WM	200	V	
RMS Reverse Voltage	V <sub>R(RMS)</sub>	141	V	
Average Rectified Output Current	Io	3	Α	
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	180	А	

### **Thermal Characteristics**

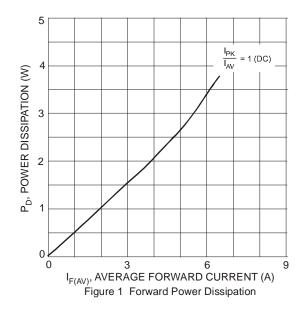
Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point	Reus	_	2.0	°C/W
Thermal Resistance Junction to Ambient Air (Note 6) T <sub>A</sub> = +25°C	$R_{ heta JA}$	75	_	°C/W
Thermal Resistance Junction to Ambient Air (Note 7) T <sub>A</sub> = +25°C	$R_{\theta JA}$	60	_	°C/W
Thermal Resistance Junction to Ambient Air (Note 8) T <sub>A</sub> = +25°C	$R_{ heta JA}$	45		°C/W
Operating Temperature Range	TJ	-65 to	+150	ů
Storage Temperature Range	T <sub>STG</sub>	-65 to	+175	°C

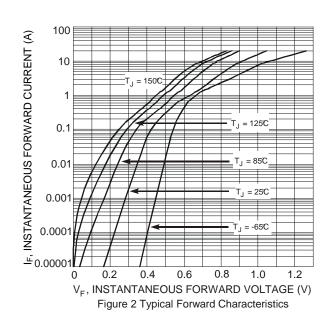
### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 9)	V <sub>(BR)R</sub>	200			V	$I_R = 10\mu A$
	VF	_	0.75	0.78	V	$I_F = 3A, T_S = +25^{\circ}C$
Forward Voltage		_	0.59	0.64		$I_F = 3A, T_S = +125$ °C
l olward voltage		_	0.82	0.88		$I_F = 6A, T_S = +25^{\circ}C$
		_	0.66	0.71		$I_F = 6A, T_S = +125^{\circ}C$
Reverse Leakage Current (Note 9)	-	_	1	10	μΑ	$T_S = +25^{\circ}C, V_R = 200V$
Neverse Leakage Guiterii (Note 9)	IR	_	8.0	4.5	mA	$T_S = +125$ °C, $V_R = 200$ V

Notes:

- 6. FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
- 7. Polymide PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
- 8. Polymide PCB, 2oz. Copper. Cathode pad dimensions 9.4mm x 7.2mm. Anode pad dimensions 2.7mm x 1.6mm.
- 9. Short duration pulse test used to minimize self-heating effect.

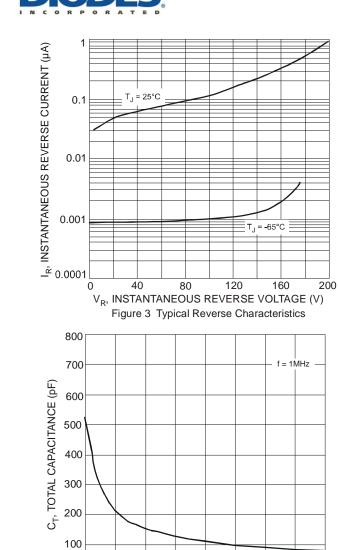


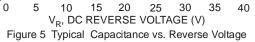


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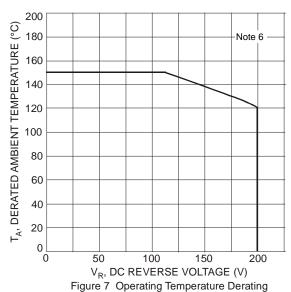


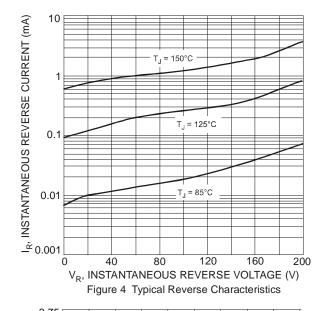


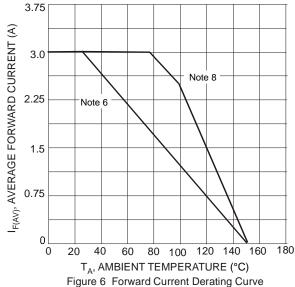
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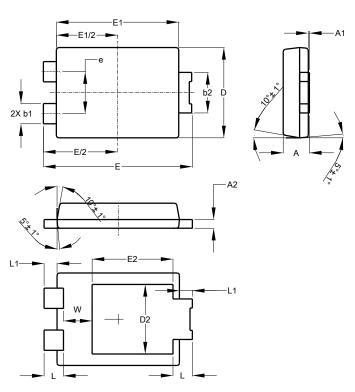
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# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI5

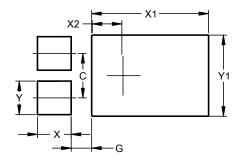


PowerDI5					
Dim	Min	Max	Тур		
Α	1.05	1.15	1.10		
A1	0.00	0.05			
A2	0.33	0.43	0.381		
b1	0.80	0.99	0.89		
b2	1.70	1.88	1.78		
D	3.90	4.05	3.966		
D2			3.054		
Е	6.40	6.60	6.51		
e			1.84		
E1	5.30	5.45	5.37		
E2			3.549		
L	0.75	0.95	0.85		
L1	0.50	0.65	0.57		
W	1.10	1.41	1.255		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI5



Dimensions	Value (in mm)
С	1.840
G	0.852
Х	1.400
X1	4.860
X2	1.310
Υ	1.390
V1	3 360



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