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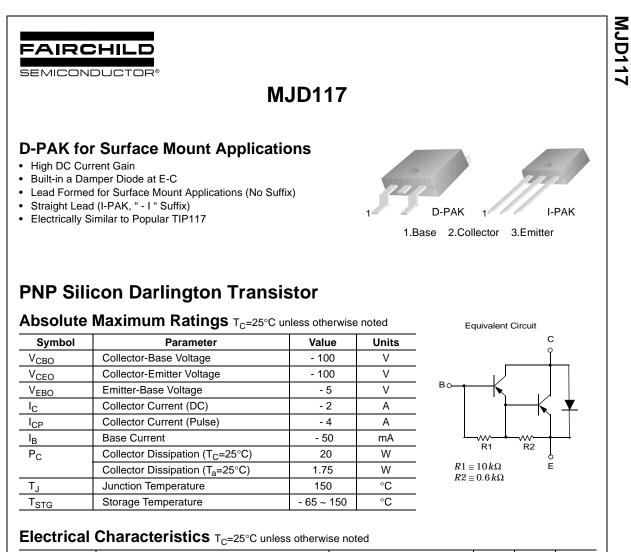


ON Semiconductor®

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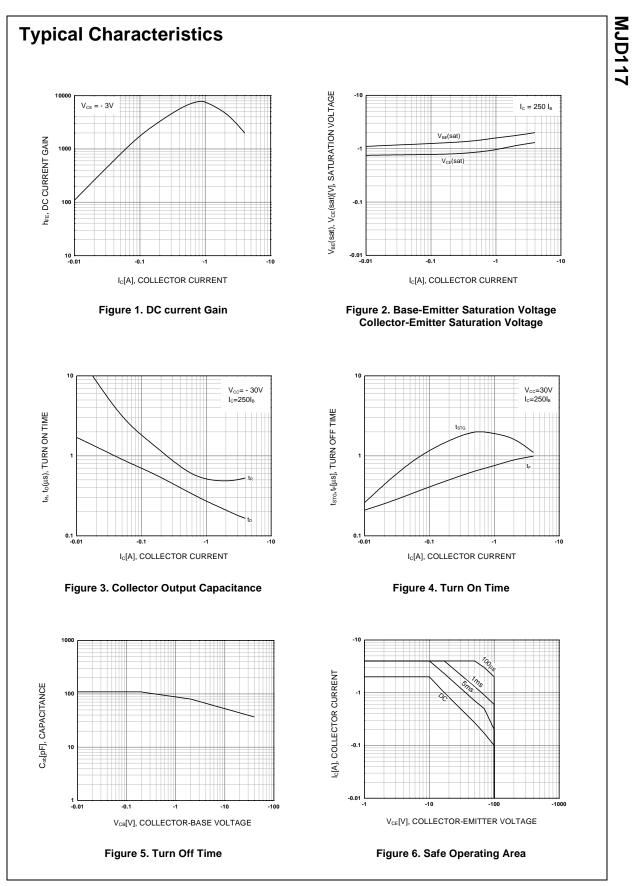
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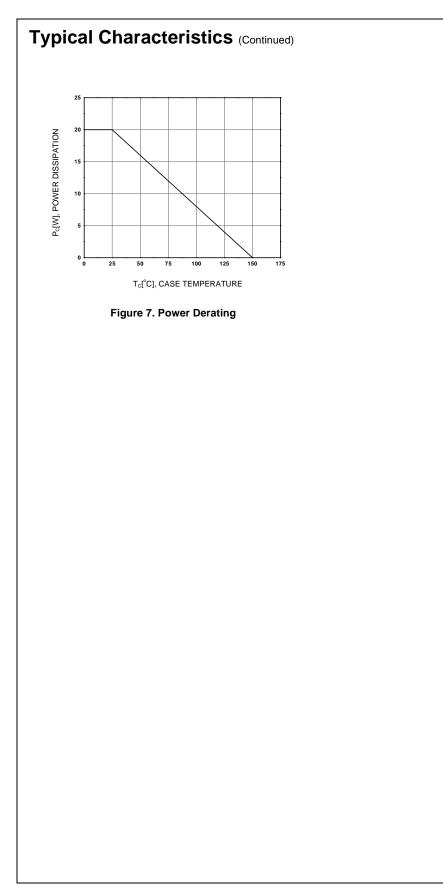
Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	*Collector-Emitter Sustaining Voltage	I _C = - 30mA, I _B = 0	- 100		V
I _{CEO}	Collector Cut-off Current	$V_{CE} = -50V, I_B = 0$		- 20	μΑ
I _{CBO}	Collector Cut-off Current	$V_{CB} = -100V, I_E = 0$		- 20	μA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		- 2	mA
h _{FE}	*DC Current Gain	$V_{CE} = -3V, V_{EB} = -0.5A$ $V_{CE} = -3V, V_{EB} = -2A$ $V_{CE} = -3V, I_{C} = -4A$	500 1000 200	12K	
V _{CE} (sat)	*Collector-Emitter Saturation Voltage	$I_{C} = -2A, I_{B} = -8mA$ $I_{C} = -4A, I_{B} = -40mA$		- 2 - 3	V V
V _{BE} (sat)	*Base-Emitter Saturation Voltage	I _C = - 4A, I _B = - 40mA		- 4	V
V _{BE} (on)	*Base-Emitter ON Voltage	V _{CE} = - 3A, I _C = - 2A		- 2.8	V
f _T	Current Gain Bandwidth Product	V _{CE} = -10V, I _C = - 0.75A	25		MHz
C _{ob}	Output Capacitance	V _{CB} = - 10V, I _E = 0 f= 0.1MHz		200	pF

* Pulse Test: PW≤300µs, Duty Cycle≤2%

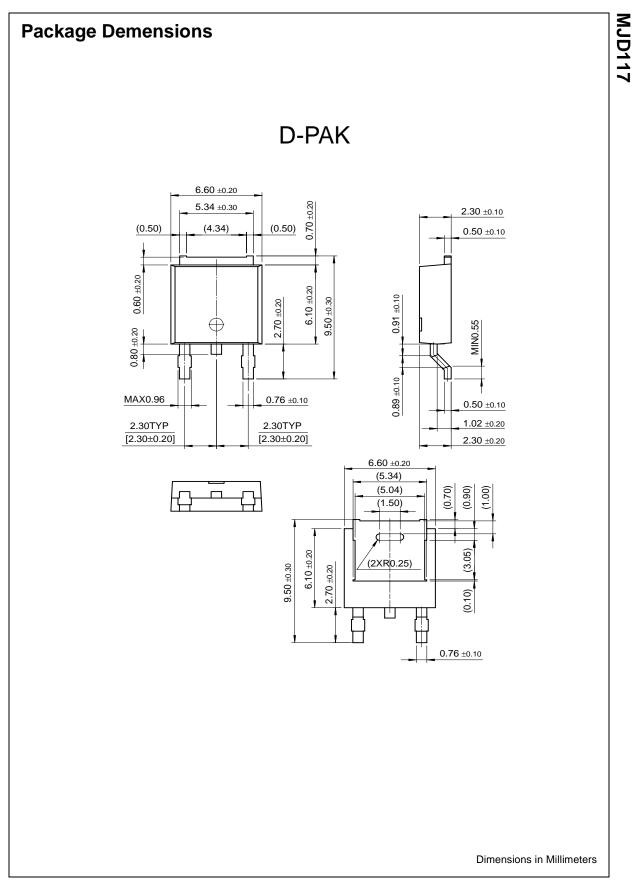


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