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KA78XXE / KA78XXAE 3-Terminal 1 A Positive Voltage Regulator

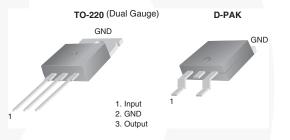
Features

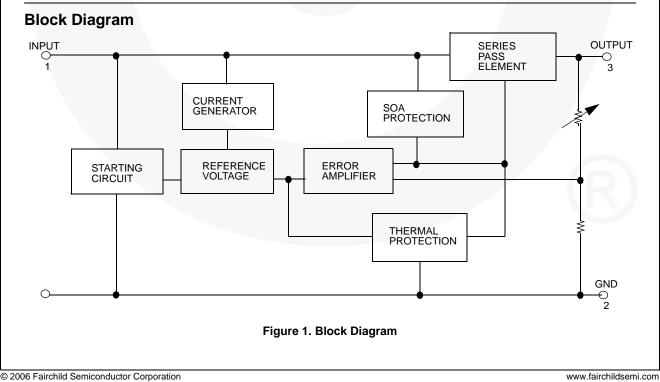
KA78XXF / KA78XXAF Rev 1.9

- Output Current up to 1 A
- Output Voltages of 5, 6, 8, 9, 10, 12, 15, 18, 24 V
- Thermal Overload Protection
- Short-Circuit Protection
- Output Transistor Safe Operating Area Protection

Description

The KA78XXE / KA78XXAE series of three-terminal positive regulators is available in the TO-220 / D-PAK package with several fixed-output voltages, making them useful in a wide range of applications. Each type employs internal current limiting, thermal shut-down, and safe operating area. If adequate heat sinking is provided, they can deliver over 1 A output current. Although designed primarily as fixed-voltage regulators, these devices can be used with external components for adjustable voltages and currents.





KA78XXE / KA78XXAE — 3-Terminal 1 A Positive Voltage Regulator

Product Number	Output Voltage Tolerance ⁽¹⁾	Package	Operating Temperature	Parking Method
KA7805ETU				
KA7806ETU				
KA7808ETU				
KA7809ETU				
KA7810ETU		TO-220 (Dual Gauge)		Rail
KA7812ETU				
KA7815ETU	±4%		-40°C to +125°C	
KA7818ETU	<u>±4</u> /0		-40 C t0 +125 C	
KA7824ETU				
KA7805ERTF				
KA7805ERTM				
KA7808ERTM		D-PAK ⁽²⁾		Tape and Reel
KA7809ERTM				
KA7812ERTM				
KA7805AETU				
KA7809AETU				
KA7810AETU	±2%	TO-220 (Dual Gauge)	0°C to +125°C	Rail
KA7812AETU	<u></u> Ξ∠ /0	10-220 (Duai Gauge)	0 0 10 +125 0	Ndii
KA7815AETU				
KA7824AETU				

Notes:

- 1. Above output voltage tolerance is available at 25°C.
- 2. Refer to below figure for TM / TF Suffix for DPAK.



D-PAK Unit Orientation

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Paramete	er	Value	Unit
VI	Input Voltage	V _O = 5 V to 18 V	35	V
vI	input voltage	V _O = 24 V	40	V
$R_{ extsf{ heta}JC}$	Thermal Resistance Junction-Case (T	5	°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance Junction-Air (TO-	-220)	65	°C/W
т	Operating Temperature Banga	KA78XXE / KA78XXER	-40 to +125	<u></u>
T _{OPR}	Operating Temperature Range	KA78XXAE	0 to +125	
T _{STG}	Storage Temperature Range		-65 to +150	°C

Electrical Characteristics (KA7805E / KA7805ER)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I =10 V, C_I= 0.33 μ F, C_O=0.1 μ F, unless otherwise specified.

Symbol	Parameter	C	Conditions	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		4.80	5.00	5.20	
V _O	Output Voltage	5.0 mA I _O V _I = 7 V to 20	1.0 A, P _O 15 W, 0 V	4.75	5.00	5.25	V
Poglino	Line Regulation ⁽³⁾	T - 125°C	$V_1 = 7 V \text{ to } 25 V$		4.0	100.0	mV
Regline		$T_{\rm J} = +25$ C	$V_{I} = 8 V \text{ to } 12 V$		1.6	50.0	
Doglood	Load Regulation ⁽³⁾	T _J = +25°C -	$I_0 = 5.0 \text{ mA to } 1.5 \text{ A}$		9	100	mV
Regload	Load Regulation (*)	$I_{\rm J} = +25^{\circ}{\rm C}$	$I_{O} = 250 \text{ mA to } 750 \text{ mA}$		4	50	IIIV
Ι _Q	Quiescent Current	T _J = +25°C			5	8	mA
41	Quipagent Current Change	$I_{O} = 5 \text{ mA to } 1.0 \text{ A}$ V ₁ = 7 V to 25 V			0.03	0.50	mA
ΔI_Q	Quiescent Current Change				0.30	1.30	ma
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽⁴⁾	l _O = 5 mA			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to '	100 kHz, T _A = +25°C		42		μV
RR	Ripple Rejection ⁽⁴⁾	f = 120 Hz, V	/ _I = 8 V to 18 V	62	73		dB
V _{Drop}	Dropout Voltage	l _O = 1 A, T _J =	= +25°C		2		V
R _O	Output Resistance ⁽⁴⁾	f = 1 kHz			15		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	, = +25°C		230		mA
I _{PK}	Peak Current ⁽⁴⁾	$T_J = +25^{\circ}C$			2.2		A

Notes:

3. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7806E)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 11 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		5.75	6.00	6.25	
V _O	Output Voltage	5.0 mA I _C V _I = 8.0 V to		5.70	6.00	6.30	V
Poglino	Line Regulation ⁽⁵⁾	T _{.1} = +25°C	$V_{I} = 8 V \text{ to } 25 V$		5.0	120.0	mV
Regline		$T_{\rm J} = +25 {\rm C}$	$V_{I} = 9 V \text{ to } 13 V$		1.5	60.0	IIIV
Poglood	Load Regulation ⁽⁵⁾	T - 125°C	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		9	120	mV
Regload		T _J = +25°C	$I_0 = 250 \text{ mA to } 750 \text{ mA}$	\	3	60	mv
ا _Q	Quiescent Current	T _J = +25°C			5	8	mA
41	Quiescent Current	I _O = 5 mA to	$I_0 = 5 \text{ mA to 1 A}$			0.5	mA
ΔI_Q	Change	$V_{I} = 8 V \text{ to } 2$	5 V			1.3	11174
$\Delta V_{O} / \Delta T$	Output Voltage Drift ⁽⁶⁾	I _O = 5 mA			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		45		μV
RR	Ripple Rejection ⁽⁶⁾	f = 120 Hz, \	/ _I = 9 V to 19 V	59	75		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J :	= +25°C		2		V
R _O	Output Resistance ⁽⁶⁾	f = 1 kHz			19		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	_A = +25°C		250		mA
I _{PK}	Peak Current ⁽⁶⁾	$T_J = +25^{\circ}C$			2.2		Α

Notes:

5. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7808E / KA7808ER)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 14 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		7.7	8.0	8.3	
V _O	Output Voltage	5.0 mA I _O V _I = 10.5 V t	1.0 A, P _O 15 W, o 23 V	7.6	8.0	8.4	V
Doglino	Line Regulation ⁽⁷⁾	T _{.1} = +25°C	$V_{I} = 10.5 \text{ V} \text{ to } 25 \text{ V}$		5	160	
Regline		$I_{\rm J} = +25$ C	V _I = 11.5 V to 17 V		2	80	mV
Doglood	Load Regulation ⁽⁷⁾		$T_{,1} = +25^{\circ}C$ $I_{O} = 5.0 \text{ mA to } 1.5 \text{ A}$		10	160	m\/
Regload		1j = +25 C	I _O = 250 mA to 750 mA		5	80	mV
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$	$T_J = +25^{\circ}C$		5	8	mA
41	Quiescent Current	I _O = 5 mA to	1.0 A		0.05	0.50	mA
ΔI_Q	Change	V _I = 10.5 A t	o 25 V	1	0.50	1.00	mA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽⁸⁾	l _O = 5 mA			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		52		μV
RR	Ripple Rejection ⁽⁸⁾	f = 120 Hz, \	/ _I = 11.5 V to 21.5 V	56	73		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	= +25°C		2		V
R _O	Output Resistance ⁽⁸⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	_A = +25°C		230		mA
I _{PK}	Peak Current ⁽⁸⁾	T _J = +25°C			2.2		A

Notes:

7. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7809E / KA7809ER)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 15 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		8.65	9.00	9.35	
V _O	Output Voltage	$5.0 \text{ mA} \le I_0 \le V_1 = 11.5 \text{ V to}$	≤ 1.0 A, P _O ≤ 15 W, o 24 V	8.60	9.00	9.40	V
Poglino	Line Regulation ⁽⁹⁾	T _{.1} = +25°C	$V_{I} = 11.5 \text{ V to } 25 \text{ V}$		6	180	mV
Regline		$T_{\rm J} = +25$ C	$V_{I} = 12 \text{ V} \text{ to } 17 \text{ V}$		2	90	
Poglood	Load Regulation ⁽⁹⁾	T - 125°C	$I_{O} = 5 \text{ mA} \text{ to } 1.5 \text{ A}$		12	180	m\/
Regload		T _J = +25°C	I _O = 250 mA to 750 mA		4	90	mV
۱ _Q	Quiescent Current	T _J = +25°C			5	8	mA
A I	Quiescent Current	$I_{O} = 5 \text{ mA to}$	_O = 5 mA to 1.0 A			0.5	س ۸
ΔI_Q	Change	$V_{\rm I} = 11.5 \rm V tc$	o 26 V			1.3	mA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹⁰⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 10	00 kHz, T _A = +25°C		58		μV
RR	Ripple Rejection ⁽¹⁰⁾	f = 120 Hz, V	I = 13 V to 23 V	56	71		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	: +25°C		2		V
R _O	Output Resistance ⁽¹⁰⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	= +25°C		250		mA
I _{PK}	Peak Current ⁽¹⁰⁾	T _J = +25°C			2.2		A

Notes:

9. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7810E)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 16 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		9.6	10.0	10.4	
V _O	Output Voltage	$5.0 \text{ mA} \le I_0 \le V_1 = 12.5 \text{ V to}$	≤ 1.0 A, P _O ≤ 15 W, o 25 V	9.5	10.0	10.5	V
Doglino	Line Regulation ⁽¹¹⁾	T _{.1} = +25°C	$V_{I} = 12.5 \text{ V to } 25 \text{ V}$		10	200	mV
Regline		$T_{\rm J} = +25$ C	$V_{I} = 13 \text{ V} \text{ to } 25 \text{ V}$		3	100	IIIV
Doglood	Load Regulation ⁽¹¹⁾	T - 125°C	$I_0 = 5 \text{ mA}$ to 1.5 A		12	200	mV
Regload		T _J = +25°C	I _O = 250 mA to 750 mA		4	400	mv
Ι _Q	Quiescent Current	T _J = +25°C			5.1	8.0	mA
AL	Quiescent Current	$I_{O} = 5 \text{ mA to}$	_D = 5 mA to 1.0 A			0.5	mA
ΔI_Q	Change	$V_{\rm I} = 12.5 \rm V tc$	o 29 V		1.0		
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹²⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 10	00 kHz, T _A = +25°C		58		μV
RR	Ripple Rejection ⁽¹²⁾	f = 120 Hz, V	r _I = 13 V to 23 V	56	71		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	= +25°C		2		V
R _O	Output Resistance ⁽¹²⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	= +25°C		250		mA
I _{PK}	Peak Current ⁽¹²⁾	T _J = +25°C			2.2		Α

Notes:

11. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7812E / KA7812ER)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 19 V, C_I = 0.33 μ F, C_O= 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		11.5	12.0	12.5	
V _O	Output Voltage	5.0 mA $\leq I_0$ V _I = 14.5 V t	≤ 1.0 A, P _O ≤ 15 W, o 27 V	11.4	12.0	12.6	V
Doglino	Line Regulation ⁽¹³⁾	T _{.1} = +25°C	$V_{I} = 14.5$ V to 30 V		10	240	mV
Regline		$T_{\rm J} = +25$ C	$V_{I} = 16 V$ to 22 V		3	120	
Dealaad	Load Regulation ⁽¹³⁾	T	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		11	240	~\/
Regload	Load Regulation	T _J = +25°C	I _O = 250 mA to 750 mA	\	5	120	mV
Ι _Q	Quiescent Current	T _J = +25°C			5.1	8.0	mA
AL	Quiescent Current	$I_0 = 5 \text{ mA to}$	1.0 A		0.1	0.5	mA
ΔI_Q	Change	V _I = 14.5 V t	o 30 V		0.5	1.0	mA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹⁴⁾	l _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		76		μV
RR	Ripple Rejection ⁽¹⁴⁾	f = 120 Hz, \	/ _I = 15 V to 25 V	55	71		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	: +25°C		2		V
R _O	Output Resistance ⁽¹⁴⁾	f = 1 kHz			18		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	_A = +25°C		230		mA
I _{PK}	Peak Current ⁽¹⁴⁾	T _J = +25°C			2.2		A

Notes:

13. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7815E)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 23 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		14.40	15.00	15.60	
V _O	Output Voltage	$5.0 \text{ mA} \le I_0 \le V_1 = 17.5 \text{ V to}$	≤ 1.0 A, P _O ≤ 15 W, o 30 V	14.25	15.00	15.75	V
Poglino	Line Regulation ⁽¹⁵⁾	T _{.1} = +25°C	$V_{I} = 17.5 \text{ V to } 30 \text{ V}$		11	300	mV
Regline		$1_{\rm J} = +25$ C	$V_{I} = 20 V \text{ to } 26 V$		3	150	
Doglood	Load Regulation ⁽¹⁵⁾	T _J = +25°C	$I_{O} = 5 \text{ mA} \text{ to } 1.5 \text{ A}$		12	300	mV
Regload		1j = +25 C	I _O = 250 mA to 750 mA		4	150	mv
Ι _Q	Quiescent Current	T _J = +25°C			5.2	8.0	mA
AL	Quiescent Current Change	l _O = 5 mA to	1.0 A			0.5	mA
ΔI_Q	Quiescent Current Change	V _I = 17.5 V t	o 30 V			1.0	
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹⁶⁾	l _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 10	00 kHz, T _A = +25°C		90		μV
RR	Ripple Rejection ⁽¹⁶⁾	f = 120 Hz, ∖	$V_{\rm I} = 18.5 \text{ V} \text{ to } 28.5 \text{ V}$	54	70		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽¹⁶⁾	f = 1 kHz			19		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	,= +25°C		250		mA
I _{PK}	Peak Current ⁽¹⁶⁾	T _J =+25°C			2.2		A

Notes:

15. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7818E)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 27 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J =+25°C		17.3	18.0	18.7	
V _O	Output Voltage	5.0 mA \leq I _O \leq V _I = 21 V to \leq	≤ 1.0 A, P _O ≤ 15 W, 33 V	17.1	18.0	18.9	V
Poglino	Line Regulation ⁽¹⁷⁾	T _{.1} = +25°C	V _I = 21 V to 33 V		15	360	mV
Regline		$I_{\rm J} = +25^{\circ}{\rm C}$	$V_{I} = 24 V \text{ to } 30 V$		5	180	
Declard	Load Regulation ⁽¹⁷⁾	T 125°C	$I_{O} = 5 \text{ mA}$ to 1.5 A		15	360	
Regload		T _J = +25°C	I _O = 250 mA to 750 mA		5	180	mV
ا _Q	Quiescent Current	$T_J = +25^{\circ}C$			5.2	8.0	mA
41	Quiescent Current	$I_0 = 5 \text{ mA to}$	$I_0 = 5 \text{ mA to } 1.0 \text{ A}$			0.5	mA
ΔI_Q	Change	$V_{I} = 21 \text{ V to}$	33 V			1.0	mA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽¹⁸⁾	I _O = 5 mA			-1		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 10	00 kHz, T _A = +25°C		110		μV
RR	Ripple Rejection ⁽¹⁸⁾	f = 120 Hz, \	$V_{\rm I} = 22 {\rm V}$ to 32 V	53	69		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽¹⁸⁾	f = 1 kHz			22		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	, = +25°C		250		mA
I _{PK}	Peak Current ⁽¹⁸⁾	T _J = +25°C			2.2		Α

Notes:

17. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7824E)

Refer to test circuit, -40°C < T_J < 125°C, I_O = 500 mA, V_I = 33 V, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Parameter	(Conditions	Min.	Тур.	Max.	Unit
		T _J = +25°C		23.00	24.00	25.00	
V _O	Output Voltage	5.0 mA \leq I _O \leq V _I = 27 V to \leq	≤ 1.0 A, P _O ≤ 15 W, 38 V	22.80	24.00	25.25	V
Regline	Line Regulation ⁽¹⁹⁾	T _{.1} = +25°C	$V_{I} = 27 V \text{ to } 38 V$		17	480	mV
Regime		$T_{\rm J} = +25$ C	$V_{I} = 30 \text{ V} \text{ to } 36 \text{ V}$		6	240	
Poglood	Load Regulation ⁽¹⁹⁾	T - 125°C	$I_0 = 5 \text{ mA to } 1.5 \text{ A}$		15	480	mV
Regload		T _J = +25°C	I _O = 250 mA to 750 mA		5	240	mv
Ι _Q	Quiescent Current	T _J = +25°C	$T_J = +25^{\circ}C$		5.2	8.0	mA
A I	Quiescent Current	$I_0 = 5 \text{ mA to}$	1.0 A		0.1	0.5	mA
ΔI_Q	Change	$V_{1} = 27 V \text{ to } 3$	38 V		0.5	1.0 m	- IIIA
$\Delta V_O / \Delta T$	Output Voltage Drift ⁽²⁰⁾	I _O = 5mA			-1.5		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 10	00 kHz, T _A = +25°C		120		μV
RR	Ripple Rejection ⁽²⁰⁾	f = 120 Hz, V	$V_{\rm I} = 28 {\rm V}$ to 38 V	50	67		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V
R _O	Output Resistance ⁽²⁰⁾	f = 1 kHz			28		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	,= +25°C		230		mA
I _{PK}	Peak Current ⁽²⁰⁾	T _J = +25°C			2.2		A

Notes:

19. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7805AE)

Refer to the test circuit, $0^{\circ}C < T_J < +125^{\circ}C$, $I_0 = 1$ A, $V_I = 10$ V, $C_I = 0.33 \mu$ F, $C_O = 0.1 \mu$ F, unless otherwise specified.

Symbol	Parameter	Co	onditions	Min.	Тур.	Max.	Unit
		T _J =+25°C		4.9	5.0	5.1	
Vo	Output Voltage	$I_0 = 5 \text{ mA to } 7$ $V_1 = 7.5 \text{ V to } 2$	1 A, P _O ≤ 15 W, 20 V	4.8	5.0	5.2	V
		V _I = 7.5 V to 2	25 V, I _O = 500 mA		5.0	50.0	
Dealine	Line Regulation ⁽²¹⁾	V _I = 8 V to 12	2 V		3.0	50.0	mV
Regline		T	V _I = 7.3 V to 20 V		5.0	50.0	mv
		T _J = +25°C	V _I = 8 V to 12 V		1.5	25.0	
		T _J =+25°C, I _C	$_{0} = 5 \text{ mA} \text{ to } 1.5 \text{ A}$		9	100	
Regload	Load Regulation ⁽²¹⁾	I _O = 5 mA to 1 A I _O = 250 mA to 750 mA			9	100	mV
					4	50	
Ι _Q	Quiescent Current	T _J = +25°C			5	6	mA
		$I_0 = 5 \text{ mA to } 2$	1 A			0.5	
ΔI_Q	Quiescent Current Change	$V_{l} = 8 V \text{ to } 25$	5 V, I _O = 500 mA			0.8	mA
		$V_{l} = 7.5 V \text{ to } 2$	$V_{I} = 7.5 \text{ V to } 20 \text{ V}, \text{T}_{\text{J}} = +25^{\circ}\text{C}$			0.8	
$\Delta V / \Delta T$	Output Voltage Drift ⁽²²⁾	l _O = 5 mA			-0.8		mV/°C
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A =+25°C		42		μV
RR	Ripple Rejection ⁽²²⁾		f = 120 Hz, I _O = 500 mA, V _I = 8 V to 18 V		68		dB
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J = +25°C			2		V
R _O	Output Resistance ⁽²²⁾	f = 1 kHz			17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	= +25°C		250		mA
I _{PK}	Peak Current ⁽²²⁾	T _J = +25°C			2.2		A

Notes:

21. Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7809AE)

Refer to the test circuit, $0^{\circ}C < T_J < +125^{\circ}C$, $I_O = 1$ A, $V_I = 15$ V, $C_I = 0.33 \mu$ F, $C_O = 0.1 \mu$ F, unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$	8.82	9.00	9.18	
V _O Output Voltage		$I_{O} = 5 \text{ mA to 1 A}, P_{O} \le 15 \text{ W},$ $V_{I} = 11.2 \text{ V to } 24 \text{ V}$	8.65	9.00	9.35	V
		$V_{I} = 11.7 \text{ V to } 25 \text{ V}, I_{O} = 500 \text{ mA}$		6	90	
Regline	Line Regulation ⁽²³⁾	V _I = 12.5 V to 19 V		4	45	mV
Regime		$T_{J} = +25^{\circ}C \frac{V_{I} = 11.5 \text{ V to } 24 \text{ V}}{V_{I} = 12.5 \text{ V to } 19 \text{ V}}$		6	90	1110
		$V_{\rm I} = 12.5 \text{ V} \text{ to } 19 \text{ V}$		2	45	
	$T_1 = +25^{\circ}C$, $I_0 = 5$ mA to 1.0 A			12	100	
Regload	Load Regulation ⁽²³⁾	I _O = 5 mA to 1.0 A		12	100	mV
		I _O = 250 mA to 750 mA		5	50	
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$		5	6	mA
		$V_{I} = 11.7 \text{ V to } 25 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$			0.8	
ΔI_Q	Quiescent Current Change	$V_{I} = 12 \text{ V to } 25 \text{ V}, I_{O} = 500 \text{ mA}$			0.8	mA
		I _O = 5 mA to 1.0 A			0.5	
$\Delta V / \Delta T$	Output Voltage Drift ⁽²⁴⁾	I _O = 5 mA		-1		mV/°C
V _N	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$		58		μV
RR	Ripple Rejection ⁽²⁴⁾	$f = 120 \text{ Hz}, I_0 = 500 \text{ mA},$ $V_1 = 12 \text{ V to } 22 \text{ V}$		62		dB
V _{Drop}	Dropout Voltage	$I_{O} = 1 \text{ A}, \text{ T}_{J} = +25^{\circ}\text{C}$		2		V
R _O	Output Resistance ⁽²⁴⁾	f = 1 kHz		17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A = +25°C		250		mA
I _{PK}	Peak Current ⁽²⁴⁾	$T_J = +25^{\circ}C$		2.2		A

Notes:

23. Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7810AE)

Refer to the test circuit, $0^{\circ}C < T_J < +125^{\circ}C$, $I_O = 1$ A, $V_I = 16$ V, $C_I = 0.33 \mu$ F, $C_O = 0.1 \mu$ F, unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
		T _J =+25°C		10.0	10.2	
Vo	Output Voltage	$I_{O} = 5 \text{ mA to 1 A}, P_{O} \le 15 \text{ W},$ $V_{I} = 12.8 \text{ V to } 25 \text{ V}$	9.6	10.0	10.4	V
		$V_{I} = 12.8 \text{ V to } 26 \text{ V}, I_{O} = 500 \text{ mA}$		8	100	
Regline	Line Regulation ⁽²⁵⁾	V _I = 13 V to 20 V		4	50	mV
Regime		$T_{J} = +25^{\circ}C \frac{V_{I} = 12.5 \text{ V to } 25 \text{ V}}{V_{I} = 13 \text{ V to } 20 \text{ V}}$		8	100	IIIV
		$V_1 = 13 V \text{ to } 20 V$		3	50	
	(25)	$T_{J} = +25^{\circ}C, I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		12	100	
Regload	Load Regulation ⁽²⁵⁾	$I_{O} = 5 \text{ mA to } 1 \text{ mA}$		12	100	mV
		I _O = 250 mA to 750 mA		5	50	
۱ _Q	Quiescent Current	$T_J = +25^{\circ}C$		5	6	mA
		I _O = 5 mA to 1.0 A			0.5	
ΔI_Q	Quiescent Current Change	$V_{I} = 12.8 \text{ V to } 25 \text{ V}, I_{O} = 500 \text{ mA}$			0.8	mA
		$V_{I} = 13 \text{ V to } 26 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$			0.5	
$\Delta V / \Delta T$	Output Voltage Drift ⁽²⁶⁾	I _O = 5 mA		-1		mV/°C
V _N	Output Noise Voltage	$f = 10 \text{ Hz to } 100 \text{ kHz}, T_A = +25^{\circ}\text{C}$		58		μV
RR	Ripple Rejection ⁽²⁶⁾	$f = 120 \text{ Hz}, I_0 = 500 \text{ mA},$ V _I = 14 V to 24 V		62		dB
V _{Drop}	Dropout Voltage	$I_{O} = 1 \text{ A}, \text{ T}_{J} = +25^{\circ}\text{C}$		2		V
R _O	Output Resistance ⁽²⁶⁾	f = 1 kHz		17		mΩ
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A = +25°C		250		mA
I _{PK}	Peak Current ⁽²⁶⁾	T _J = +25°C		2.2		A

Notes:

25. Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7812AE)

Refer to the test circuit, $0^{\circ}C < T_J < +125^{\circ}C$, $I_O = 1$ A, $V_I = 19$ V, $C_I = 0.33 \mu$ F, $C_O = 0.1 \mu$ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit	
		$T_J = +25^{\circ}C$	$T_J = +25^{\circ}C$		12.00	12.25		
V _O	Output Voltage	$I_{O} = 5 \text{ mA to 1 A}, P_{O} \le 15 \text{ W},$ $V_{I} = 14.8 \text{ V to 27 V}$ 1:		11.50	12.00	12.50	V	
		V _I = 14.8 V to 30 V, I _O = 500 mA			10	120	1	
Doglino	Line Regulation ⁽²⁷⁾	V _I = 16 V to 2	2 V		4	120	mV	
Regline	Line Regulation.	T .25%C	V _I = 14.5 V to 27 V		10	120		
		$I_{\rm J} = +25^{\circ}{\rm C}$	V_{l} = 14.5 V to 27 V V_{l} = 16 V to 22 V		3	60		
		T _J = +25°C, I	_O = 5 mA to 1.5 A		12	100		
Regload	Load Regulation ⁽²⁷⁾	$I_{O} = 5 \text{ mA to}$	1.0 A		12	100	mV	
		I _O = 250 mA to 750 mA			5	50		
Ι _Q	Quiescent Current	$T_J = +25^{\circ}C$			5.1	6.0	mA	
		$V_{I} = 15 V \text{ to } 3$	30 V, T _J = +25°C			0.8		
ΔI_Q	Quiescent Current Change	$V_{I} = 14 \text{ V to } 2$	27 V, I _O = 500 mA			0.8	mA	
		$I_{O} = 5 \text{ mA to}$	1.0 A			0.5		
$\Delta V / \Delta T$	Output Voltage Drift ⁽²⁸⁾	l _O = 5 mA			-1		mV/°C	
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		76		μV	
RR	Ripple Rejection ⁽²⁸⁾	$f = 120 \text{ Hz}, I_0 = 500 \text{ mA}, V_1 = 14 \text{ V to } 24 \text{ V}$			60		dB	
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	+25°C		2		V	
R _O	Output Resistance ⁽²⁸⁾	f = 1 kHz			18		mΩ	
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	= +25°C		250		mA	
I _{PK}	Peak Current ⁽²⁸⁾	T _J = +25°C			2.2		А	

Notes:

27. Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Electrical Characteristics (KA7815AE)

Refer to the test circuit, $0^{\circ}C < T_J < +125^{\circ}C$, $I_O = 1$ A, $V_I = 23$ V, $C_I = 0.33 \mu$ F, $C_O = 0.1 \mu$ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit	
		T _J = +25°C		14.7	15.0	15.3		
Vo	Output Voltage	$I_{O} = 5 \text{ mA to 1 A}, P_{O} \le 15 \text{ W},$ $V_{I} = 17.7 \text{ V to 30 V}$		14.4	15.0	15.6	V	
		$V_{I} = 17.9 \text{ V to } 30 \text{ V}, I_{O} = 500 \text{ mA}$			10	150	50	
Regline	Line Regulation ⁽²⁹⁾	$V_{I} = 20 V \text{ to } 2$	$V_1 = 20 V \text{ to } 26 V$		5	150		
Regime		T 105°C	$V_{I} = 17.5 V \text{ to } 30 V$ $V_{I} = 20 V \text{ to } 26 V$		11	150	mV	
		$I_{\rm J} = +25^{\circ}{\rm C}$	V _I = 20 V to 26 V		3	75		
		$T_{J} = +25^{\circ}C, I_{0}$	_O = 5 mA to 1.5 A		12	100		
Regload	ad Load Regulation ⁽²⁹⁾	$I_{O} = 5 \text{ mA to}$	1.0 A		12	100	mV	
		I _O = 250 mA to 750 mA			5	50		
Ι _Q	Quiescent Current	T _J = +25°C			5.2	6.0	mA	
		$V_{I} = 17.5 \text{ V to } 30 \text{ V}, \text{ T}_{J} = +25^{\circ}\text{C}$				0.8		
ΔI_Q	Quiescent Current Change	V _I = 17.5 V to	0 30 V, I _O = 500 mA			0.8	mA	
		$I_{O} = 5 \text{ mA to}$	1.0 A			0.5		
$\Delta V / \Delta T$	Output Voltage Drift ⁽³⁰⁾	I _O = 5 mA			-1		mV/°C	
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		90		μV	
RR	Ripple Rejection ⁽³⁰⁾	f = 120 Hz, I _O = 500 mA, V _I = 18.5 V to 28.5 V			58		dB	
V _{Drop}	Dropout Voltage	$I_{O} = 1 \text{ A}, \text{ T}_{J} = +25^{\circ}\text{C}$			2		V	
R _O	Output Resistance ⁽³⁰⁾	f = 1 kHz			19		mΩ	
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	= +25°C		250		mA	
I _{PK}	Peak Current ⁽³⁰⁾	T _J = +25°C			2.2		A	

Notes:

29. Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

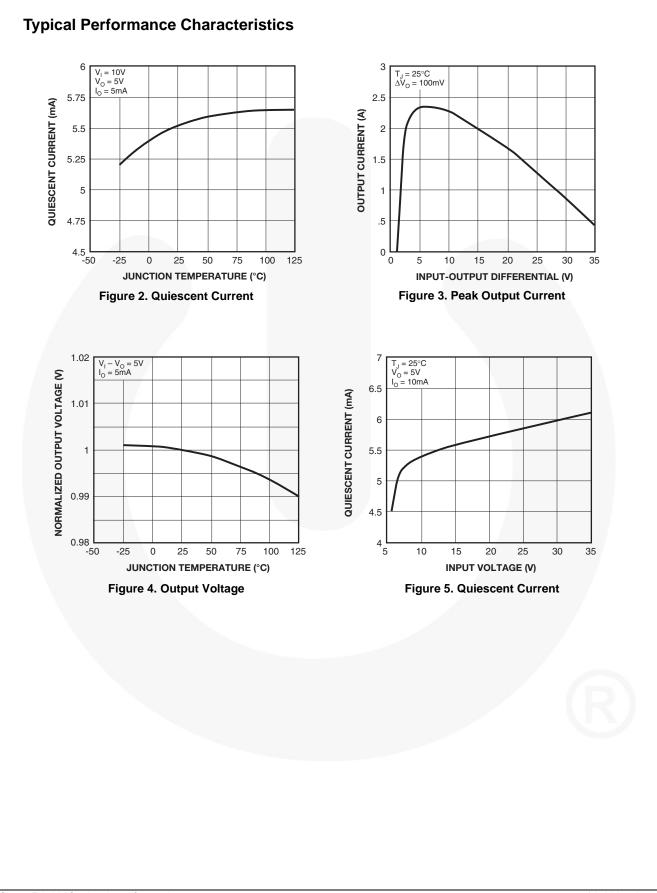
Electrical Characteristics (KA7824AE)

Refer to the test circuit, $0^{\circ}C < T_J < +125^{\circ}C$, $I_O = 1$ A, $V_I = 33$ V, $C_I = 0.33 \mu$ F, $C_O = 0.1 \mu$ F, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit	
		T _J = +25°C		23.5	24.0	24.5		
V _O	Output Voltage	ut Voltage $I_O = 5 \text{ mA to 1 A}, P_O \le 15 \text{ W},$ $V_I = 27.3 \text{ V to 38 V}$		23.0	24.0	25.0	V	
		V _I = 27 V to 38 V, I _O = 500 mA			18	240		
Doglino	Line Regulation ⁽³¹⁾	$V_{I} = 21 \text{ V to } 3$	3 V		6	240	m\/	
Regline		T .25%C	$V_1 = 26.7 V \text{ to } 38 V$ $V_1 = 30 V \text{ to } 36 V$		18	240	- mV	
		T _J = +25°C	V _I = 30 V to 36 V		6	120		
	(24)		$T_{J} = +25^{\circ}C, I_{O} = 5 \text{ mA to } 1.5 \text{ A}$		15	100		
Regload	oad Load Regulation ⁽³¹⁾	$I_{O} = 5 \text{ mA to}$	1.0 A		15	100	mV	
		I _O = 250 mA to 750 mA			7	50		
Ι _Q	Quiescent Current	T _J = +25°C			5.2	6.0	mA	
		$V_{\rm I} = 27.3 \ V \ tc$	o 38 V, T _J = +25°C			0.8		
ΔI_Q	Quiescent Current Change	$V_{\rm I} = 27.3 \ V \ tc$	o 38 V, I _O = 500 mA			0.8	mA	
		$I_{O} = 5 \text{ mA to}$	1.0 A			0.5		
$\Delta V / \Delta T$	Output Voltage Drift ⁽³²⁾	I _O = 5 mA			-1.5		mV/°C	
V _N	Output Noise Voltage	f = 10 Hz to 1	00 kHz, T _A = +25°C		120		μV	
RR	Ripple Rejection ⁽³²⁾	$f = 120 \text{ Hz}, I_0 = 500 \text{ mA}, V_1 = 28 \text{ V to } 38 \text{ V}$			54		dB	
V _{Drop}	Dropout Voltage	I _O = 1 A, T _J =	: +25°C		2		V	
R _O	Output Resistance ⁽³²⁾	f = 1 kHz			20		mΩ	
I _{SC}	Short-Circuit Current	V _I = 35 V, T _A	= +25°C		250		mA	
I _{PK}	Peak Current ⁽³²⁾	T _J = +25°C			2.2		A	

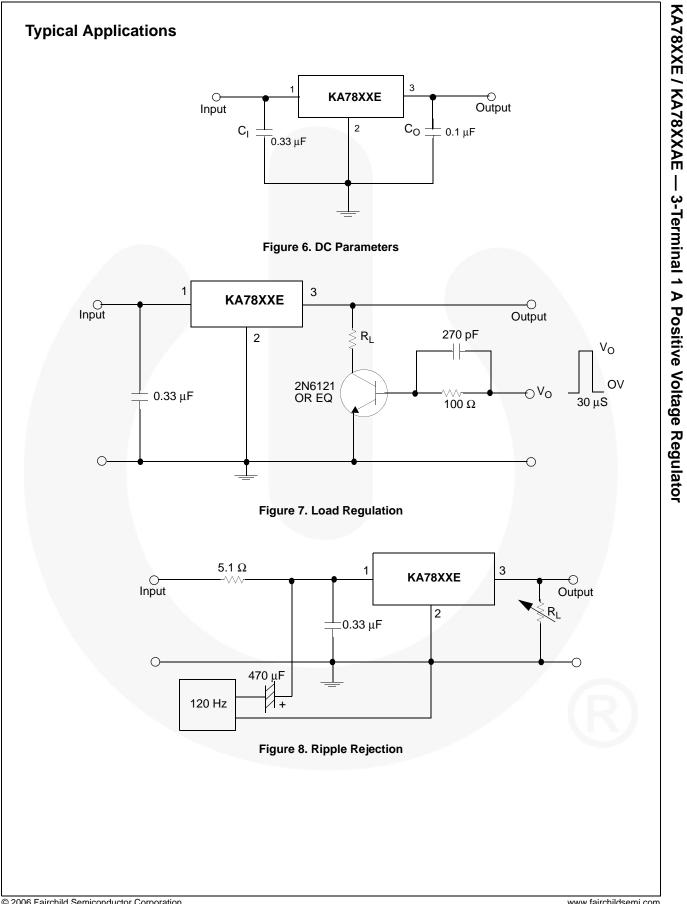
Notes:

31. Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.



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- 3-Terminal 1 A Positive Voltage Regulator





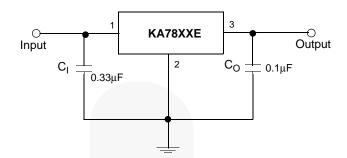


Figure 9. Fixed Output Regulator

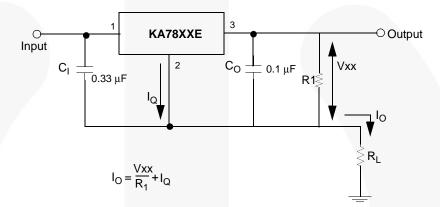
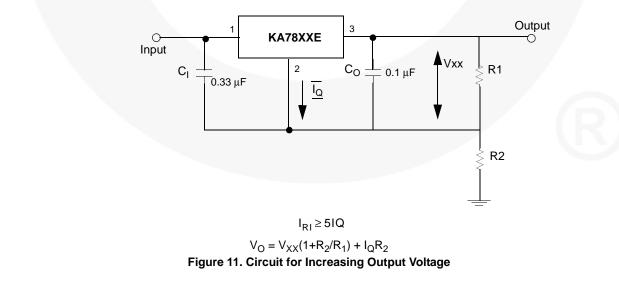
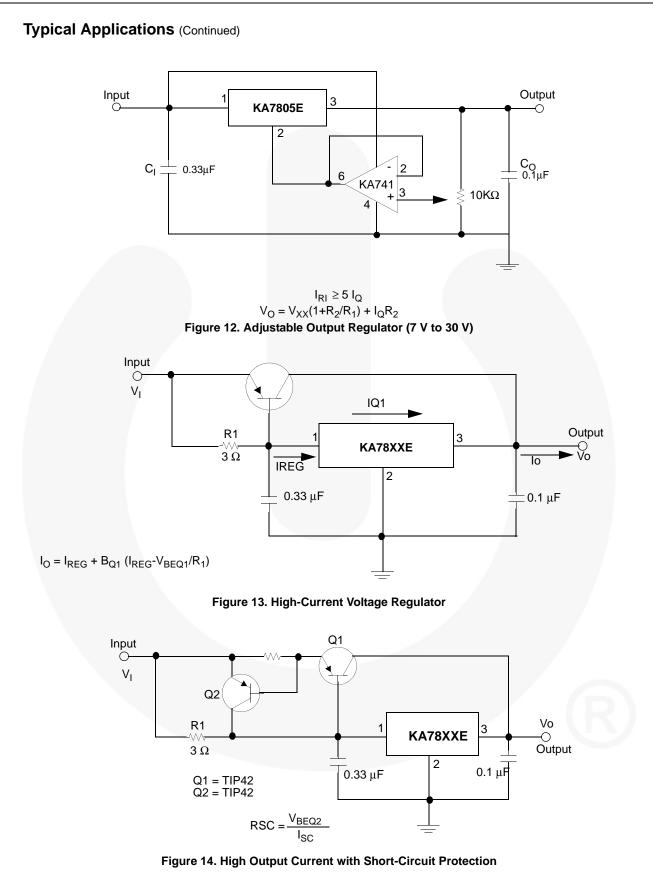


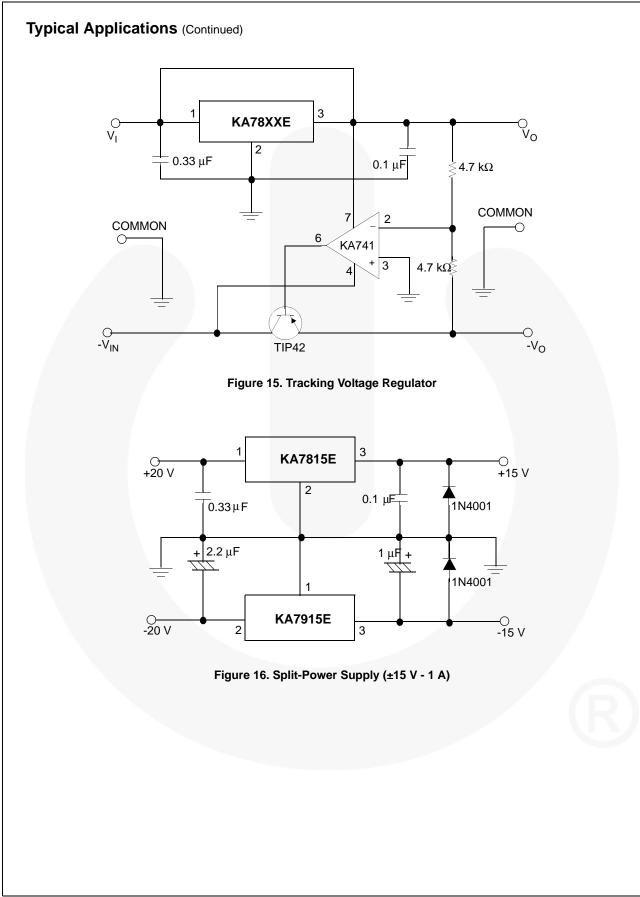
Figure 10. Constant Current Regulator

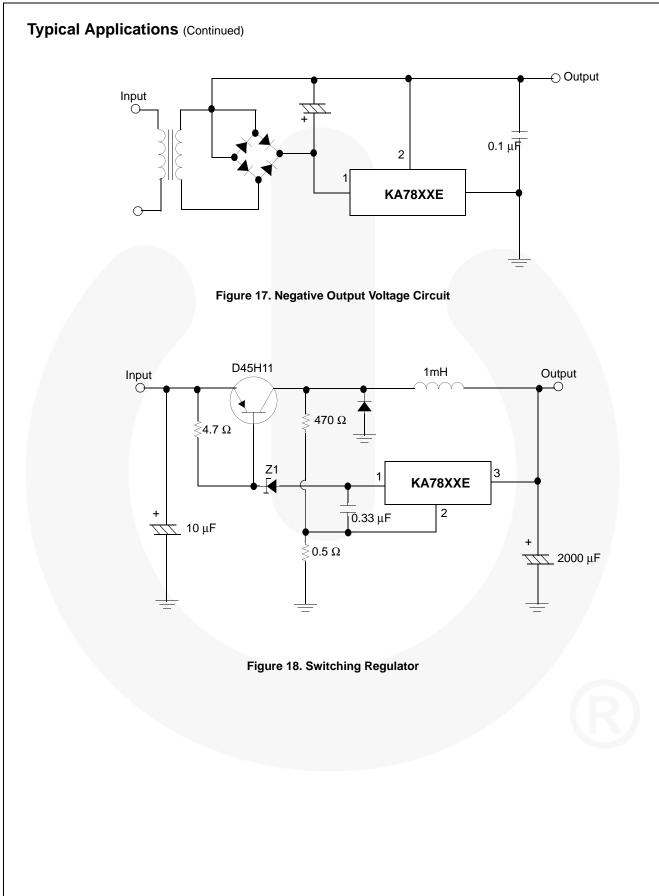
Notes:

- 33. To specify an output voltage, substitute voltage value for "XX". A common ground is required between the input and the output voltage. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.
- 34. C₁ is required if regulator is located an appreciable distance from power supply filter.
- 35. C_O improves stability and transient response.

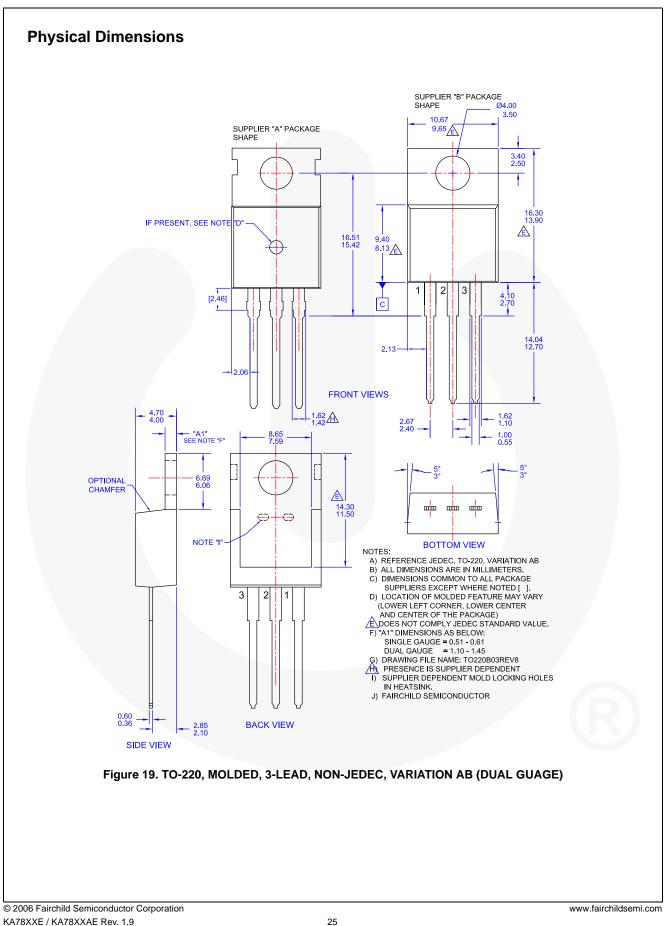


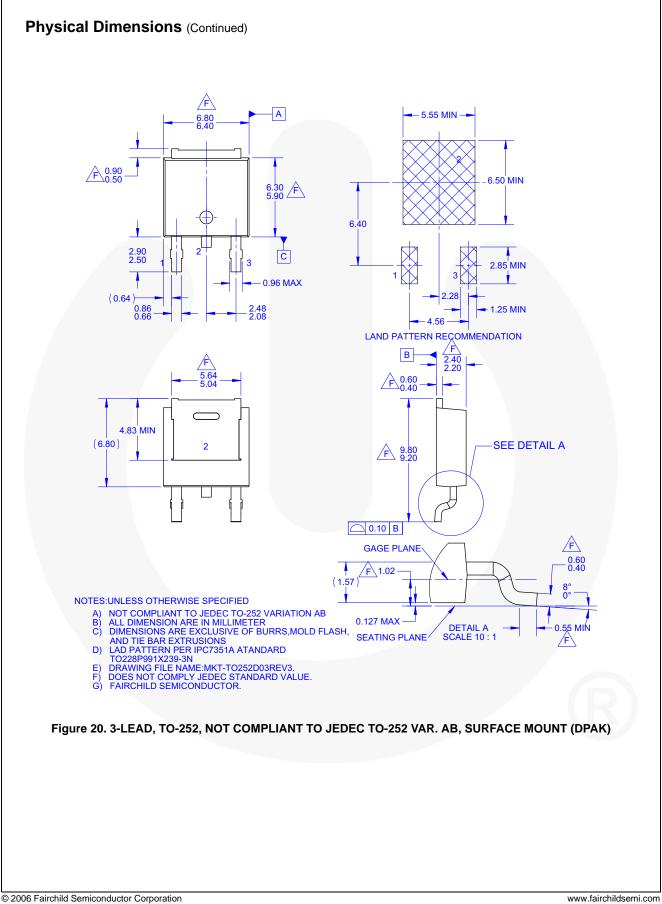






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- 3-Terminal 1 A Positive Voltage Regulator

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