May, 9th 2012 Automotive grade

International **IOR** Rectifier

AUIPS1041(L)(R)/AUIPS1042G

SINGLE/DUAL CHANNEL INTELLIGENT POWER LOW SIDE SWITCH

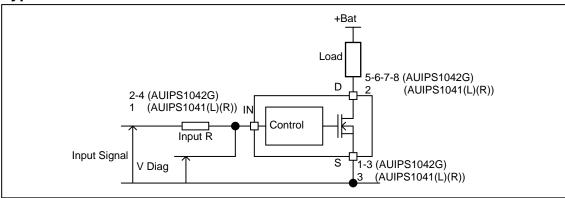
Features

- Over temperature shutdown
- Over current shutdown
- Active clamp
- Low current & logic level input
- ESD protection
- Optimized Turn On/Off for EMI
- Diagnostic on the input current

Description

The AUIPS1041(L)(R) and AUIPS1042G are three terminal Intelligent Power Switches (IPS) featuring low side MOSFETs with over-current, over-temperature, ESD protection and drain to source active clamp. The AUIPS1042G is a dual channel device while the AUIPS1041 is a single channel. These devices offer protections and the high reliability required in harsh environments. Each switch provides efficient protection by turning OFF the power MOSFET when the temperature exceeds 165°C or when the drain current reaches 4.5A. The device restarts once the input is cycled. A serial resistance connected to the input provides the diagnostic. The avalanche capability is significantly enhanced by the active clamp and covers most inductive load demagnetizations.

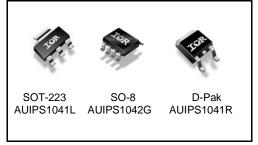
Typical Connection



Product Summary

Rds(on) 100mΩ (max.) Vclamp 39V Ishutdown 4.5A (typ.)

Packages



Qualification Information⁺

Qualification Level		Automotive (per AEC-Q100 ^{††}) Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.			
Moisture Sensitivity Level		DPAK-3L	MSL1, 260°C (per IPC/JEDEC J-STD-020)		
		SOT223-3L	MSL2, 260°C (per IPC/JEDEC J-STD-020)		
		8L-SOICN	MSL2, 260°C (per IPC/JEDEC J-STD-020)		
	Machine Model		Class M4 (+/-450V) (per AEC-Q100-003)		
ESD	Human Body Model		Class H2 (+/-2500V) (per AEC-Q100-002)		
Charged Device Model		Class C4 (+/-1000V) (per AEC-Q100-011)			
IC Latch-Up Test		Class II, L (per AEC-Q			
RoHS Complia	nt	Yes	Yes		

Qualification standards can be found at International Rectifier's web site http://www.irf.com/ † ††

Exceptions to AEC-Q100 requirements are noted in the qualification report.

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C, Vcc=6..36V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vds	Maximum drain to source voltage	-0.3	36	V
Vds cont	Maximum continuous drain to source voltage	-	28	V
Vin	Maximum input voltage	-0.3	6	V
Isd cont.	Max diode continuous current (limited by thermal dissipation)	—	1.5	А
Pd	Maximum power dissipation (internally limited by thermal protection) Rth=60°C/W AUIPS1041L 1" sqr. Footprint		2	W
	Rth=100°C/W AUIPS1042G std. footprint		1.25	
Tj max.	Max. storage & operating temperature junction temperature	-40	150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient AUIPS1041L SOT-223 std. footprint	100	_	
Rth2	Thermal resistance junction to ambient AUIPS1041L SOT-223 1" sqr. Footprint	60	_	
Rth1	Thermal resistance junction to ambient AUIPS1041R D-Pak std. footprint	70	_	1
Rth2	Thermal resistance junction to case AUIPS1041R D-Pak	6	_	°C/W
Rth1	Thermal resistance junction to ambient <u>AUIPS1042G SO-8 std. Footprint</u> 1 die active	100	_	
Rth1	Thermal resistance junction to ambient AUIPS1042G SO-8 std. footprint 2 die active	130	—	

Recommended Operating Conditions These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
VIH	High level input voltage	4.5	5.5	V
VIL	Low level input voltage	0	0.5	v
	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V Rth=60°C/W AUIPS1041L 1" sqr. Footprint	_	1.95	
lds	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V Rth=50°C/W AUIPS1041R 1" sqr. Footprint	_	2.2	
	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V Rth=100°C/W AUIPS1042G 1" sqr. Footprint - 1 die active		1.5	A
	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V Rth=130°C/W AUIPS1042G 1" sgr. Footprint - 2 die active		0.7	
Rin	Recommended resistor in series with IN pin to generate a diagnostic	0.5	10	kΩ
Max L	Max. recommended load inductance (including line inductance) (1)	_	20	μH
Max. F	Max. frequency	_	2000	Hz
Max. t rise	Max. input rising time	_	1	μs

(1) Higher inductance is possible if maximum load current is limited - see figure 11

Static Electrical Characteristics

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Rds(on)	ON state resistance Tj=25°C	_	80	100	mΩ	Vin=5V. Ids=3A
	ON state resistance Tj=150°C	_	135	175	1115.2	VIII=5V, IUS=5A
ldss1	Drain to source leakage current		0.1	2		Vcc=14V, Tj=25°C
ldss2	Drain to source leakage current	_	0.2	4	μA	Vcc=28V, Tj=25°C
V clamp1	Drain to source clamp voltage 1	36	38	_		Id=10mA
V clamp2	Drain to source clamp voltage 2	_	39	42	V	Id=1A
Vin clamp	IN to source pin clamp voltage	5.5	6.5	7.5	v	lin=1mA
Vth	Input threshold voltage	_	1.7	_		Id=10mA

Switching Electrical Characteristics

Vcc=14V, Resistive load=5Ω, Rinput=0Ω, Vin=5V, Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tdon	Turn-on delay time to 20%	2	7	15		
Tr	Rise time 20% to 80%	2	7	20		See figure 2
Tdoff	Turn-off delay time to 80%	15	40	150	μs	See figure 2
Tf	Fall time 80% to 20%	4	10	20		
Eon + Eoff	Turn on and off energy	_	0.2		mJ	

Protection Characteristics

Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	—	°C	See figure 1
lsd	Over current threshold	2.7	4.5	6	А	See figure 1
OV	Over voltage protection (not active when the device is ON)	34	37	-	V	
Vreset	IN protection reset threshold		1.7	_	V	
Treset	Time to reset protection	15(2)	50	200	μs	Vin=0V, Tj=25°C

(2) Guaranteed by design

Diagnostic

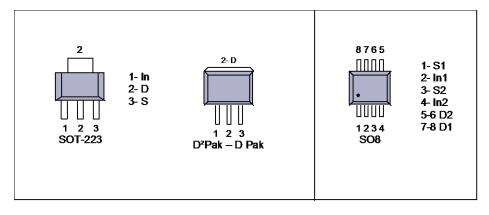
Tj= -40..150°C, Vcc=6..28V (unless otherwise specified), typical value are given for Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
lin, on	ON state IN positive current	10	32	80		Vin=5V
lin, off	OFF state IN positive current (after protection latched)	120	230	350	μA	Vin=5V

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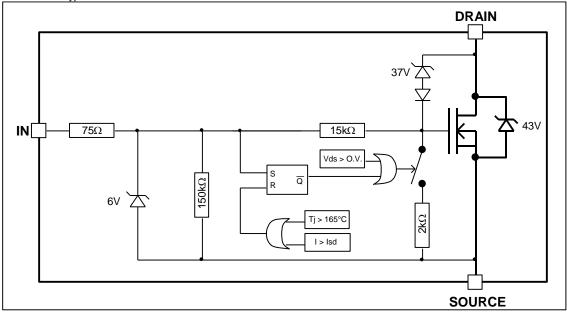
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Lead Assignments



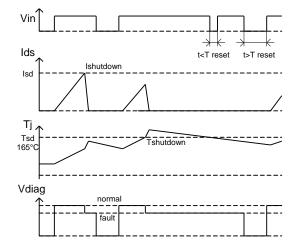
Functional Block Diagram

All values are typical



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All curves are typical values. Operating in the shaded area is not recommended.

Figure 1 – Timing diagram

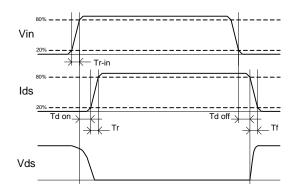


Figure 2 – IN rise time & switching definitions

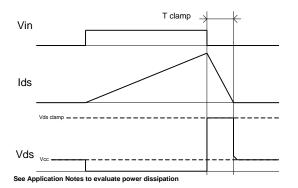


Figure 3 – Active clamp waveforms

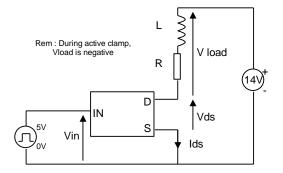


Figure 4 – Active clamp test circuit

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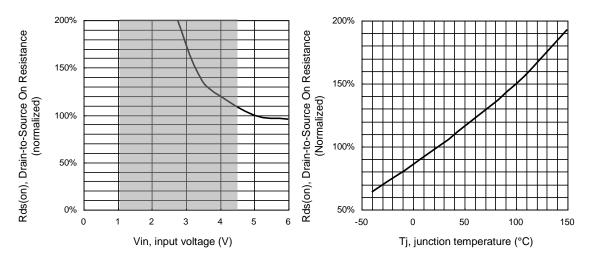
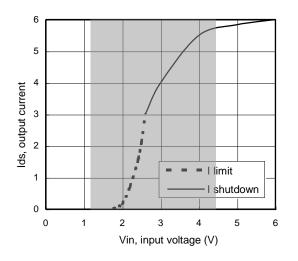


Figure 5 – Normalized Rds(on) (%) Vs Input voltage (V)



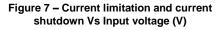
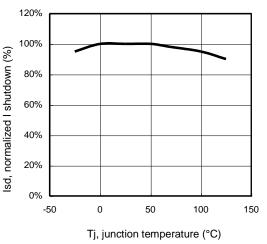
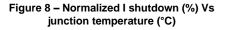
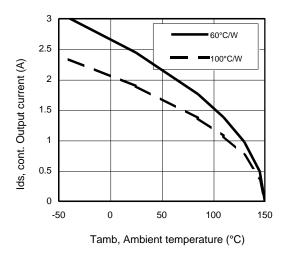


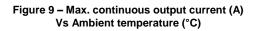
Figure 6 - Normalized Rds(on) (%) Vs Tj (°C)

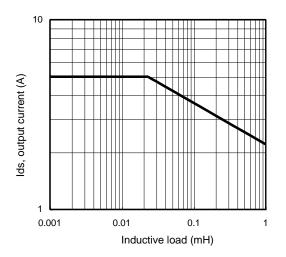


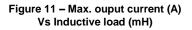


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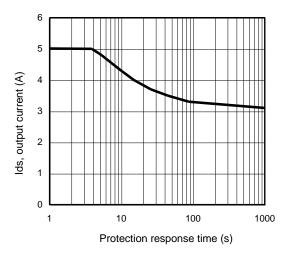
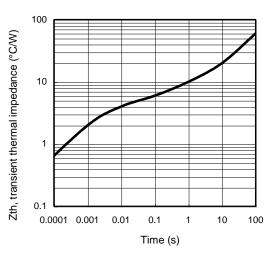
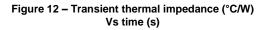
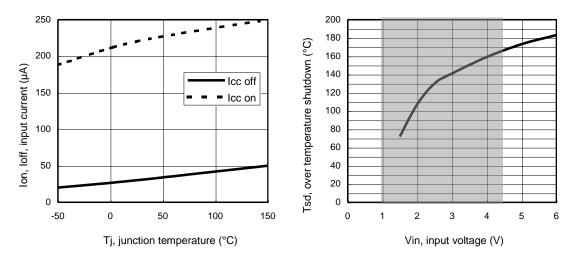


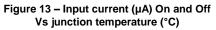
Figure 10 – Ids (A) Vs over temperature protection response time (s) / IPS1041L

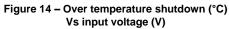




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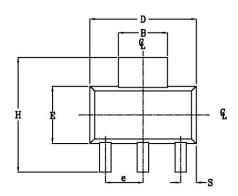


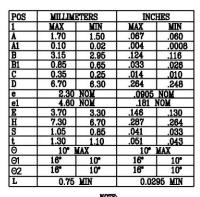




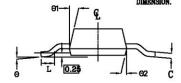
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Case Outline - SOT-223 - Automotive Q100 PbF MSL2 qualified





 NOTE: 1. PACKAGE OUTLINE EXCLUSIVE OF ANY MOLD FLASHES DIMENSION. 2. PACKAGE OUTLINE EXCLUSIVE OF BURR DIMENSION.



Leads and drain are plated with 100% Sn

MÁX.

(4)

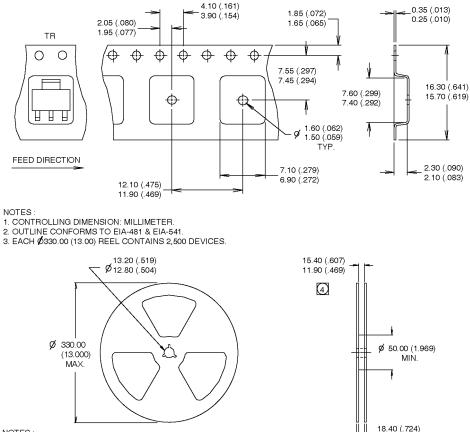
14.40 (.566) 12.4<u>0 (.</u>488)

3

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Tape & Reel - SOT-223

Dimensions are shown in milimeters (inches)



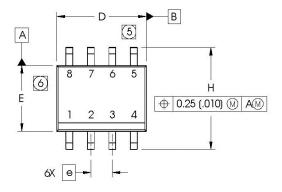
NOTES :

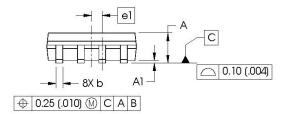
- 1. OUTLINE COMFORMS TO EIA-418-1.
- 2. CONTROLLING DIMENSION: MILLIMETER.
- DIMENSION MEASURED @ HUB.
- INCLUDES FLANGE DISTORTION @ OUTER EDGE.

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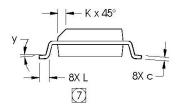
Case Outline - SO-8 - Automotive Q100 PbF MSL2 qualified

Dimensions are shown in millimeters (inches)





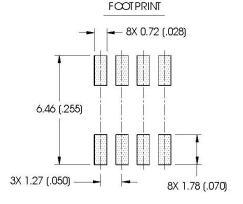
DIM	INC	HES	MILLIN	M ETERS	
	MIN	MAX	MIN	MAX	
Α	.0532	.0688	1.35	1.75	
A1	.0040	.0098	0.10	0.25	
b	.013	.020	0.33	0.51	
С	.0075	.0098	0.19	0.25	
D	.189	.1968	4.80	5.00	
Е	.1497	.1574	3.80	4.00	
е	.050 B.	ASIC	1.27 BASIC		
e1	.025 B.	ASIC	0.635	BASIC	
Н	.2284	.2440	5.80	6.20	
Κ	.0099	.0196	0.25	0.50	
L	.016	.050	0.40	1.27	
y	0°	8°	0°	8°	



NOTES:

- 1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
- 2. CONTROLLING DIMENSION: MILLIMETER
- 3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
- 4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- 5 DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.15 (.006).
- 6 DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.010).
- DIMENSION IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE.

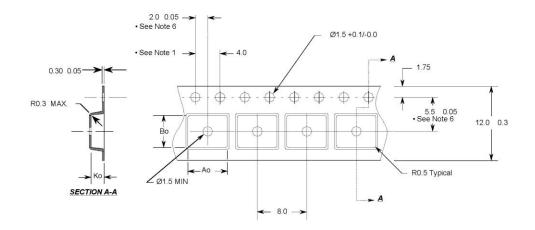
Leads and drain are plated with 100% Sn



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Tape & Reel - SO-8



Ao = 6.4 mm

Bo = 5.2 mm

Ko = 2.1 mm

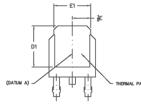
Notes:

- 1. 10 sprocket hole pitch cumulative tolerance 0.2
- 2. Camber not to exceed 1mm in 100mm
- 3. Material: Black Conductive Advantek Polystyrene
- 4. Ao and Bo measured on a plane 0.3mm above the
- bottom of the pocket
- 5. Ko measured from a plane on the inside bottom of the
- pocket to the top surface of the carrier.
- 6. Pocket position relative to sprocket hole measured as
- true position of pocket, not pocket hole.

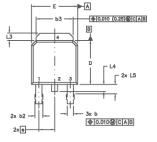
- All Dimensions in Millimeters -

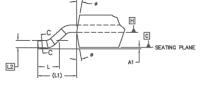
International **TOR** Rectifier

Case Outline - D-Pak - Automotive Q100 PbF MSL1 qualified

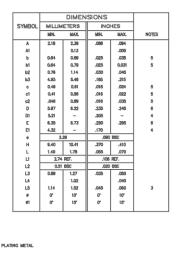


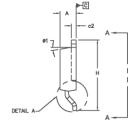






DETAIL "A" ROTATED 90"





NOTES:

SECTION C-C

PLAT

1.0 DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994.

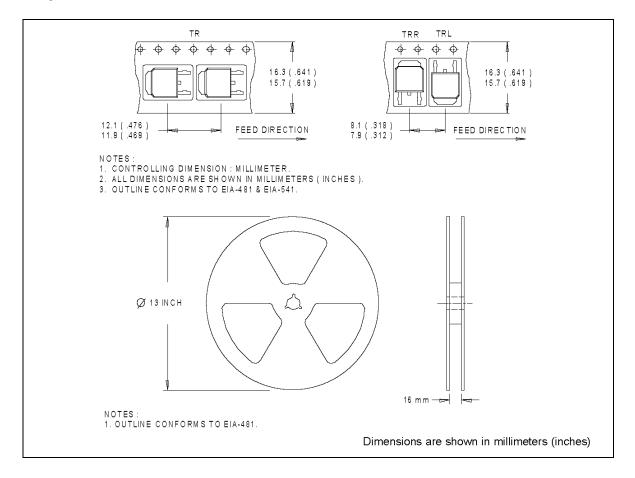
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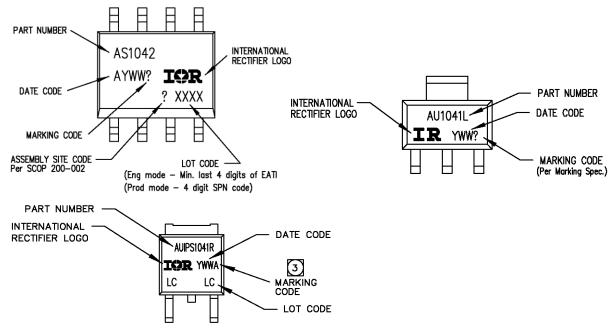
- DIMENSIONING AND TOLERANCING PER ASME Y14.5 M 1994. DIMENSIONS ARE SHOWN IN INCHES [MILLINETERS]. LEAD DIMENSION UNCONTROLLED IN LS DIMENSION DIA DE LESTBALESI A MINIMUM MOUNTING SURFACE FOR THERMAL PAD. SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 [0.127] AND JOI [0.25.0F ROW THE LEAD TP. DIMENSION D & E DO NOT INCLUEE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST ENTREMES OF THE FLASTIC BODY. 2.0 3.0 4.0 5.0
- 6.0
- 7.0 OUTLINE CONFORMS TO JEDEC OUTLINE TO-252AA.
- 8.0 LEADS AND DRAIN ARE PLTED WITH 100% Sn

International

Tape & Reel - D-Pak



Part Marking Information



Ordering Information

Base Part Number	Deckson Trees	Standard Pack		Occurrente (a Devel Merryhan
Dase i art iumber	Package Type	Form	Quantity	Complete Part Number
AUIPS1042	SOIC-8	Tube	95	AUIPS1042G
A01F31042	3010-6	Tape and reel	2500	AUIPS1042GTR
AUIPS1041	SOT-223	Tube	80	AUIPS1041L
A01F31041		Tape and reel	2500	AUIPS1041LTR
		Tube	75	AUIPS1041R
AUIPS1041	D-Pak-5-Lead	Tape and reel	2000	AUIPS1041RTR
AUIPS1041	D-Fak-5-Leau	Tape and reel left	3000	AUIPS1041RTRL
		Tape and reel right	3000	AUIPS1041RTRR

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For technical support, please contact IR's Technical Assistance Center http://www.irf.com/technical-info/

WORLD HEADQUARTERS:

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Revision History

Revision	Date	Notes/Changes
C1	November, 24 th , 2010	AU release
C2	December, 7 th 2010	Remove ESD section page 3
C3	December, 9 th 2010	Update qual page
C4	December, 14 th 2010	Update Tdon
D	February, 28 th 2011	Update Max rating
E	March, 14 th 2011	Update part marking
F	November, 14 th 2011	Update T&R SOT223
G	May 9th, 2012	Update component number for the SOT223
		tube.

Mouser Electronics

Authorized Distributor

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Infineon:

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