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RELEASED FOR PUBLICATION

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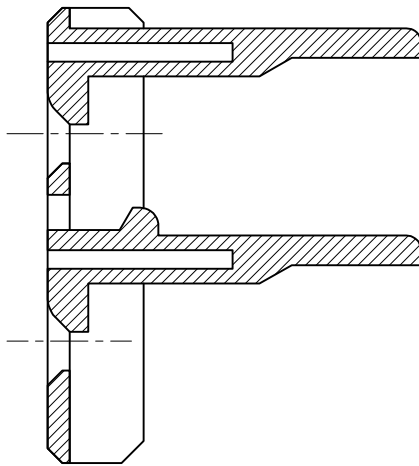
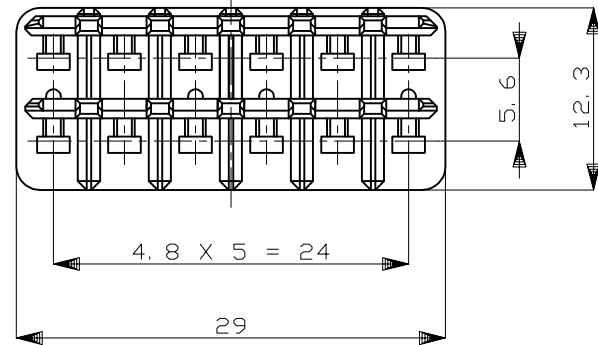
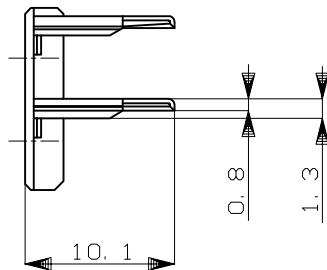
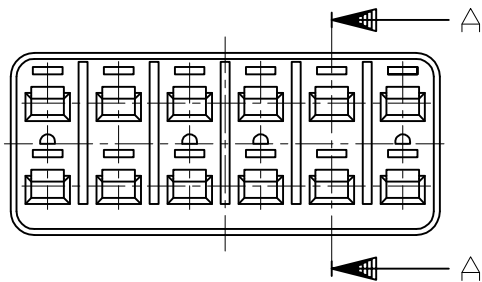
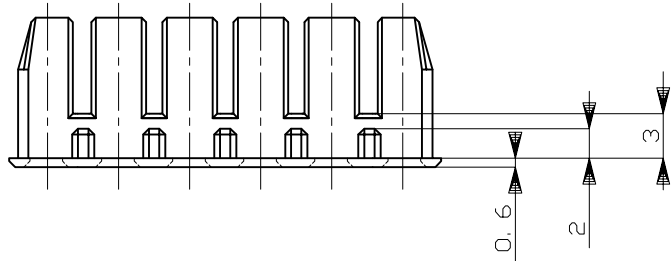
LOC J	DIST —	REVISIONS						
		P	LTR	DESCRIPTION		DATE	DWN	APVD
			B1	REVISED ECR-15-000191		27JAN2015	TS	KB

D

C

B


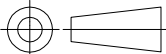
A



SECTION  
断面 A-A  
(尺度 5:1)

1. 本品は、プラグハウジング型番174661に装着される。
1. TO BE MATED WITH PLUG HOUSING (PN 174661)

GENERAL TOLERANCE	
0 < * ≤ 10	: ±0.2
10 < * ≤ 30	: ±0.25
30 < * ≤ 100	: ±0.3
ANGLES	: ±3°

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN K.ODA 5/2587	<div>TE Connectivity</div>			
DIMENSIONS: mm		CHK Y.MATSUSAKA 5/28'87				
	TOLERANCES UNLESS OTHERWISE SPECIFIED:  0 PLC ± - 1 PLC ± - 2 PLC ± - 3 PLC ± - 4 PLC ± - ANGLES ± -	APVD M.NOGUCHI 11/4'88	NAME  12P. ECONOSEAL J-MARKII+ CONNECTOR DOUBLE LOCK PLATE FOR PLUG HOUSING			
		PRODUCT SPEC  108-5262				
		APPLICATION SPEC  —	SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO  —
		MATERIAL  PBT	FINISH  —	WEIGHT  —	A3	
CUSTOMER DRAWING			SCALE 2:1	SHEET 1 OF 1	REV B1	

NOT the LATEST  
REVISION

# Mouser Electronics

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[1-174662-1](#)