

LTC1350

3.3V Low Power EIA/TIA-562 3-Driver/ 5-Receiver Transceiver

FEATURES

- Low Supply Current: 300µA
- Receivers 4 and 5 Kept Alive in Shutdown: 35uA
- ESD Protection: ±10kV
- Operates from a Single 3.3V Supply
- Uses Small Capacitors: 0.1µF
- Operates to 120kBaud
- Three-State Outputs are High Impedance When Off
- Output Overvoltage Does Not Force Current **Back into Supplies**
- EIA/TIA-562 I/O Lines Can Be Forced to ±25V Without Damage
- Flowthrough Architecture

APPLICATIONS

- **Notebook Computers**
- Palmtop Computers

DESCRIPTION

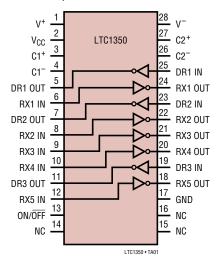
The LTC®1350 is a 3-driver/5-receiver EIA/TIA-562 transceiver with very low supply current. In the no load condition, the supply current is only 300µA. The charge pump only requires four 0.1µF capacitors.

In Shutdown mode, two receivers are kept alive and the supply current is only 35µA. All RS232 outputs assume a high impedance state in Shutdown or with the power off.

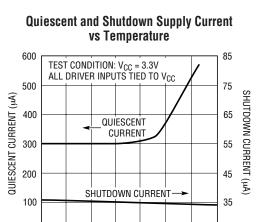
The LTC1350 is fully compliant with all data rate and overvoltage EIA/TIA-562 specifications. The transceiver can operate up to 120kbaud with a 1000pF and $3k\Omega$ load. Both driver outputs and receiver inputs can be forced to ±25V without damage and can survive multiple ±10kV ESD strikes.

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TYPICAL APPLICATION







20 40

0

0

-40 -20 60

100



ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V _{CC}) 5V Input Voltage
Driver $-0.3V$ to V _{CC} + 0.3V
Receiver – 25V to 25V
ON/\overline{OFF} Pin $-0.3V$ to V_{CC} + 0.3V
Output Voltage
Driver25V to 25V
Receiver $-0.3V$ to V _{CC} + 0.3V
Short-Circuit Duration
V ⁺
V ⁻
Driver Output Indefinite
Receiver Output Indefinite
Operating Temperature Range
Commercial (LTC1350C) 0°C to 70°C
Industrial (LTC1350I) –40°C to 85°C
Storage Temperature Range –65°C to 150°C
Lead Temperature (Soldering, 10 sec) 300°C

TOP VIEW ORDER PART 28 V-V⁺ 1 NUMBER V_{CC} 2 27 C2+ 26 C2-C1⁺ 3 LTC1350CG 25 DR1 IN C1- 4 LTC1350CNW DR1 OUT 5 24 RX1 OUT LTC1350CSW 23 DR2 IN RX1 IN 6 LTC1350IG 22 RX2 OUT DR2 OUT 7 LTC1350INW 21 RX3 OUT RX2 IN 8 LTC1350ISW RX3 IN 9 20 RX4 OUT RX4 IN 10 19 DR3 IN DR3 OUT 11 18 RX5 OUT 17 GND RX5 IN 12 ON/OFF 13 16 NC 15 NC NC 14 NW PACKAGE G PACKAGE 28-LEAD PDIP 28-LEAD SSOP SW PACKAGE 28-LEAD PLASTIC SO WIDE $\begin{array}{l} T_{JMAX} = 125^{\circ}C, \ \theta_{JA} = 96^{\circ}C/W \ (G) \\ T_{JMAX} = 125^{\circ}C, \ \theta_{JA} = 56^{\circ}C/W \ (NW) \\ T_{JMAX} = 125^{\circ}C, \ \theta_{JA} = 85^{\circ}C/W \ (SW) \end{array}$

Consult LTC Marketing for parts specified with wider operating temperature ranges.

DC ELECTRICAL CHARACTERISTICS The • denotes specifications which apply over the full operating

temperature range. V_{CC} = 3.3V, C1 = C2 = C3 = C4 = 0.1µF, unless noted.

PARAMETER	CONDITIONS			MIN	ТҮР	MAX	UNITS
Any Driver							
Output Voltage Swing	3k to GND	Positive Negative	•	3.7 -3.7	4.5 - 4.5		V V
Logic Input Voltage Level	Input Low Level (V _{OUT} = High) Input High Level (V _{OUT} = Low)		•	2.0	1.4 1.4	0.8	V V
Logic Input Current	$V_{IN} = V_{CC}$ $V_{IN} = 0V$		•			5 -5	μΑ μΑ
Output Short-Circuit Current	$V_{OUT} = 0V$			±9	±10		mA
Output Leakage Current	Shutdown (Note 3), $V_{OUT} = \pm 20V$				10	500	μA
Any Receiver							
Input Voltage Thresholds	Input Low Threshold Input High Threshold		•	0.8	1.3 1.7	2.4	V V
Hysteresis				0.1	0.4	1	V
Input Resistance	$V_{IN} = \pm 10V$			3	5	7	kΩ
Output Voltage	Output Low, $I_{OUT} = -1.6mA$ (V _{CC} = 3 Output High, $I_{OUT} = 160\mu A$ (V _{CC} = 3.	,	•	3.0	0.2 3.2	0.4	V V
Output Short-Circuit Current	Sinking Current, V _{OUT} = V _{CC}	*		-3	-20		mA
Output Leakage Current	Shutdown (Note 3), $0V \le V_{0UT} \le V_C$	0	•		1	10	μA

PACKAGE/ORDER INFORMATION



1350fa

DC ELECTRICAL CHARACTERISTICS The • denotes specifications which apply over the full operating

temperature range. V_{CC} = 3.3V, C1 = C2 = C3 = C4 = 0.1µF, unless noted.
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PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Power Supply Generator						
V ⁺ Output Voltage	I _{OUT} = 0mA			5.7		V
	$I_{OUT} = 5 m A$			5.5		V
V ⁻ Output Voltage	I _{OUT} = 0mA			-5.3		V
	$I_{OUT} = -5mA$			-5.0		V
Supply Rise Time	Shutdown to Turn-On			0.2		ms
Power Supply	·	·				
V _{CC} Supply Current	No Load (All Drivers $V_{IN} = V_{CC}$)(Note 2) 0°C $\leq T_A \leq$ 70°C			0.3	0.6	mA
	No Load (All Drivers $V_{IN} = 0$)(Note 2) $0^{\circ}C \le T_A \le 70^{\circ}C$			0.5	1.0	mA
	No Load (All Drivers $V_{IN} = V_{CC}$)(Note 2) 0°C $\leq T_A \leq 85$ °C			0.3	1.0	mA
	No Load (All Drivers $V_{IN} = V_{CC}$)(Note 2) -40°C $\leq T_A \leq 0$ °C			0.3	1.5	mA
	No Load (All Drivers $V_{IN} = 0$)(Note 2) $-40^{\circ}C \le T_A \le 85^{\circ}C$			0.5	1.5	mA
	Shutdown (Note 3)	•		35	50	μA
ON/OFF Threshold Low				1.4	0.8	V
ON/OFF Threshold High		•	2.0	1.4		V

AC CHARACTERISTICS The • denotes specifications which apply over the full operating temperature range. $V_{CC} = 5V$, $C1 = C2 = C3 = C4 = 0.1 \mu F$, unless noted.

PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Slew Rate	R _L = 3k, C _L = 51pF			8	30	V/µs
	$R_{L} = 3k, C_{L} = 1000pF$		3	5		V/µs
Driver Propagation Delay	t _{HLD} (Figure 1)	•		2	3.5	μS
(TTL to EIA/TIA-562)	t _{LHD} (Figure 1)	•		2	3.5	μS
Receiver Propagation Delay	t _{HLR} (Figure 2)	•		0.3	0.8	μS
(EIA/TIA-562 to TTL)	t _{LHR} (Figure 2)	•		0.3	0.8	μS

Note 1: Absolute Maximum Ratings are those values beyond which the life of the device may be impaired.

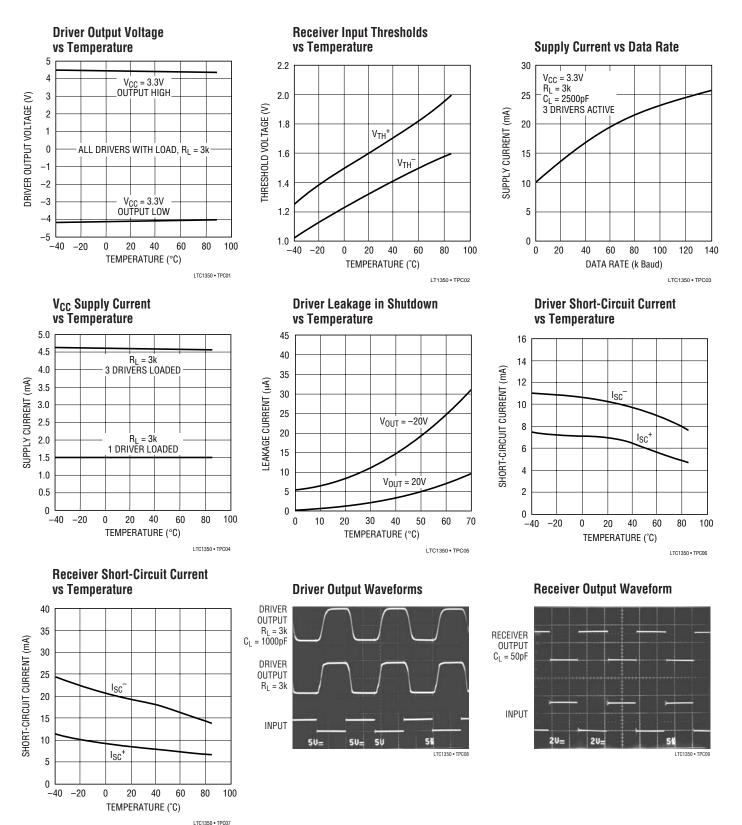
Note 2: Supply current is measured with driver and receiver outputs unloaded.

Note 3: Supply current measurement in Shutdown mode is performed with $V_{ON/OFF} = 0V$.



1350fa

TYPICAL PERFORMANCE CHARACTERISTICS



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PIN FUNCTIONS

 V_{CC} : 3.3V Input Supply Pin. Supply current is typically 35μ A in the Shutdown mode. This pin should be decoupled with a 0.1 μ F ceramic capacitor.

GND: Ground Pin.

ON/OFF: TTL/CMOS Compatible Shutdown Pin. A logic low puts the device in the Shutdown mode with receivers 4 and 5 kept alive and the supply current equal to 35μ A. All driver and other receiver outputs are in high impedance state. This pin cannot float.

V⁺: Positive Supply Output. V⁺ $\cong 2V_{CC} - 1V$. This pin requires an external capacitor (C = 0.1µF) for charge storage. The capacitor may be tied to ground or V_{CC}. With multiple devices, the V⁺ and V⁻ pins may be paralleled into common capacitors. For a large number of devices, increasing the size of the shared common storage capacitors is recommended to reduce ripple.

V⁻: Negative Supply Output. V⁻ \cong - (2V_{CC} - 1.3V). This pin requires an external capacitor (C = 0.1µF) for charge storage.

C1⁺, C1⁻, C2⁺, C2⁻: Commutating Capacitor Inputs. These pins require two external capacitors ($C = 0.1 \mu F$): one from C1⁺ to C1⁻ and another from C2⁺ to C2⁻. To maintain charge pump efficiency, the capacitor's effective series resistance should be less than 20Ω .

DR IN: EIA/TIA-562 Driver Input Pins. Inputs are TTL/ CMOS compatible. Inputs should not be allowed to float. Tie unused inputs to V_{CC} .

DR OUT: Driver Outputs at EIA/TIA-562 Voltage Levels. Outputs are in a high impedance state when in the Shutdown mode or $V_{CC} = 0V$. The driver outputs are protected against ESD to $\pm 10kV$ for human body model discharges.

RX IN: Receiver Inputs. These pins can be forced to $\pm 25V$ without damage. The receiver inputs are protected against ESD to $\pm 10kV$ for human body model discharges. Each receiver provides 0.4V of hysteresis for noise immunity.

RX OUT: Receiver Outputs with TTL/CMOS Voltage Levels. Receiver 1, 2 and 3 outputs are in a high impedance state when in Shutdown mode to allow data line sharing. Receivers 4 and 5 are kept alive in Shutdown.

SWITCHING TIME WAVEFORMS



Figure 1. Driver Propagation Delay Timing

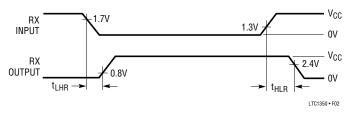
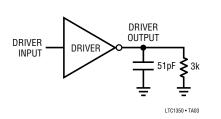


Figure 2. Receiver Propagation Delay Timing

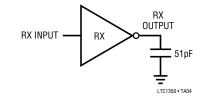


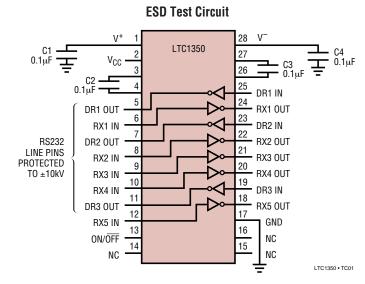
TEST CIRCUITS



Driver Timing Test Load

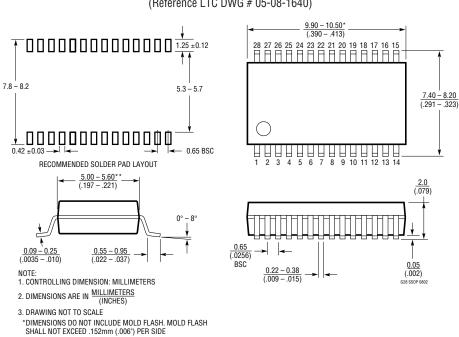
Receiver Timing Test Load







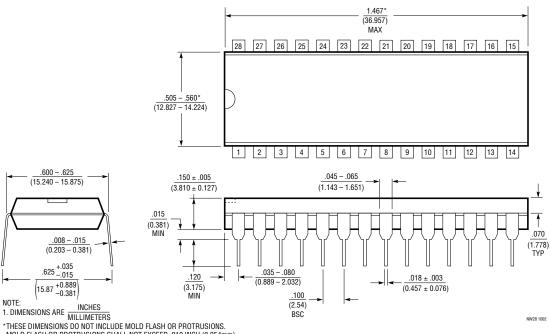
PACKAGE DESCRIPTION



G Package 28-Lead Plastic SSOP (5.3mm) (Reference LTC DWG # 05-08-1640)

NW Package 28-Lead PDIP (Wide .600 Inch) (Reference LTC DWG # 05-08-1520)

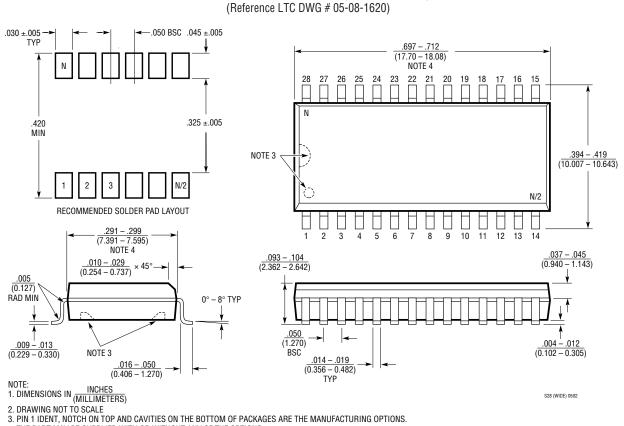
**DIMENSIONS DO NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED .254mm (.010") PER SIDE



MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)



PACKAGE DESCRIPTION



SW Package 28-Lead Plastic Small Outline (Wide .300 Inch)

THE PART MAY BE SUPPLIED WITH OR WITHOUT ANY OF THE OPTIONS 4. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .006" (0.15mm)

RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT®1137A	5V, 3 Driver, 5 Receiver RS232 Transceiver	±15kV ESD per IEC 1000-4
LTC1327	3.3V, 3 Driver, 5 Receiver RS562 Transceiver	300μA Supply Current, 0.2μA in Shutdown
LTC1337	5V, 3 Driver, 5 Receiver RS232 Transceiver	300µA Supply Current, 1µA in Shutdown
LTC1348	3.3V to 5V, 3 Driver, 5 Receiver RS232 Transceiver	True RS232 on 3.3V, 5 Receivers Active in Shutdown
LTC1385	3.3V, 2 Driver, 2 Receiver RS562 Transceiver	200µA Supply Current, 2 Receivers Active in Shutdown
LTC1386	3.3V, 2 Driver, 2 Receiver RS562 Transceiver	200µA Supply Current, Narrow 16-Pin SO
LTC2844	3.3V, Software-Selectable Multiprotocol Transceiver	4 Drivers, 4 Receivers for Control Signals Including LL
LTC2845	3.3V, Software-Selectable Multiprotocol Transceiver	5 Drivers, 5 Receivers for Control Signals Including LL, RL and TM
LTC2846	3.3V, Software-Selectable Multiprotocol Transceiver	4 Drivers, 4 Receivers with Termination for Data/Clock



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Analog Devices Inc.:

LTC1350ISW#TRPBF LTC1350CNW#PBF LTC1350CSW#TR LTC1350IG#PBF LTC1350CG#PBF LTC1350CSW#TRPBF LTC1350ISW LTC1350IG#TR LTC1350CG#TR LTC1350CG#TRPBF LTC1350IG#TRPBF LTC1350CG LTC1350CSW LTC1350ISW#PBF LTC1350CNW LTC1350CSW#PBF LTC1350IG LTC1350INW LTC1350ISW#TR