Switch Mode Power Rectifiers

These state-of-the-art devices have the following features:

Features

- Low Power Loss / High Efficiency
- New Package Provides Capability of Inspection and Probe After Board Mounting
- Guardring for Stress Protection
- Low Forward Voltage Drop
- 150°C Operating Junction Temperature
- Wettable Flacks Option Available
- NRVB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable*
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Mechanical Characteristics:

- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94–0 @ 0.125 in.
- Lead Finish: 100% Matte Sn (Tin)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Device Meets MSL 1 Requirements

Applications

- Output Rectification in Compact Portable Consumer Applications
- Freewheeling Diode used with Inductive Loads

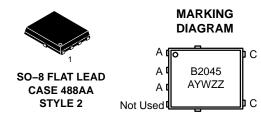


ON Semiconductor®

http://onsemi.com

SCHOTTKY BARRIER RECTIFIERS 20 AMPERES 45 VOLTS





B2045 = Specific Device Code A = Assembly Location

Y = Year
W = Work Week
ZZ = Lot Traceability

ORDERING INFORMATION

| Device | Package | Shipping† |
|-----------------|----------------------|-----------------------|
| MBR2045MFST1G | SO-8 FL (Pb-Free) | 1500 / Tape & Reel |
| NRVB2045MFST1G* | SO-8 FL (Pb-Free) | 1500 / Tape & Reel |
| MBR2045MFST3G | SO-8 FL (Pb-Free) | 5000 / Tape & Reel |
| NRVB2045MFST3G* | SO-8 FL (Pb-Free) | 5000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|--------------------------------------|-------------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage | V _{RRM} V _{RWM} | | V |
| DC Blocking Voltage | V_R | 45 | |
| Average Rectified Forward Current (Rated V _R , T _C = 130°C) | I _{F(AV)} | 20 | А |
| Peak Repetitive Forward Current, (Rated V _R , Square Wave, 20 kHz, T _C = 135°C) | I _{FRM} | 40 | А |
| Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz) | I _{FSM} | 400 | А |
| Storage Temperature Range | T _{stg} | -65 to +175 | °C |
| Operating Junction Temperature | TJ | -55 to +150 | °C |
| Unclamped Inductive Switching Energy (10 mH Inductor, Non-repetitive) | E _{AS} | 150 | mJ |
| ESD Rating (Human Body Model) | | 3B | |
| ESD Rating (Machine Model) | | M4 | |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

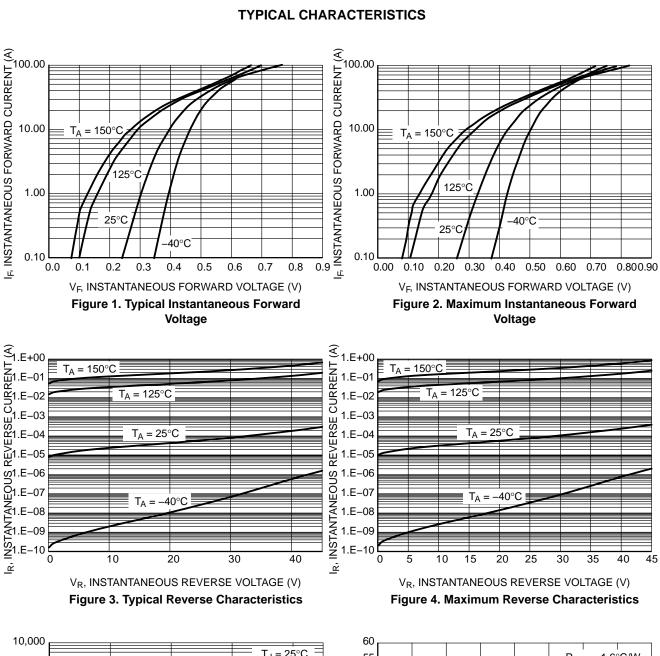
NOTE: The heat generated must be less than the thermal conductivity from Junction-to-Ambient: dPD/dTJ < 1/RJA

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Тур | Max | Unit |
|--|----------------|-----|-----|----------|
| Thermal Resistance, Junction-to-Case, Steady State (Assumes 600 mm² 1 oz. copper bond pad, on a FR4 board) | $R_{	heta JC}$ | - | 1.6 | °C/W |
| ELECTRICAL CHARACTERISTICS | . | - | - | <u>-</u> |
| Instantaneous Forward Voltage (Note 1) | VF | | | V |

| (i _F = 15 A, T _J = 125°C) | VF | 0.35 | 0.41 | V |
|---|----------------|------|------|----|
| $(i_F = 15 \text{ A}, T_J = 25^{\circ}\text{C})$ | | 0.44 | 0.49 | |
| $(i_F = 30 \text{ A}, T_J = 125^{\circ}\text{C})$ | | 0.46 | 0.58 | |
| $(i_F = 30 \text{ A}, T_J = 25^{\circ}\text{C})$ | | 0.51 | 0.61 | |
| Instantaneous Reverse Current (Note 1) | i _R | | | mA |
| (Rated dc Voltage, T _J = 125°C) | | 200 | 300 | |
| (Rated dc Voltage, T _J = 25°C) | | 0.3 | 0.6 | |

^{1.} Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.



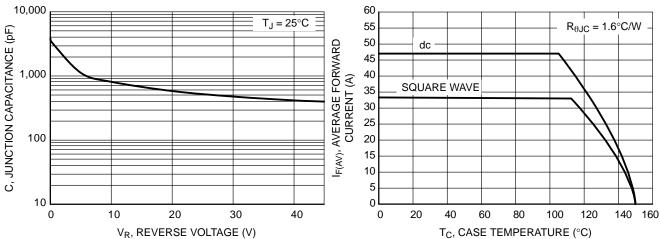
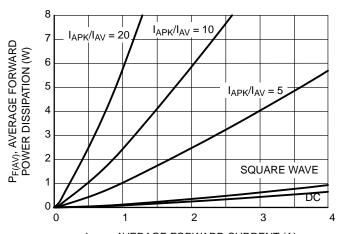


Figure 5. Typical Junction Capacitance

Figure 6. Current Derating TO-220AB

TYPICAL CHARACTERISTICS



 $I_{F(AV)}$, AVERAGE FORWARD CURRENT (A)

Figure 7. Forward Power Dissipation

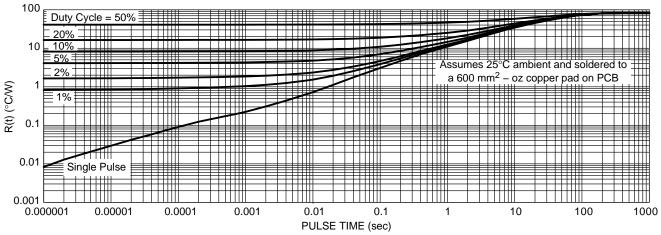
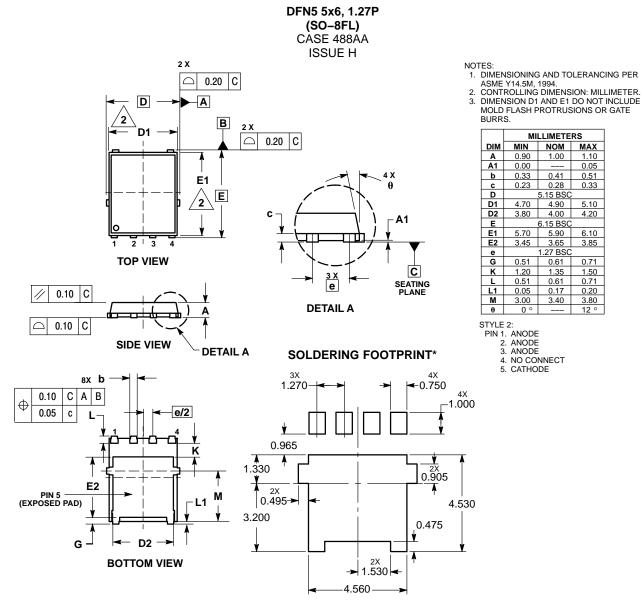


Figure 8. Thermal Response

PACKAGE DIMENSIONS



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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