

1. HIDDEN LINES ARE USED TO SHOW POLARIZATION WHEN USING -N OPTION. © REPRESENTS A CRITICAL DIMENSION. 3. FFSD CABLE ASSEMBLIES ARE LEAD FREE. 4. ALL ASSEMBLIES TO BE 100% ELECTRICALLY TESTED FOR SHORTS AND OPENS. 5. ALL ASSEMBLIES TO BE 100% HI-POT TESTED AT 630V.—(C) 6. STRAIN RELIEF TO WITHSTAND 5LB[22N] STRAIGHT PULL. 7. NOTE DELETED. DIMENSIONS AND ITS REQUIREMENTS 10. COLORED WIRE MUST MATCH FIRST POSITION INDICATOR ON THE FIRST CONNECTOR FOR ALL OPTIONS WITH THE EXCEPTION OF -RW. FOR LENGTHS LESS THAN 12.5", TOLERANCE SHALL BE +/-.125, ALL OTHER LENGTHS SHALL BE +/-1%. $\langle \mathbb{C} \rangle$ XX.XX[XX.X x 25.4]±1% (SEE NOTE 8 & 11)

.075 [1.91] **REF** .120 [3.05] **REF** (No OF POS x .050[1.27]) + .165[4.19]

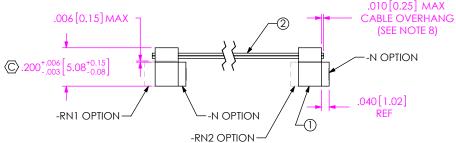


FIG 1 (FFSD-10-D-XX.XX-01-X-N SHOWN)

8. WHEN CUTTING CABLE TO LENGTH, CONDUCTORS SHOULD NOT EXTEND PAST THE CABLE INSULATION, EXTENDING LESS THAN 25% OF CABLE THICKNESS ON CONSECUTIVE AND LESS THAN 50% OF CABLE THICKNESS ON NON-CONSECUTIVE STRANDS IS CONSIDERED A PROCESS INDICATOR.

9. REFERENCE FFSD-XX-XX-X-N-XXX FOR CONNECTOR ASSEMBLY

No OF POSITIONS -02 THRU -25 (PER ROW) **END ASSEMBLY** -S: SINGLE END -D: DOUBLE END ASSEMBLY LENGTH -D"XX", -R, -M & -O OPTION: 2.00[50.8] MINIMUM ALL OTHER OPTION: 1.00[25.4] MINIMUM LEAD STYLE

FFSD-XX-X-XX.XX-01-X-X-XXX

PLATING OPTIONS

- LEAVE BLANK FOR STANDARD, USE C-67-L -S: USE C-67-S

-F: USE C-67-F

-RW: REVERSE WIRING (NOT AVAILABLE WITH -RN1 OR -RN2) ": DAISY CHAIN (MINIMUM 2.00[50.8], 1.00[25.4] INCREMENTS) -R: REVERSED (NOT AVAILABLE WITH -M OR -O) (MINIMUM 2.00[50.8], 1.00[25.4] INCREMENTS) -RN1: REVERSE POLARIZATION NOTCH (NOT AVAILABLE WITH -RW OR -RN2) -RN2: REVERSE POLARIZATION NOTCH (NOT AVAILABLE WITH -RW OR -RN1) -M: MIDDLE REVERSE (NOT AVAILABLE WITH -R OR -O) MINIMUM 2.00[50.8], 1.00[25.4] INCREMENTS) -O: OUTSIDE REVERSE (NOT AVAILABLE WITH -R OR -M) MINIMUM 2.00[50.8], 1.00[25.4] INCREMENTS) -SR: STRAIN RELIEF (NOT AVAILABLE WITH -O, -M, -R, OR -DXX) (SOLD AS ASSEMBLIES ONLY) STANDARD POLARIZATION NOTCH -N: POLARIZATION NOTCH

> $XX.XX[XX.X \times 25.4]\pm1\%$ (SEE NOTE 8 & 11)

(NOT AVAILABLE IN -02 THRU -04) (LEAVE BLANK FOR -02, -03, -04)

OPTION

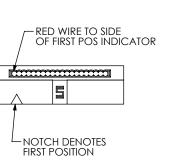


FIG 2 (FFSD-10-S-XX.XX-01-X-N SHOWN) (SAME AS FIG 1, DIFFERENT AS SHOWN)

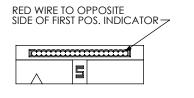


FIG 11 (FFSD-10-X-XX.XX-01-X-N-RW SHOWN)

TABLE 1	
ITEM #	PART
1	FFSD-XX-01-X-N-XX
2	CB-XX-30-7/38-025-GR
3	FFSD-XX-01-X-N-SR1
4	FFSR-XX-01
5	FFSD-XX-01-X-N-SR2
6	FFSR-XX-02

F:\DWG\MISC\MKTG\FFSD-XX-X-XX.XX-01-X-X-XXX-MKT.SLDDRW

UNLESS OTHERWISE SPECIFIED. DIMENSIONS ARE IN INCHES. TOLERANCES ARE: DECIMALS **ANGLES**

.XX: ±.01 [.3] .XXX: ±.005 [.13] .XXXX: ±.0020 [.051]

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DO NOT SCALE DRAWING SHEET SCALE: 2:1 INSULATORS: PBT UL94V-0, COLOR: BLACK CONTACT: BeCu WIRE: 30 AWG, 7/38, TINNED, STRANDED WITH GRAY PVC INSULATOR
PLATING: .000010 GOLD OVER .000050 NICKEL REMAINDER: .000200 REF TIN OVER .000050 NICKEL



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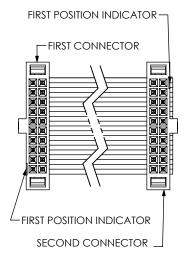
.050 x .050 DOUBLE ROW FEMALE IDC ASSEMBLY

FFSD-XX-X-XX.XX-01-X-X-XXX

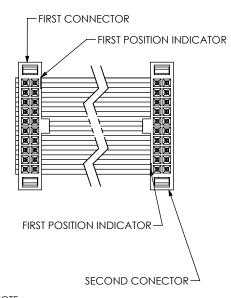
BY: T NEWTON 02/15/2008 SHEET 1 OF 4



FIG 3 (FFSD-10-D-XX.XX-01-X-N-RN1 SHOWN) (SAME AS FIG 1, DIFFERENT AS SHOWN)

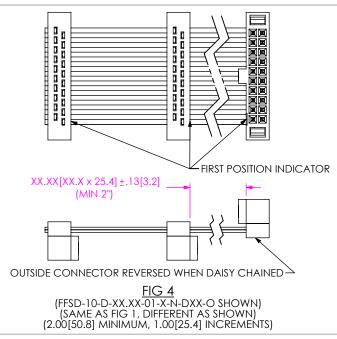


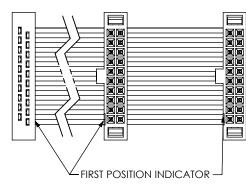
(FFSD-10-D-XX.XX-01-X-N-RN2 SHOWN) (SAME AS FIG 1, DIFFERENT AS SHOWN)



-RN1 = REVERSE NOTCH ON 1ST CONNECTOR ONLY
(NOT AVAILABLE WITH -R OPTION)
-RN2 = REVERSE NOTCH ON 2ND CONNECTOR ONLY
(NOT AVAILABLE WITH -R OPTION)

(REVERSE NOTCH NOT AVAILABLE WITH -RW OR ANY DAISY CHAIN OPTIONS)





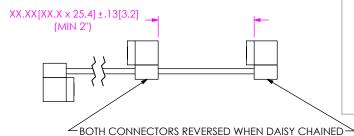
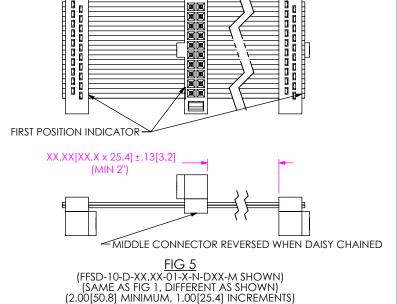
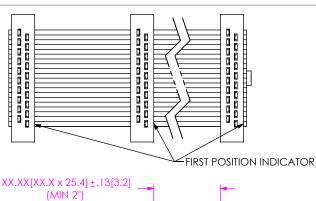


FIG 6 (FFSD-10-D-XX.XX-01-X-N-DXX-R SHOWN) (SAME AS FIG 1, DIFFERENT AS SHOWN) (NOT AVAILABLE WITH -RN1 OR -RN2 OPTION) (2.00[50.8] MINIMUM, 1.00[25.4] INCREMENTS)

F:\DWG\MISC\MKTG\FFSD-XX-X-XX.XX-01-X-X-XXX-MKT.SLDDRW





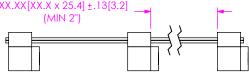


FIG 7 (FFSD-10-D-XX.XX-01-X-N-DXX SHOWN) (SAME AS FIG 1, DIFFERENT AS SHOWN) (2.00[50.8] MINIMUM, 1.00[25.4] INCREMENTS)

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.050 x .050 DOUBLE ROW FEMALE IDC ASSEMBLY

FFSD-XX-X-XX.XX-01-X-X-XXX

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