#### TOSHIBA PHOTOCOUPLER PHOTO RELAY

# **TLP3115**

## MEASUREMENT INSTRUMENTS LOGIC IC TESTERS / MEMORY TESTERS BOARD TESTERS / SCANNERS

The TOSHIBA TLP3115 Mini-flat photorelay is a small-outline photorelay, suitable for surface-mount assembly. The TLP3115 consists of an infrared-emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

The TLP3115 features low CR product and especially low On-state resistance, allowing high ON-state current.

Its characteristics also include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measuring instruments.

#### **Features**

• 4 pin SOP (2.54SOP4) : 2.1 mm high, 2.54 mm pitch

• 1-Form-A

Peak Off-State Voltage : 40 V (min)
 Trigger LED Current : 4 mA (max)
 On-State Current : 300 mA (max)

• On-State Resistance :  $1.5 \Omega$  (max),  $1.0 \Omega$  (typ.) • Output Capacitance : 14 pF (max), 10 pF (typ.)

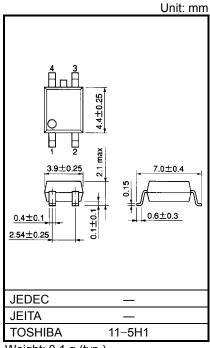
• Isolation Voltage : 1500 Vrms (min)

• UL-recognized : UL 1577, File No.E67349

• cUL-recognized : CSA Component Acceptance Service No.5A

File No.E67349

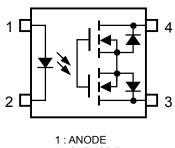
• VDE-approved : EN 60747-5-5 (Note 1)



Weight: 0.1 g (typ.)

Note 1: When a VDE approved type is needed, please designate the **Option(V4)**.

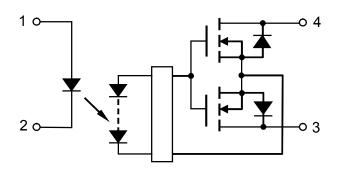
## Pin Configuration (top view)



2 : CATHODE 3 : DRAIN

4: DRAIN

#### **Schematic**



Start of commercial production 2000-11

#### Absolute Maximum Ratings (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	RATING	UNIT	
	Forward Current	lF	50	mA	
	Forward Current Derating (Ta ≥ 25°C)	ΔI <sub>F</sub> /°C	-0.5	mA/°C	
	Reverse Voltage	V <sub>R</sub>	5	V	
Ξ	Diode Power Dissipation	$P_D$	50	mW	
	Diode Power Dissipation Derating (Ta ≥25°C)	∆P <sub>D</sub> /°C	-0.5	mW/°C	
	Junction Temperature	Tj	125	°C	
DETECTOR	Off-State Output Terminal Voltage	Voff	40	٧	
	On-State Current	Ion	300	mA	
	On-State Current Derating (Ta ≥ 25°C)	Δlon/°C	-3.0	mA/°C	
Ĭ	Output Power Dissipation	Po	135	mW	
□	Output Power Dissipation Derating (Ta ≥ 25°C)	ΔP <sub>o</sub> /°C	-1.35	mW / °C	
	Junction Temperature	Tj	125	°C	
Stora	ge Temperature Range	T <sub>stg</sub>	-40 to 125	°C	
Opera	ating Temperature Range	Topr	-20 to 85	°C	
Lead	Soldering Temperature (10 s)	T <sub>sol</sub>	260	°C	
Isolat	ion Voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)	BVs	1500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: Pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

#### CAUTION

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

## **Recommended Operating Conditions**

CHARACTERISTIC	SYMBOL	MIN	TYP.	MAX	UNIT
Supply Voltage	$V_{DD}$	_	_	32	V
Forward Current	lF	10	_	30	mA
On-State Current	Ion	_	_	300	mA
Operating Temperature	T <sub>opr</sub>	25	_	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

#### Individual Electrical Characteristics (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
	Forward Voltage	VF	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
ГED	Reverse Current	IR	V <sub>R</sub> = 5 V	_	_	10	μА
	Capacitance between terminals	CT	V <sub>F</sub> = 0 V, f = 1 MHz	_	15	_	pF
CTOR	Off-State Current	loff	V <sub>OFF</sub> = 30 V, Ta = 50 °C	-	ı	1000	pА
DETECTOR	Capacitance between terminals	Coff	V = 0 V, f = 100 MHz, t < 1 s	_	10	14	pF



# **Coupled Electrical Characteristics (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Trigger LED Current	I <sub>FT</sub>	I <sub>ON</sub> = 100 mA	_	_	4	mA
Return LED Current	I <sub>FC</sub>	I <sub>OFF</sub> = 10 μA	0.2	0.75	_	mA
On-State Resistance	Ron	I <sub>ON</sub> = 300 mA, I <sub>F</sub> = 5 mA, t < 1 s	_	1.0	1.5	Ω

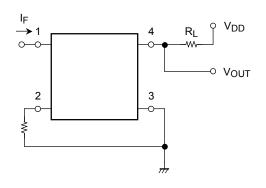
## **Isolation Characteristics (Ta = 25°C)**

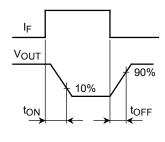
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance Input to Output	Cs	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation Resistance	Rs	V <sub>S</sub> = 500 V, R.H. ≤ 60 %	5 × 10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation Voltage	BVs	AC, 60 s	1500	_	_	Vrms

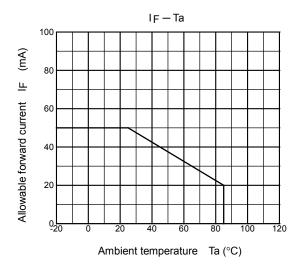
# **Switching Characteristics (Ta = 25°C)**

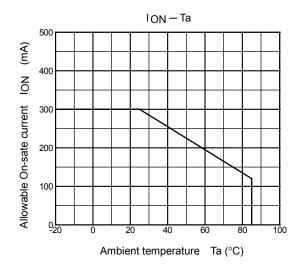
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Turn-on Time	ton	$R_L = 200 \Omega$ (NOTE 2	) –	_	500	0
Turn-off Time	toff	$V_{DD} = 10 \text{ V}, I_F = 10 \text{ mA}$	_	_	500	μS

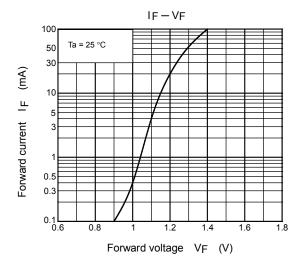
NOTE 2: SWITCHING TIME TEST CIRCUIT

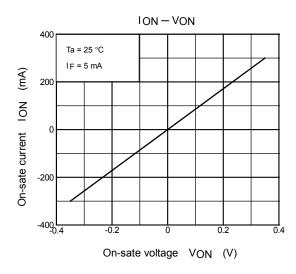


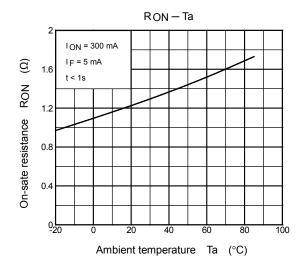


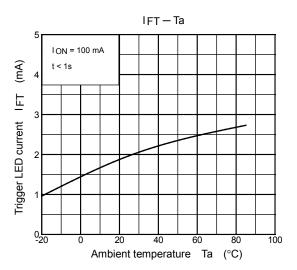




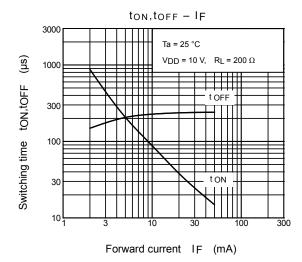


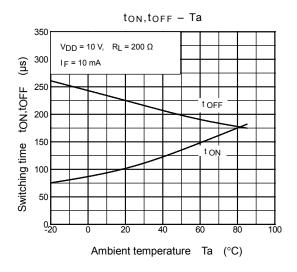


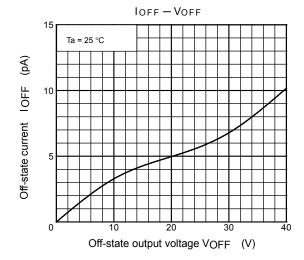


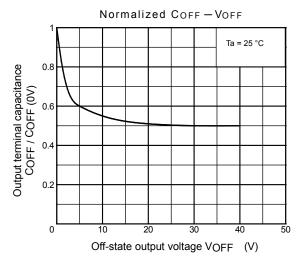


NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.









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