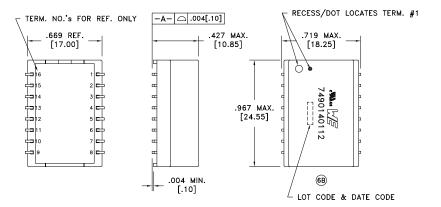
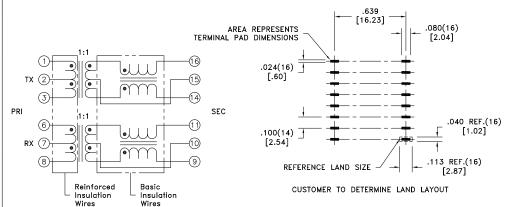
CUSTOMER TERMINAL RoHS LEAD(Pb)-FREE Sn100% Yes Yes





ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
INDUCTANCE	16-14	100kHz, 100mVAC, 8mADC, Lp	350uH min.
INDUCTANCE	11-9	100kHz, 100mVAC, 8mADC, Lp	350uH min.
DIELECTRIC	1-16	tie(1 thru 8, 9 thru 16), 7200Vrms, 1 second	6000Vrms, 1 minute
TURNS RATIO		(1-3):(16-14)	1:1, ±2%
TURNS RATIO		(6-8):(11-9)	1:1, ±2%
INSERTION LOSS		100k - 100MHz	-1.5dB max.
CROSSTALK		1M - 100MHz	−40dB min.



GENERAL SPECIFICATIONS: OPERATING TEMPERATURE RANGE: -40°C to +85°C.

COPLANARITY: All 16 terminals must lie on a plane within .004 [.10] of Surface A after lead tinning.

- Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:
 - Reinforced insulation for a primary circuit at a working voltage of 250Vrms.
- (6B) Designed to comply with 10mm min. creepage and 7mm min. clearance as defined by IEC60601-1; 2006.
 - Provide two means of patient protection up to a working voltage of 250Vrms.

AGENCY NUMBER UL60950-1 E205930

CSA60950-E205930 (Via CUL) PART NO.

REV.	DATE	Packaging Specifications	+ 1
		Method: Tape & Reel	(A)
		PKG-0670	Ψ
6B	9/15	www.we-online.com/midcom	CONVENTION PLACEMENT
6A	11/11	SEE REVISION SHEET FOR	REVISION LEVEL

Design using reinforced insulation wire on PRI side, and using basic insulation wire on SEC side.

ONVENTION PLACEMENT

Tolerances unless otherwise specified: Angles: ±1° Decimals: ±.005 [.13] Footprint: ±.005 [.13] Fractions: ±1/64

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

DRAWING TITLE

TRANSFORMER EDFP-J16

eiSos p/n: **7490140112**

compliant [
MROHS

7490140112

SPECIFICATION SHEET 1 OF 1

Mouser Electronics

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