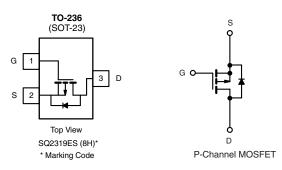
SQ2319ES



Vishay Siliconix

Automotive P-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	- 40			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -10 \text{ V}$	0.075			
$R_{DS(on)} (\Omega)$ at $V_{GS} = -4.5 V$	0.145			
I _D (A)	- 4.6			
Configuration	Single			



FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- AEC-Q101 Qualified^c
- 100 % $R_{\rm q}$ and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



KOHS COMPLIANT HALOGEN FREE

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and Halogen-free	SQ2319ES-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T _C	_c = 25 °C, unles	s otherwise noted)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	- 40	v
Gate-Source Voltage		V _{GS}	± 20	v
Continuous Drain Current	T _C = 25 °C	- I _D	- 4.6	
	T _C = 125 °C		- 2.6	
Continuous Source Current (Diode Conduction)		I _S	- 3.7	А
Pulsed Drain Current ^a		I _{DM}	- 18	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 12	
Single Pulse Avalanche Energy		E _{AS}	7.2	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	PD	3	W
	T _C = 125 °C		1	VV V
Operating Junction and Storage Temperature Range	ge	T _J , T _{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^b	R _{thJA}	166	°C/W
nction-to-Foot (Drain)		R _{thJF}	50	0/10

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$

b. When mounted on 1" square PCB (FR-4 material).

c. Parametric verification ongoing.

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	1					I	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} :	V _{GS} = 0, I _D = - 250 μA		-	-	v
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$		- 2.0	- 2.5	
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	± 100	nA
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	V _{DS} = - 40 V	-	-	- 1	
	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = -40 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	- 50	μA
		$V_{GS} = 0 V$	$V_{DS} = -40 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	- 150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 10 V	$V_{DS} \le$ - 5 V	- 10	-	-	Α
		V _{GS} = - 10 V	I _D = - 3 A	-	0.061	0.075	
Drain-Source On-State Resistance ^a		V _{GS} = - 10 V	I _D = - 3 A, T _J = 125 °C	-	-	0.116	
	R _{DS(on)}	V _{GS} = - 10 V	I _D = - 3 A, T _J = 175 °C	-	-	0.139	Ω
		V _{GS} = - 4.5 V	I _D = - 2.4 A	-	0.120	0.145	
Forward Transconductanceb	9 _{fs}	V _{DS} :	= - 5 V, I _D = - 3 A	-	8	-	S
Dynamic ^b		-			-		
Input Capacitance	C _{iss}		V _{GS} = 0 V V _{DS} = - 25 V, f = 1 MHz	-	493	620	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	76	95	
Reverse Transfer Capacitance	C _{rss}			-	51	65	
Total Gate Charge ^c	Qg		$V = V_{DS} = -20 V, I_{D} = -3 A$	-	10.5	16	nC
Gate-Source Charge ^c	Q _{gs}	V _{GS} = - 10 V		-	1.8	-	
Gate-Drain Charge ^c	Q _{gd}			-	2.6	-	
Gate Resistance	Rg	f = 1 MHz		5	10	15	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	5	8	
Rise Time ^c	t _r	= חח	$V_{DD} = -20 \text{ V}, \text{ R}_{\text{I}} = 6.7 \Omega$		11	17	- ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong -3$ A, $V_{GEN} = -10$ V, $R_g = 1 \Omega$		-	19	29	
Fall Time ^c	t _f			-	8	12	
Source-Drain Diode Ratings and Chara	acteristics ^b	•			•		
					-	10	^
Pulsed Current ^a	I _{SM}			-	-	- 18	A

Notes

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

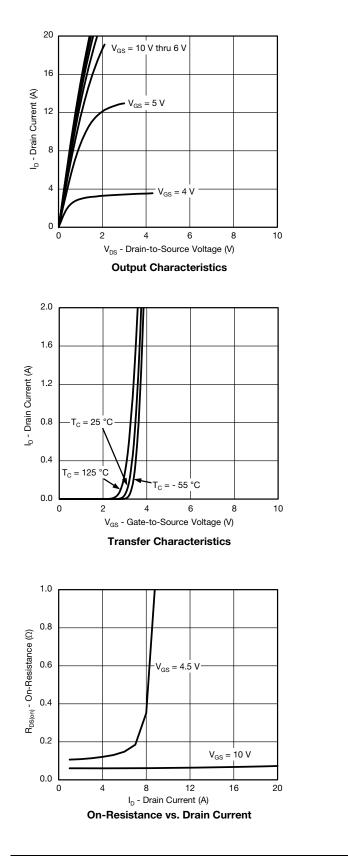
c. Independent of operating temperature.

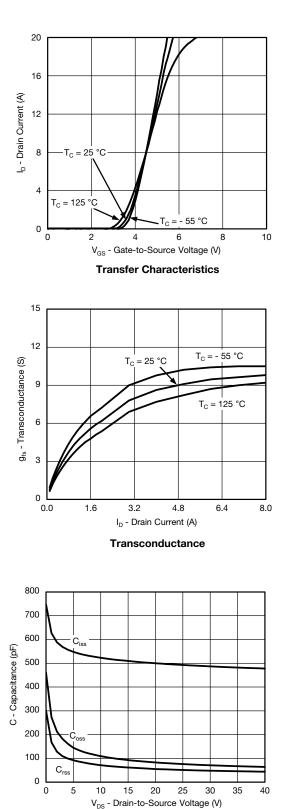
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)





Capacitance

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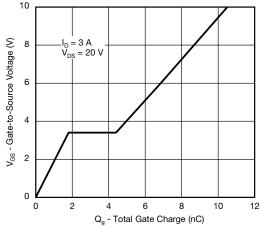
Document Number: 65735



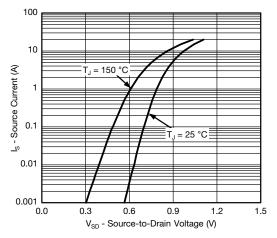
SQ2319ES

Vishay Siliconix

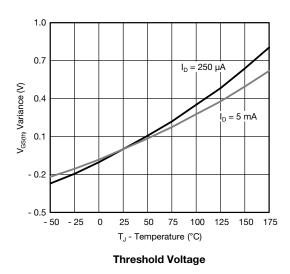
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

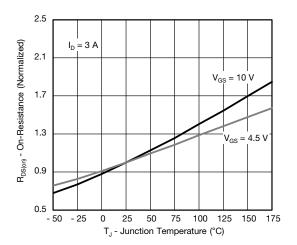


Gate Charge

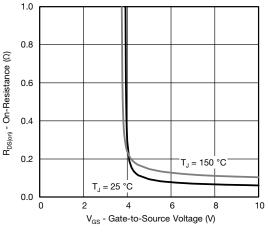


Source Drain Diode Forward Voltage

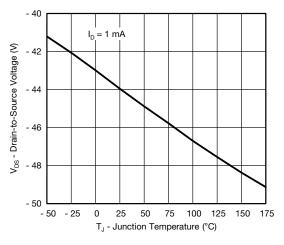




On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

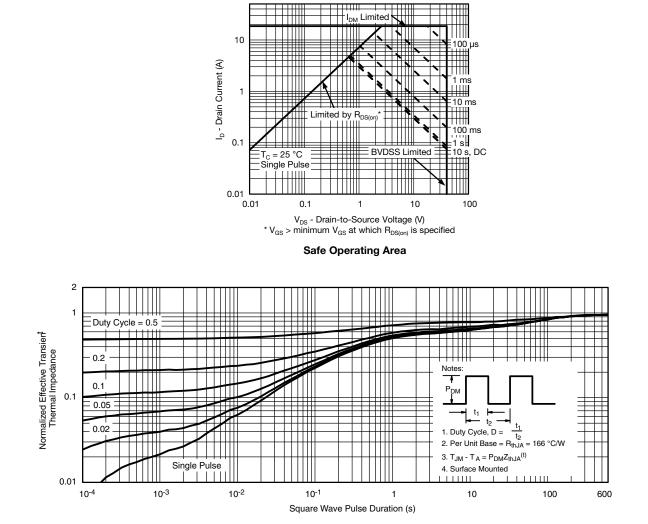
S11-2111-Rev. C, 07-Nov-11

Document Number: 65735



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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)

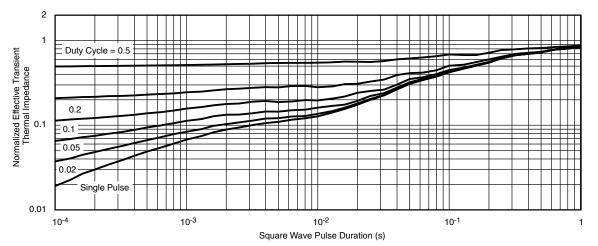


Normalized Thermal Transient Impedance, Junction-to-Ambient



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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?65735.



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