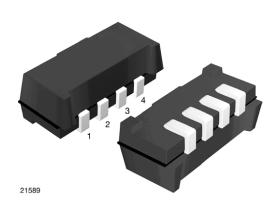


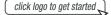
www.vishay.com

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



DESIGN SUPPORT TOOLS





MECHANICAL DATA

Pinning:

1, 4 = GND, $2 = V_S$, 3 = OUT

ORDERING CODE

Taping:

TSOP75...WTT - top view taped TSOP75...WTR - side view taped

FEATURES

- Very low supply current
- · Photo detector and preamplifier in one package
- Compatible also with short burst dataformats
- Supply voltage: 2.5 V to 5.5 V
- · Improved immunity against ambient light
- Capable of side or top view
- Low profile 2.35 mm
- Insensitive to supply voltage ripple and noise
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN

FREE

<u>(5-2008)</u>

DESCRIPTION

The TSOP753..W, TSOP755..W series are a miniaturized receiver module for infrared remote control systems. Two PIN diodes and a preamplifier are assembled on a leadframe, the epoxy package contains an IR filter.

The TSOP753..W..series is optimized to suppress almost all spurious pulses from energy saving lamps like CFLs. AGC3 may also suppress some data signals if continuously transmitted.

The TSOP755..W series contain a very robust AGC5. This series should only be used for critically noisy environments.

This component has not been qualified according to automotive specifications.

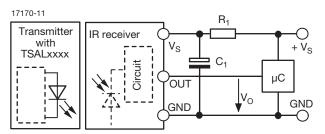
PARTS T	ABLE			
AGC		NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)	VERY NOISY ENVIRONMENTS AND SHORT BURSTS (AGC5)	
	30 kHz	TSOP75330W	TSOP75530W	
	33 kHz	TSOP75333W	TSOP75533W	
Carrier	36 kHz	TSOP75336W ⁽¹⁾	TSOP75536W	
frequency	38 kHz	TSOP75338W (2)(3)(4)(5)	TSOP75538W	
	40 kHz	TSOP75340W	TSOP75540W	
	56 kHz	TSOP75356W	TSOP75556W	
Package		Heimdal	l no lens	
Pinning		1, 4 = GND, 2	= V _S , 3 = OUT	
Dimensions (mm)		6.8 W x 3.0 H x 2.35 D		
Mounting		SN	MD	
Application		Remote control		
Best choice	pice for (1) MCIR (2) Mitsubishi (3) RECS-80 Code (4) r-map (5) XMP-1, XMP-2		Code (4) r-map (5) XMP-1, XMP-2	



BLOCK DIAGRAM

2 Vs 30 kΩ Vs 3 OUT OUT dulator dulator GND

APPLICATION CIRCUIT



 R_1 and C_1 recommended to reduce supply ripple for $V_S < 2.8 \text{ V}$

ABSOLUTE MAXIMUM RA	TINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Supply voltage		Vs	-0.3 to +6	V		
Supply current		I _S	3	mA		
Output voltage		Vo	-0.3 to (V _S + 0.3)	V		
Output current		I ₀	5	mA		
Junction temperature		T _i	100	°C		
Storage temperature range		T _{stg}	-25 to +85	°C		
Operating temperature range		T _{amb}	-25 to +85	°C		
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW		

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only
and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification
is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability

ELECTRICAL AND	OPTICAL CHARACTERISTICS (T _{amb} = 25	°C, unles	s otherw	se specif	ied)		
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Supply voltage		Vs	2.5	-	5.5	V	
Cupply ourrent	$E_{V} = 0, V_{S} = 3.3 \text{ V}$	I _{SD}	0.27	0.35	0.45	mA	
Supply current	$E_v = 40 \text{ klx, sunlight}$	I _{SH}	1	0.45	-	mA	
Transmission distance	$E_V = 0$, test signal see Fig. 1, IR diode TSAL6200, $I_F = 50$ mA	d	ı	16	-	m	
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2$, test signal see Fig. 1	V _{OSL}	-	-	100	mV	
Minimum irradiance	Pulse width tolerance: t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see Fig. 1	E _{e min.}	-	0.25	0.5	mW/m ²	
Maximum irradiance	t_{pi} - $5/f_o < t_{po} < t_{pi} + 6/f_o$, test signal see Fig. 1	E _{e max.}	30	-	-	W/m ²	
Directivity	Angle of half transmission distance	Ψ1/2	=	± 75	-	deg	

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

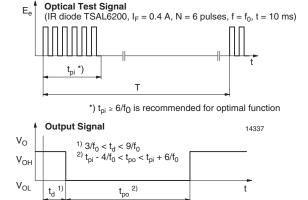


Fig. 1 - Output Active Low

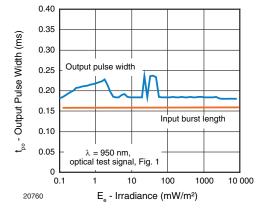


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

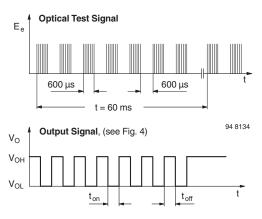


Fig. 3 - Output Function

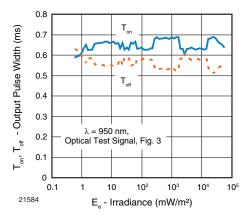


Fig. 4 - Output Pulse Diagram

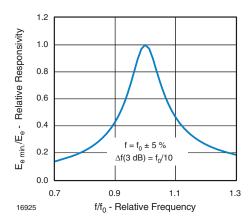


Fig. 5 - Frequency Dependence of Responsivity

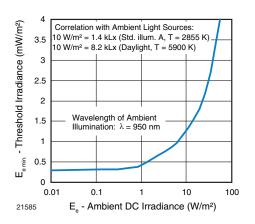


Fig. 6 - Sensitivity in Bright Ambient

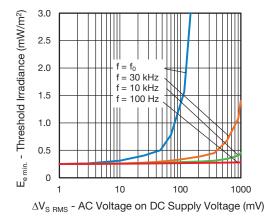


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

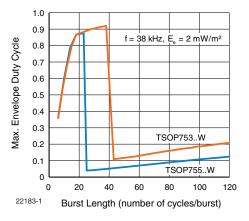


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

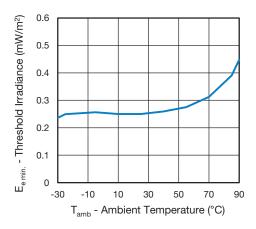


Fig. 9 - Sensitivity vs. Ambient Temperature

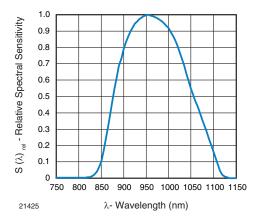


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

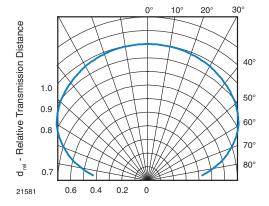


Fig. 11 - Horizontal Directivity

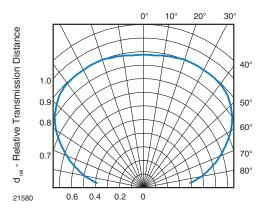


Fig. 12 - Vertical Directivity

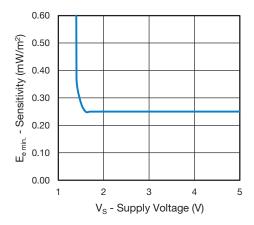


Fig. 13 - Sensitivity vs. Supply Voltage

SUITABLE DATA FORMAT

The TSOP753..W, TSOP755..W series are designed to suppress spurious output pulses due to noise or disturbance signals. Data and disturbance signals can be distinguished by the devices according to carrier frequency, burst length and envelope duty cycle. The data signal should be close to the band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP753..W, TSOP755..W in the presence of a disturbance signal, the sensitivity of the receiver is reduced to insure that no spurious pulses are present at the output. Some examples of disturbance signals which are suppressed are:

- DC light (e.g. from tungsten bulb or sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated pattern from fluorescent lamps with electronic ballasts (see Fig. 14 or Fig. 15)

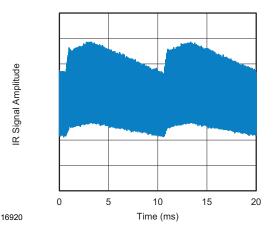


Fig. 14 - IR Disturbance from Fluorescent Lamp With Low Modulation

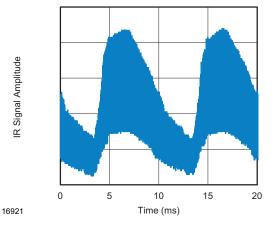


Fig. 15 - IR Disturbance from Fluorescent Lamp With High Modulation

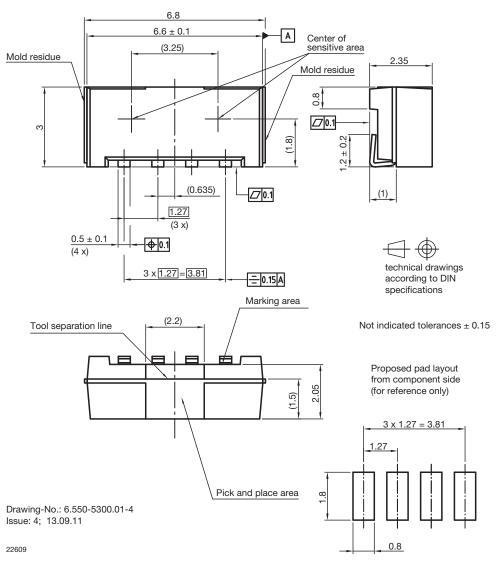
	TSOP753W	TSOP755W
Minimum burst length	6 cycles/burst	6 cycles/burst
After each burst of length a minimum gap time is required of	6 to 35 cycles ≥ 10 cycles	6 to 24 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	35 cycles > 4 x burst length	24 cycles > 25 ms
Maximum number of continuous short bursts/second	2000	2000
MCIR code	Preferred	Yes
RCMM code	Preferred	Yes
XMP-1, XMP-2, code	Preferred	Yes
Suppression of interference from fluorescent lamps	Mild and complex disturbance patterns are suppressed (example: signal pattern of Fig. 14 and Fig. 15)	Critical disturbance patterns are suppressed, e.g. highly dimmed LCDs

Note

• For data formats with long bursts please see the datasheet for TSOP752..W, TSOP754..W



PACKAGE DIMENSIONS in millimeters



ASSEMBLY INSTRUCTIONS

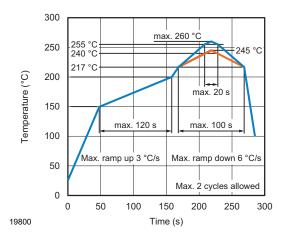
Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

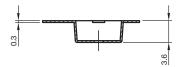
Manual Soldering

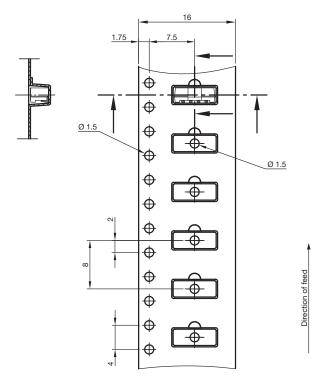
- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- · Handle products only after the temperature has cooled off

VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



TAPING VERSION TSOP..TR DIMENSIONS in millimeters

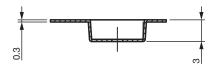


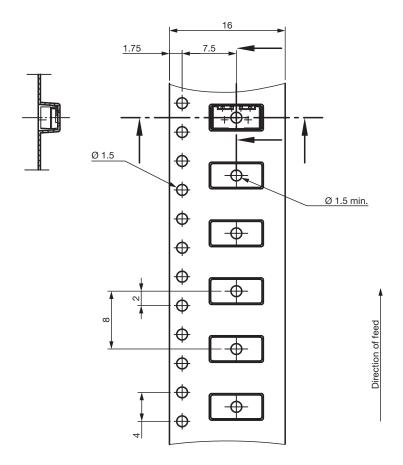


Drawing-No.: 9.700-5342.01-4 Issue: 2; 12.06.13



TAPING VERSION TSOP..TT DIMENSIONS in millimeters



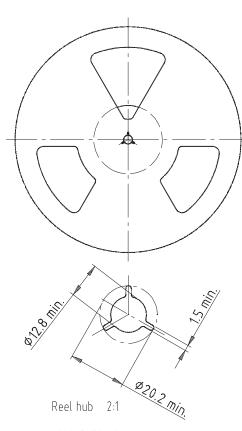


Drawing-No.: 9.700-5341.01-4

Issue: 3; 06.10.15

technical drawings according to DIN specifications

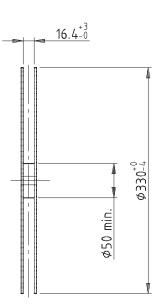
REEL DIMENSIONS in millimeters



Drawing-No.: 9.800-5052.V2-4

Issue: 1; 07.05.02

16734



Form of the leave open of the wheel is supplier specific.

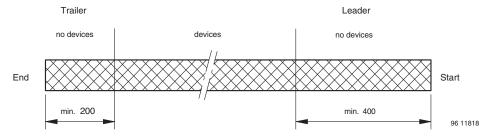
Dimension acc. to IEC EN 60 286-3

Tape width 16



technical drawings according to DIN specifications

LEADER AND TRAILER DIMENSIONS in millimeters



COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N $300 \pm 10 \text{ mm/min.}$ $165^{\circ} \text{ to } 180^{\circ} \text{ peel angle}$

LABEL

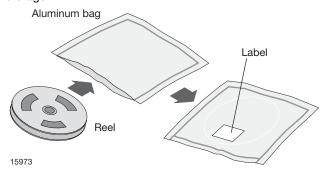
Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITING	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	PCK	-	
Mixed code indicator	MIXED CODE	-	
Origin	xxxxxx+	Company logo	
Long bar code top	Туре	Length	
Item-number	N	8	
Plant-code	N	2	
Sequence-number	X	3	
Quantity	N	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	X	3	
Data-code	N	3	
Batch-number	X	10	
Filter	-	1	
Total length	-	17	

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

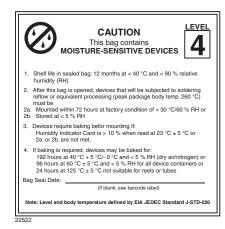
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard J-STD-020 level 4 label is included on all dry bags.



EIA JEDEC standard J-STD-020 level 4 is included on all dry bags



TSOP753..W, TSOP755..W

Vishay Semiconductors

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

BAR CODE PRODUCT LABEL (example)



22178



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Vishay

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