



ON Semiconductor®

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CPH6121

Bipolar Transistor -12V, -3A, Low $V_{CE(sat)}$, PNP Single CPH6

Applications

- Relay drivers, lamp drivers, motor drivers, strobes

Features

- Adoption of MBIT process
- High current capacitance
- Low collector-to-emitter saturation voltage
- High speed switching
- Ultrasmall-sized package permitting applied sets to be made small and slim (0.9mm)
- High allowable power dissipation

Specifications

Absolute Maximum Ratings at $T_a=25^\circ\text{C}$

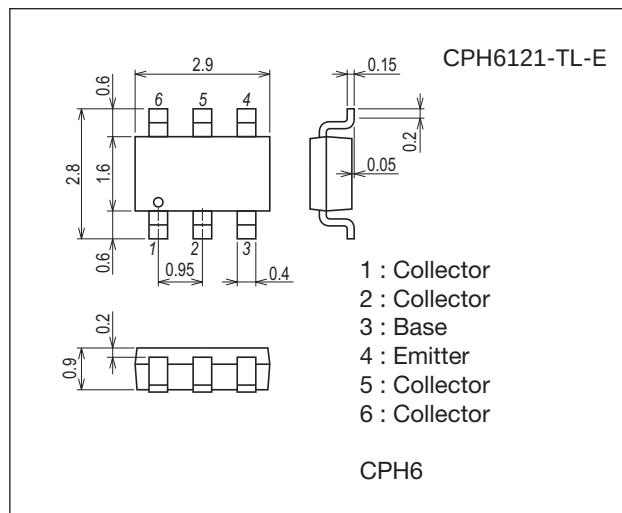
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CBO}		-15	V
Collector-to-Emitter Voltage	V_{CEO}		-12	V
Emitter-to-Base Voltage	V_{EBO}		-5	V
Collector Current	I_C		-3	A
Collector Current (Pulse)	I_{CP}		-5	A
Base Current	I_B		-600	mA
Collector Dissipation	P_C	Mounted on a ceramic board (600mm ² ×0.8mm)	1.3	W
Junction Temperature	T_j		150	°C
Storage Temperature	T_{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

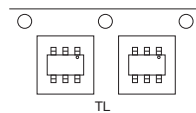
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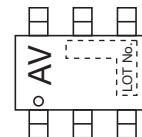
Product & Package Information

- Package : CPH6
- JEITA, JEDEC : SC-74, SOT-26, SOT-457
- Minimum Packing Quantity : 3,000 pcs./reel

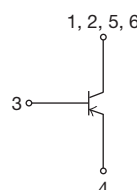
Packing Type: TL



Marking



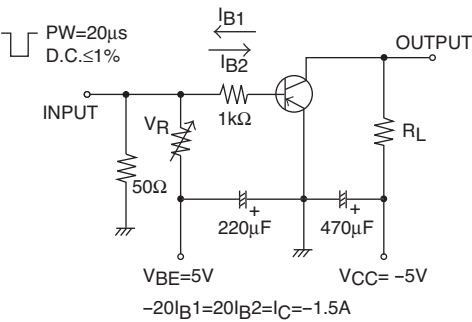
Electrical Connection



Electrical Characteristics at Ta=25°C

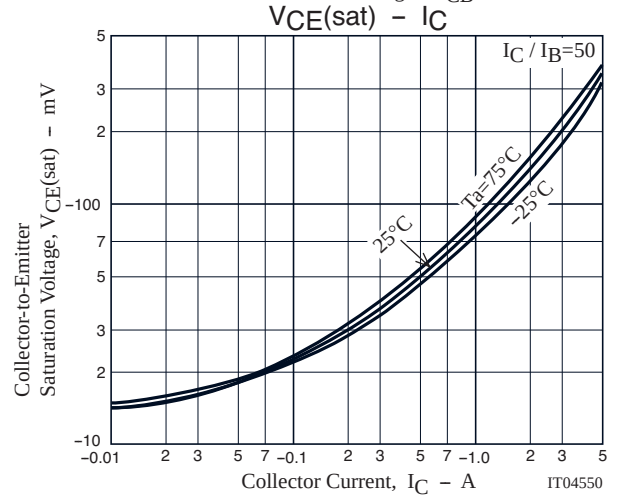
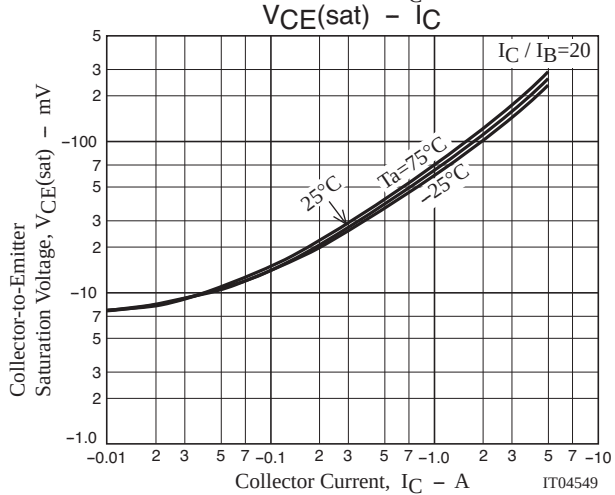
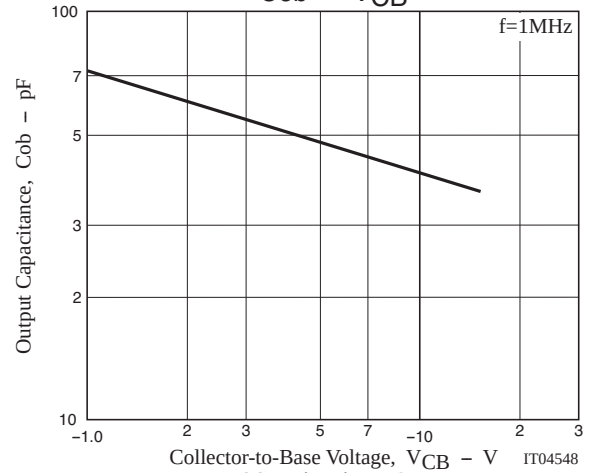
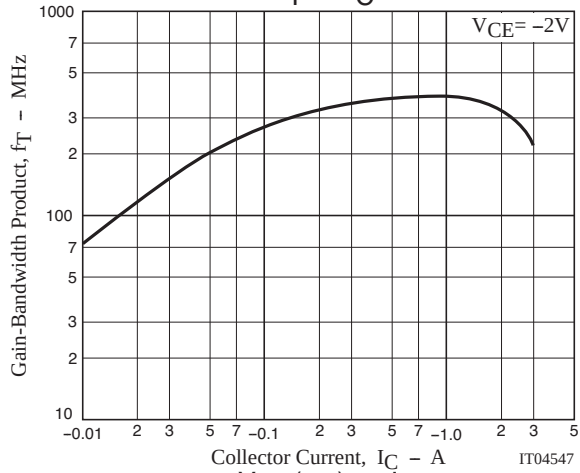
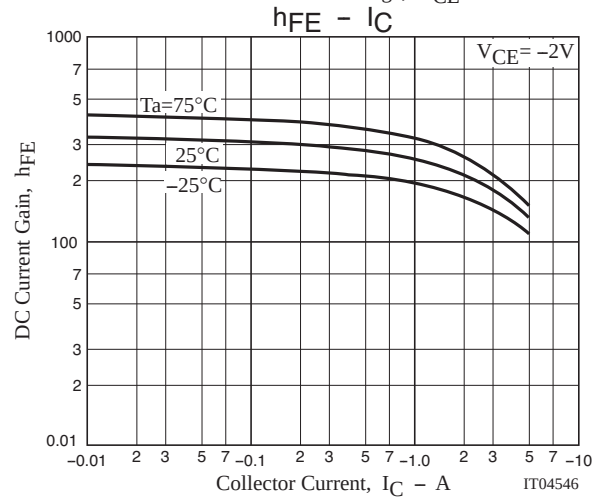
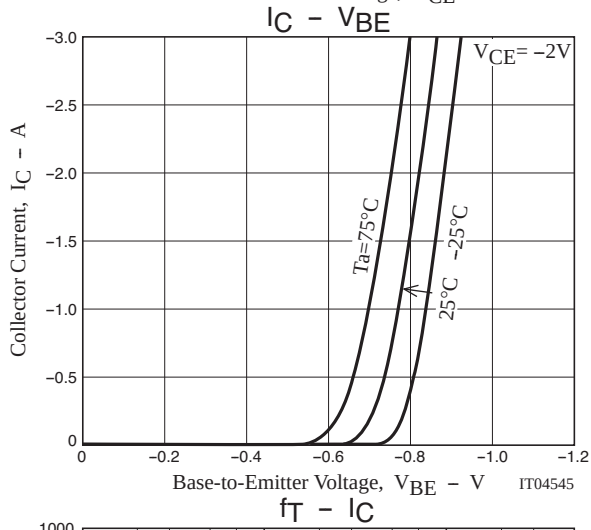
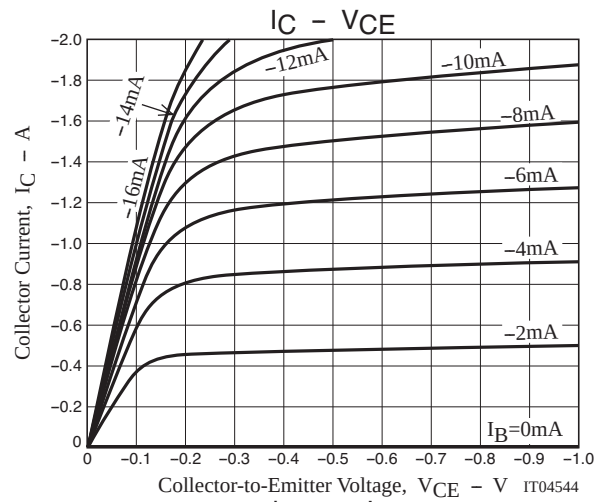
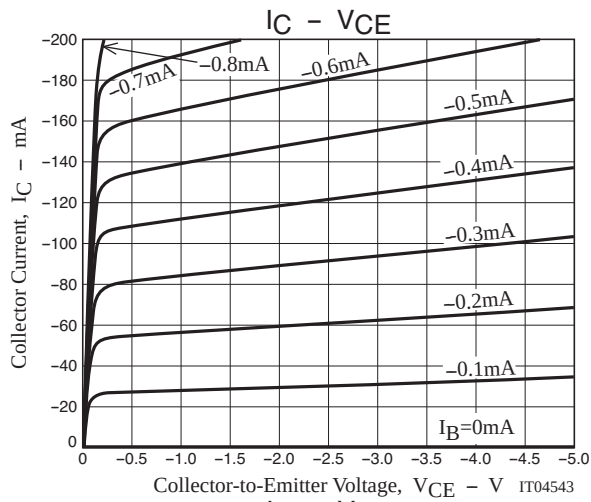
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	ICBO	V _{CB} = -12V, I _E =0A			-0.1	μA
Emitter Cutoff Current	IEBO	V _{EB} = -4V, I _C =0A			-0.1	μA
DC Current Gain	h _{FE}	V _{CE} = -2V, I _C = -500mA	200		560	
Gain-Bandwidth Product	f _T	V _{CE} = -2V, I _C = -500mA		380		MHz
Output Capacitance	Cob	V _{CB} = -10V, f=1MHz		40		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C = -1.5A, I _B = -30mA		-110	-165	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C = -1.5A, I _B = -30mA		-0.85	-1.2	V
Collector-to-Base Breakdown Voltage	V _{(BR)CBO}	I _C = -10μA, I _E =0A	-15			V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C = -1mA, R _{BE} =∞	-12			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E = -10μA, I _C =0A	-5			V
Turn-On Time	t _{on}	See specified Test Circuit.		30		ns
Storage Time	t _{stg}			90		ns
Fall Time	t _f			14		ns

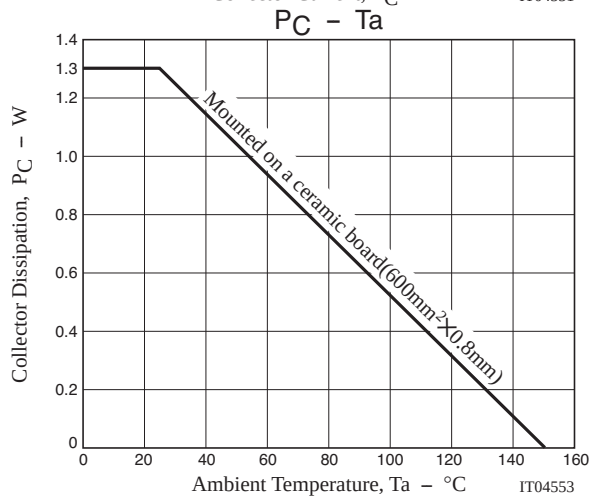
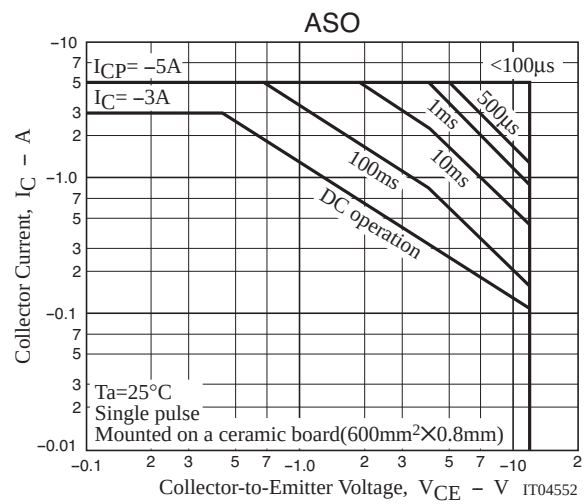
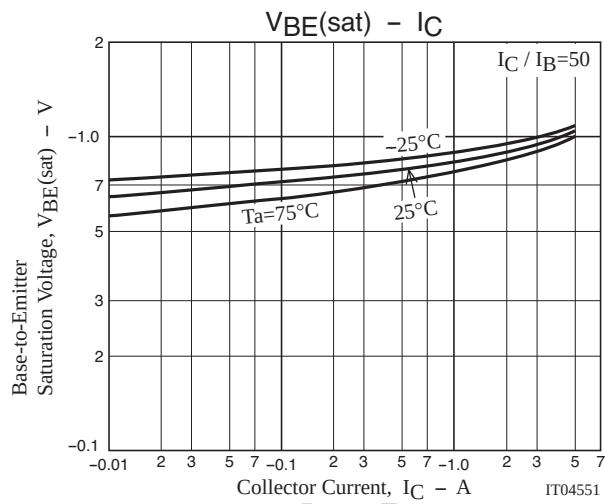
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
CPH6121-TL-E	CPH6	3,000pcs./reel	Pb Free





Embossed Taping Specification

CPH6121-TL-E

1. Packing Format

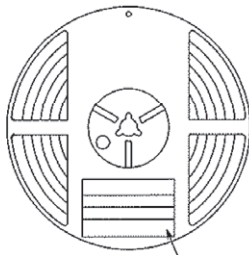
Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
CPH6	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Reel label, Inner box label
(unit:mm)

Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

Packing method



Reel label

Type No.
LOT No.
Quantity
Origin

TYPE	000000000
LOT	00
QTY	0,000 (1) LEAD FREE *
SPECIAL	*20722005310C*
ASSEMBLY	**** (DIFFUSION:****)

NOTE (1)

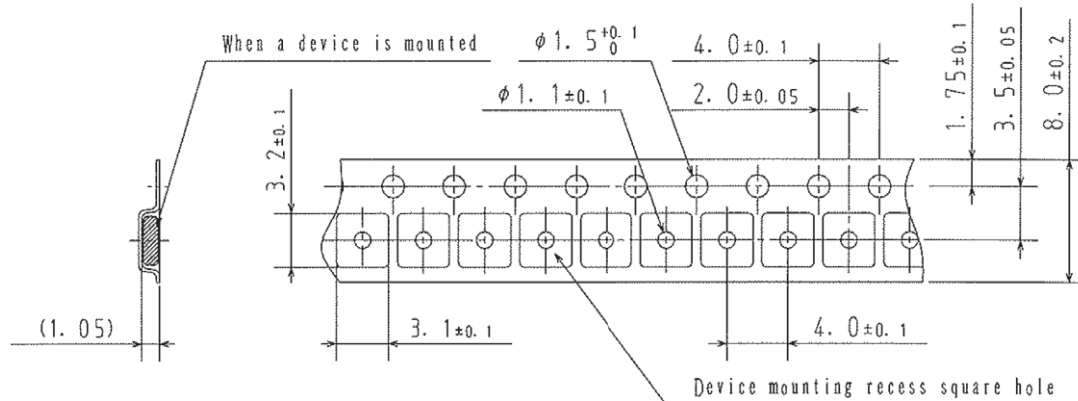
The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

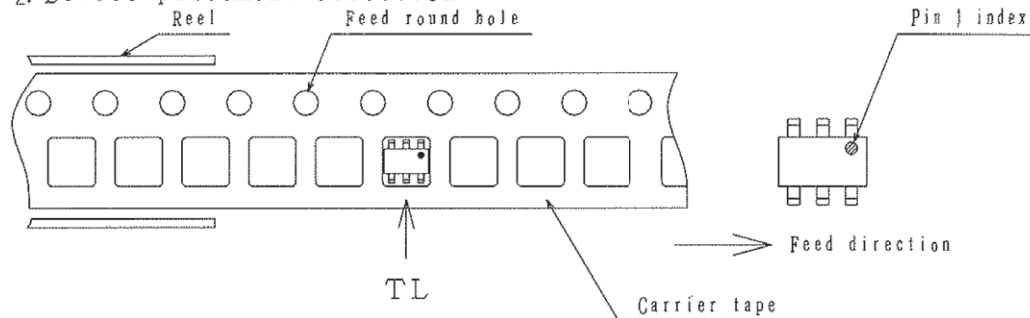
TYPE CODE	*****
TYPE	*****
QTY	0,000 PCS (1) LEAD FREE *
LOT	*****
PACKAGE	*****
SPECIAL	*20722005310C*
ASSEMBLY	**** (DIFFUSION:****)

2. Taping configuration

2-1. Carrier tape size (unit:mm)

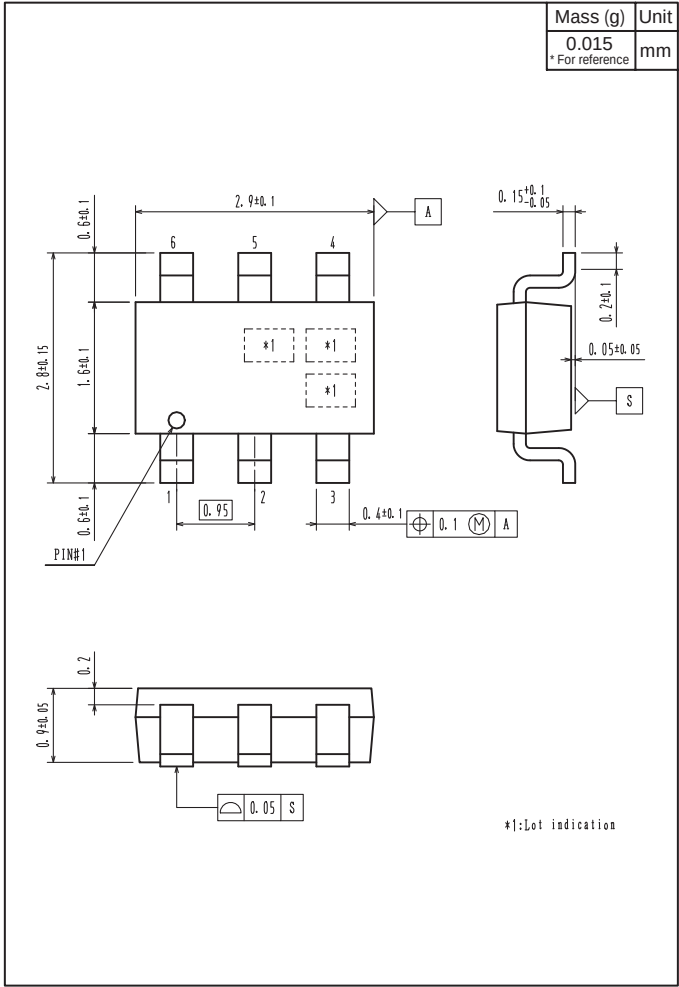


2-2. Device placement direction

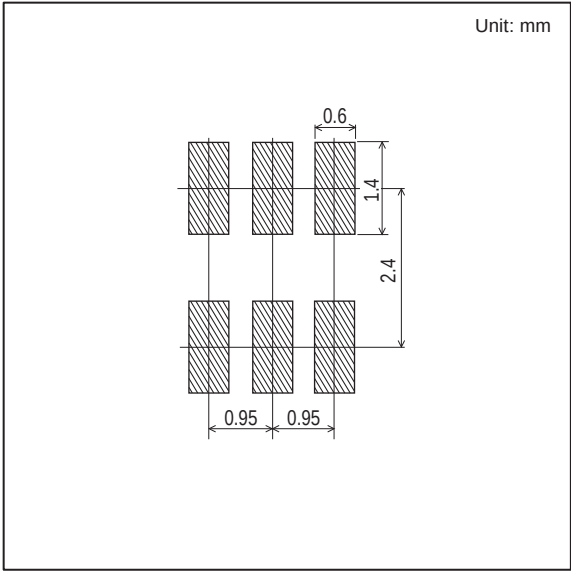


Those with pin 1 index on the feed hole side.....TL

Outline Drawing
CPH6121-TL-E



Land Pattern Example



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