

20V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	Max R _{DS(on)} (Note 6)	Max I _D T _A = 25°C
	$60m\Omega @ V_{GS} = -4.5V$	-4.23A
-20V	90m $Ω @ VGS = -2.5V$	-3.49A
	113m Ω @ V _{GS} = -1.8V	-3.11A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Motor Control
- Power management functions
- Analog Switch

Features

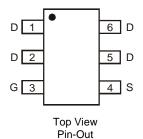
- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

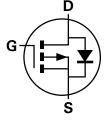
Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.0013 grams (approximate)









Equivalent Circuit

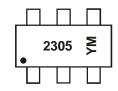
Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMP2305UVT-7	2305	7	8	3,000
DMP2305UVT-13	2305	13	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



2305 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date Code Key

Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Υ		Z		Α	E	3	С		D		Е
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	-20	V		
Gate-Source Voltage	V_{GSS}	±8	V		
Continuous Drain Current (Note 6) V _{GS} = -4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-4.23 -2.98	А
Continuous Drain Current (Note 6) $V_{GS} = -2.5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		I _D	-3.49 -2.79	А	
Maximum Continuous Body Diode Forward Current	I _S	-4.23	Α		
Pulsed Drain Current (10μs pulse, duty cycle = 1%	I _{DM}	-16	Α		

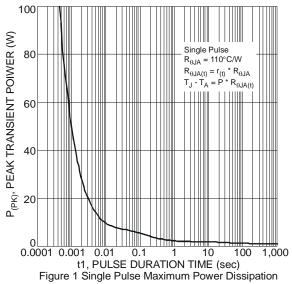
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

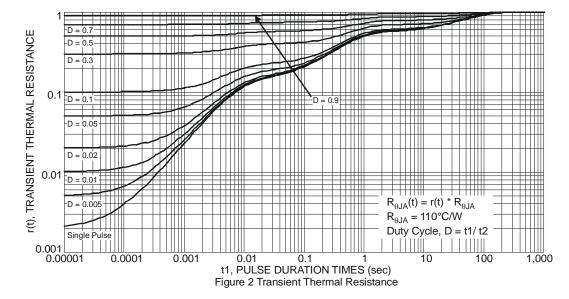
Characteristic	Symbol	Value	Units		
Total Bower Dissipation	(Note 5)	В	1.25	W	
Total Power Dissipation	(Note 6)	P _D	1.64		
Thermal Desigtance Junction to Ambient	(Note 5)		100	,	
Thermal Resistance, Junction to Ambient	(Note 6)	R_{\thetaJA}	76	°C/W	
Thermal Resistance, Junction to Case	(Note 6)	$R_{ heta JC}$	14		
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C		

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:



Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)









Electrical Characteristics @T_A = 25°C unless otherwise specified

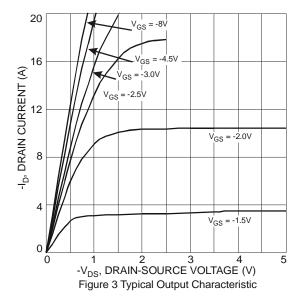
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_		V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}			-1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_		±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	-0.5	_	-0.9	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
			45	60		$V_{GS} = -4.5V$, $I_D = -4.2A$
Static Drain-Source On-Resistance	R _{DS} (ON)		60	90	mΩ	$V_{GS} = -2.5V$, $I_D = -3.4A$
		_	87	113		$V_{GS} = -1.8V, I_D = -2.0A$
Forward Transfer Admittance	Y _{fs}	_	9	_	S	$V_{DS} = -5V, I_{D} = -4A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}		727	_		V 20V V 0V
Output Capacitance	Coss		69		pF	$V_{DS} = -20V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	64	_		1 – 1.000112
Gate Resistance	R_{G}		23		Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$
Total Gate Charge	Qg		7.6	_		
Gate-Source Charge	Q_{gs}		1.4	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -4V$, $I_{D} = -3.5A$
Gate-Drain Charge	Q_{gd}		1.2			
Turn-On Delay Time	t _{D(on)}	_	14.0			
Turn-On Rise Time	t _r		13.0	_	ns	$V_{DS} = -4V$, $V_{GS} = -4.5V$,
Turn-Off Delay Time	t _{D(off)}		53.8		118	$R_L = 4\Omega$, $R_G = 6\Omega$, $I_D = -1A$
Turn-Off Fall Time	t _f		23.2	_		

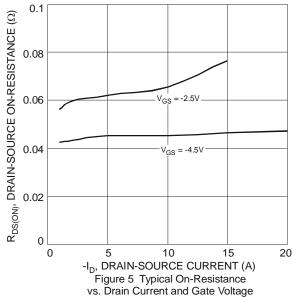
Notes:

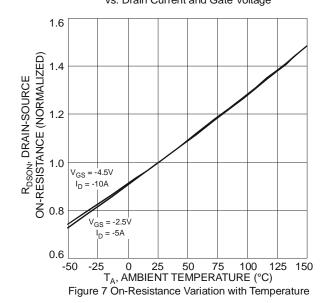
^{7.} Short duration pulse test used to minimize self-heating effect.

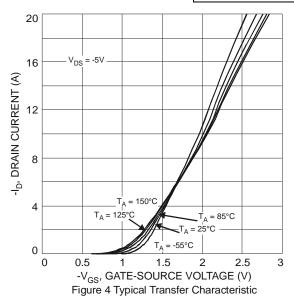
^{8.} Guaranteed by design. Not subject to product testing.

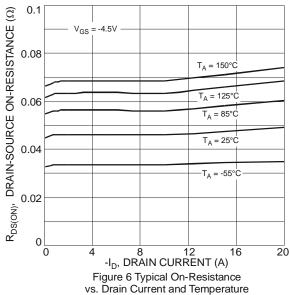


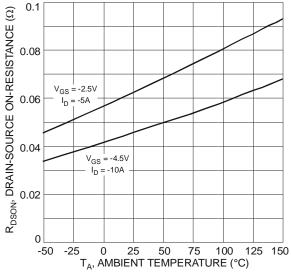














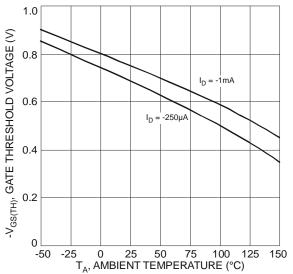
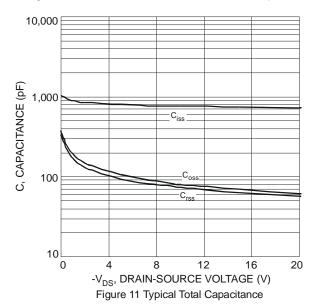
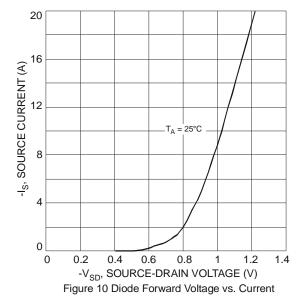


Figure 9 Gate Threshold Variation vs. Ambient Temperature





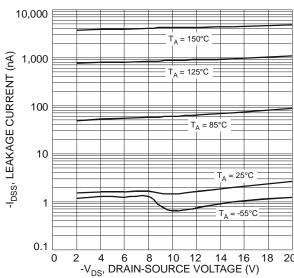
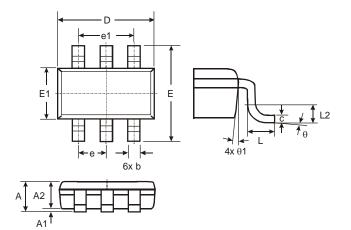


Figure 12 Typical Leakage Current vs. Drain-Source Voltage



Package Outline Dimensions

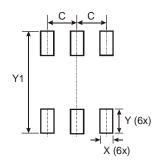
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	TSOT26						
Dim	Min	Max	Тур				
Α	-	1.00	1				
A1	0.01	0.10	-				
A2	0.84	0.90	1				
D	_	-	2.90				
Е	_	_	2.80				
E1	_	-	1.60				
b	0.30	0.45	1				
С	0.12	0.20	-				
e	-	_	0.95				
e1	-	-	1.90				
L	0.30	0.50					
L2	-	_	0.25				
θ	0°	8°	4°				
θ1	4°	12°	-				
All D	All Dimensions in mm						

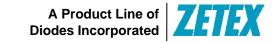
Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.950
Х	0.700
Υ	1.000
Y1	3 199





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