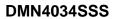




A Product Line of Diodes Incorporated



40V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D T _A = 25°C		
40V	$34m\Omega @ V_{GS} = 10V$	7.2A		
	59mΩ @ V_{GS} = 4.5V	5.5A		

Description and Applications

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor control
- Backlighting
- DC-DC Converters
- Power management functions

Features and Benefits

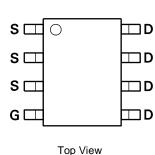
- 100% Unclamped Inductive Switch (UIS) test in production
- Low on-resistance
- Fast switching speed
- Max Q_g rated
- "Green" component and RoHS compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability

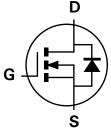
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.074 grams (approximate)



Top View





Equivalent Circuit

Ordering Information (Note 1)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel		
DMN4034SSS-13	N4034SS	13	12	2,500		

Note: 1. Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds; further information about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.

Marking Information



Cili = Manufacturer's Marking N4034SS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 09 = 2009) WW = Week (01 - 53)



Maximum Ratings @T_A = 25°C unless otherwise specified

	Characteristic		Symbol	Value	Unit	
Drain-Source voltage			V _{DSS}	40	V	
Gate-Source voltage (Note 2)			V _{GS}	±20	V	
Single Pulsed Avalanche Energy		(Note 7)	E _{AS}	27	mJ	
Single Pulsed Avalanche Current		(Note 7)	I _{AS}	15.25	A	
		(Note 4)		7.2		
Continuous Drain current	$V_{GS} = 10V$	$T_A = 70^{\circ}C$ (Note 4)	ID	5.8	A	
		(Note 3)		5.4		
Pulsed Drain current	ed Drain current V _{GS} = 10V (Note 5)		I _{DM}	33.0	A	
Continuous Source current (Body diode)		(Note 4)	Is	4.1	A	
Pulsed Source current (Body diode)		(Note 5)	I _{SM}	33.0	A	

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Power dissipation	(Note 3)		1.56 12.5	W
Linear derating factor	(Note 4)	P _D	2.8 22.5	mW/°C
Thermal Desistance Junction to Ambient	(Note 3)		80	
Thermal Resistance, Junction to Ambient	(Note 4)	R _{0JA}	44.5	°C/W
Thermal Resistance, Junction to Lead	(Note 6)	R _{θJL}	37	
Operating and storage temperature range		T _J , T _{STG}	-55 to 150	٥C

Notes:

2. AEC-Q101 V_{SS} maximum is ±16V. 3. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

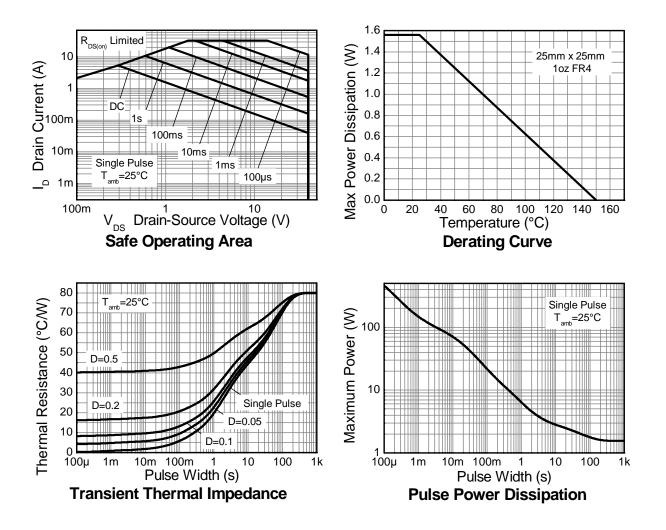
4. Same as note (3), except the device is measured at t \leq 10 sec.

5. Same as note (3), except the device is pulsed with D = 0.02 and pulse width 300μ s. The pulse current is limited by the maximum junction temperature. 6. Thermal resistance from junction to solder-point (at the end of the drain lead).

7. UIS in production with L = 100 μ H, V_{DD} = 40V.



Thermal Characteristics





Electrical Characteristics @T_A = 25°C unless otherwise specified

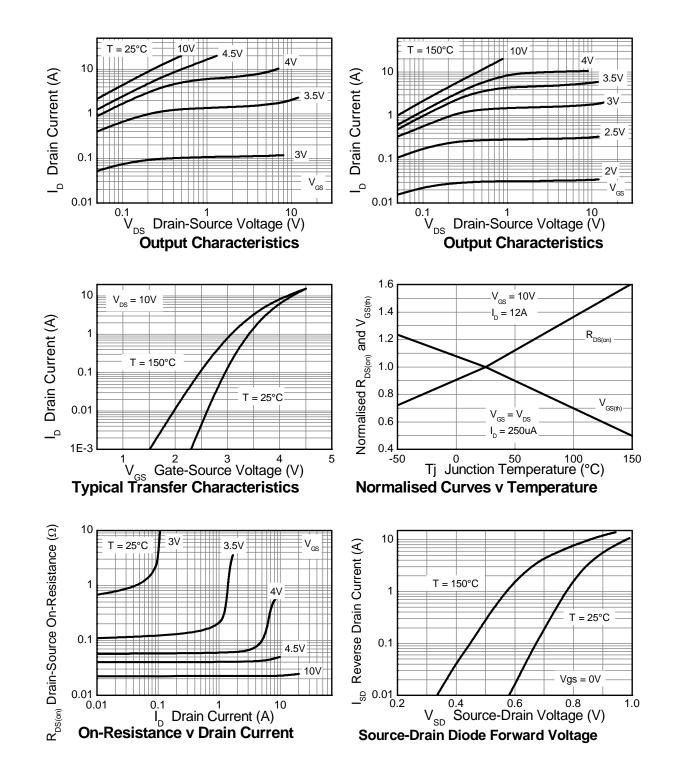
Characteristic	Symbol	Min	Тур	Max	Unit	Test	Condition	
OFF CHARACTERISTICS				•	•	·		
Drain-Source Breakdown Voltage	BV _{DSS}	40	—	_	V	$I_D = 250 \mu A, V_{GS} = 0 V$		
Zero Gate Voltage Drain Current	I _{DSS}	_	—	1	μA	$V_{DS} = 40V, V_{G}$	s = 0V	
Gate-Source Leakage	I _{GSS}	_	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS								
Gate Threshold Voltage	V _{GS(th)}	1.0	_	3.0	V	$I_{D} = 250 \mu A, V_{D}$	os = V _{GS}	
Static Drain-Source On-Resistance (Note 8)	P		0.023	0.034	Ω	$V_{GS} = 10V, I_D = 6A$		
	R _{DS (ON)}		0.039	0.059		V_{GS} = 4.5V, I_D	= 5A	
Forward Transconductance (Notes 8 & 9)	g fs	_	20.5		S	$V_{DS} = 15V, I_D = 6A$		
Diode Forward Voltage (Note 8)	V _{SD}	_	0.87	1.1	V	$I_{\rm S} = 6A, V_{\rm GS} = 0V$		
Reverse recovery time (Note 9)	t _{rr}		11.9		ns	-I _S = 2.5A, di/dt = 100A/μs		
Reverse recovery charge (Note 9)	Q _{rr}	_	4.9	_	nC			
DYNAMIC CHARACTERISTICS (Note 9)								
Input Capacitance	C _{iss}	—	453	—	pF			
Output Capacitance	C _{oss}	_	79.1		pF	V _{DS} = 20V, V _{GS} = 0V f = 1MHz		
Reverse Transfer Capacitance	C _{rss}	_	40.5		pF	1 - 110112		
Total Gate Charge (Note 10)	Qg	_	4.9	8	nC	$V_{GS} = 4.5V$		
Total Gate Charge (Note 10)	Qg		10	18	nC		V _{DS} = 20V	
Gate-Source Charge (Note 10)	Q _{gs}	_	1.8	_	nC	$V_{GS} = 10V$	$I_D = 6A$	
Gate-Drain Charge (Note 10)	Q _{gd}	_	2.4	_	nC	1		
Turn-On Delay Time (Note 10)	t _{D(on)}	_	2.7	_	ns			
Turn-On Rise Time (Note 10)	tr	_	2.7	_	ns	V _{DD} = 20V, V _{GS} = 10V		
Turn-Off Delay Time (Note 10)	t _{D(off)}	_	14	_	ns	I _D = 1A, R _G ≅ 6.0Ω		
Turn-Off Fall Time (Note 10)	t _f		6	_	ns			

Notes:

Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%
For design aid only, not subject to production testing.
Switching characteristics are independent of operating junction temperatures.

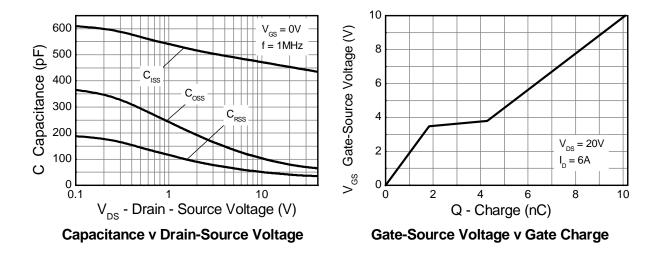


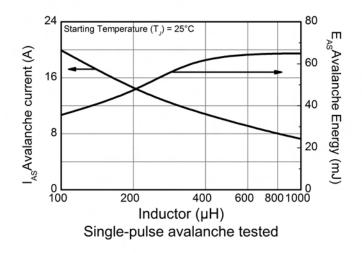
Typical Characteristics





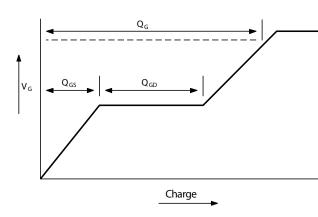
Typical Characteristics – continued



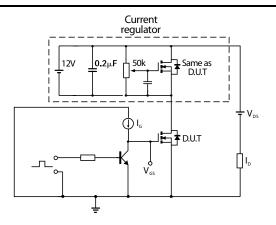




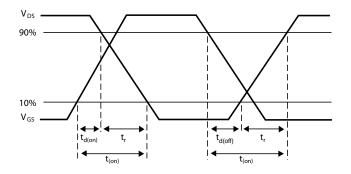
Test Circuits



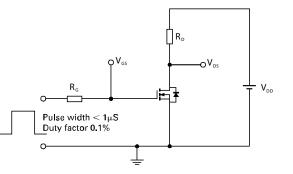
Basic gate charge waveform







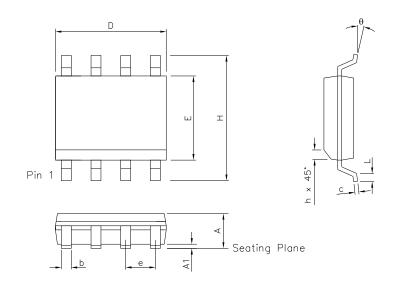
Switching time waveforms



Switching time test circuit

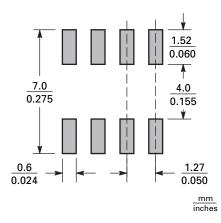


Package Outline Dimensions



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
А	0.053	0.069	1.35	1.75	е	0.050 BSC		1.27 BSC	
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	С	0.008	0.010	0.19	0.25
н	0.228	0.244	5.80	6.20	θ	0°	8°	0°	8°
E	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

Suggested Pad Layout





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