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BD136 / BD138 / BD140 — PNP Epitaxial Silicon Transistor



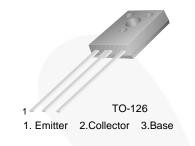
# BD136 / BD138 / BD140 PNP Epitaxial Silicon Transistor

## Features

Complement to BD135, BD137 and BD139 respectively

## Applications

Medium Power Linear and Switching



# **Ordering Information**

Part Number	Marking	Package	Packing Method
BD13610S	BD136-10	TO-126 3L	Bulk
BD13610STU	BD136-10	TO-126 3L	Rail
BD13616S	BD136-16	TO-126 3L	Bulk
BD13616STU	BD136-16	TO-126 3L	Rail
BD13810STU	BD138-10	TO-126 3L	Rail
BD13816STU	BD138-16	TO-126 3L	Rail
BD14010STU	BD140-10	TO-126 3L	Rail
BD14016S	BD140-16	TO-126 3L	Bulk
BD14016STU	BD140-16	TO-126 3L	Rail

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter		Value	Unit
		BD136	-45	
V <sub>CBO</sub> Co	Collector-Base Voltage	BD138	-60	V
		BD140	-80	
		BD136	-45	V
V <sub>CEO</sub> Collector-Emitte	Collector-Emitter Voltage	BD138	-60	
		BD140	-80	
V <sub>EBO</sub>	Emitter-Base Voltage		-5	V
۱ <sub>C</sub>	Collector Current (DC)		-1.5	А
۱ <sub>C</sub>	Collector Current (Pulse)		-3.0	А
I <sub>B</sub>	Base Current		-0.5	А
P <sub>C</sub> Collec	Collector Dissipation	T <sub>C</sub> = 25°C	12.5	W
	Collector Dissipation	$T_A = 25^{\circ}C$	1.25	
ТJ	Junction Temperature		150	°C
T <sub>STG</sub>	Storage Temperature		-55 to +150	°C

# **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit
V <sub>CEO</sub> (sus) Collector-Em Voltage <sup>(1)</sup>		BD136		-45			
	Collector-Emitter Sustaining	BD138	I <sub>C</sub> = -30 mA, I <sub>B</sub> = 0	-60			V
		BD140		-80			
I <sub>CBO</sub>	Collector Cut-Off Current		$V_{CB} = -30 \text{ V}, \text{ I}_{E} = 0$			-0.1	μA
I <sub>EBO</sub>	Emitter Cut-Off Current		$V_{EB} = -5 V, I_{C} = 0$			-10	μA
h <sub>FE1</sub>	DC Current Gain <sup>(1)</sup>		$V_{CE}$ = -2 V, $I_{C}$ = -5 mA	25			
h <sub>FE2</sub>	DC Current Gain <sup>(1)</sup>		$V_{CE} = -2 V, I_{C} = -0.5 A$	25			
h <sub>FE3</sub>	DC Current Gain <sup>(1)</sup>		$V_{CE} = -2 V, I_{C} = -150 mA$	40		250	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(1)</sup>		$I_{\rm C}$ = -500 mA, $I_{\rm B}$ = -50 mA			-0.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage <sup>(1)</sup>		$V_{CE} = -2 V, I_{C} = -0.5 A$			-1	V

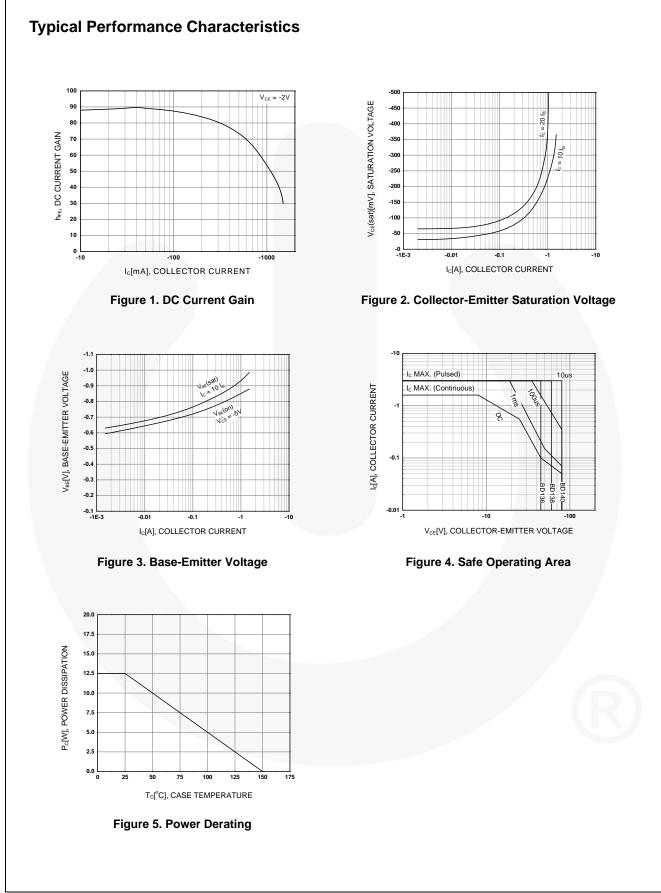
## Note:

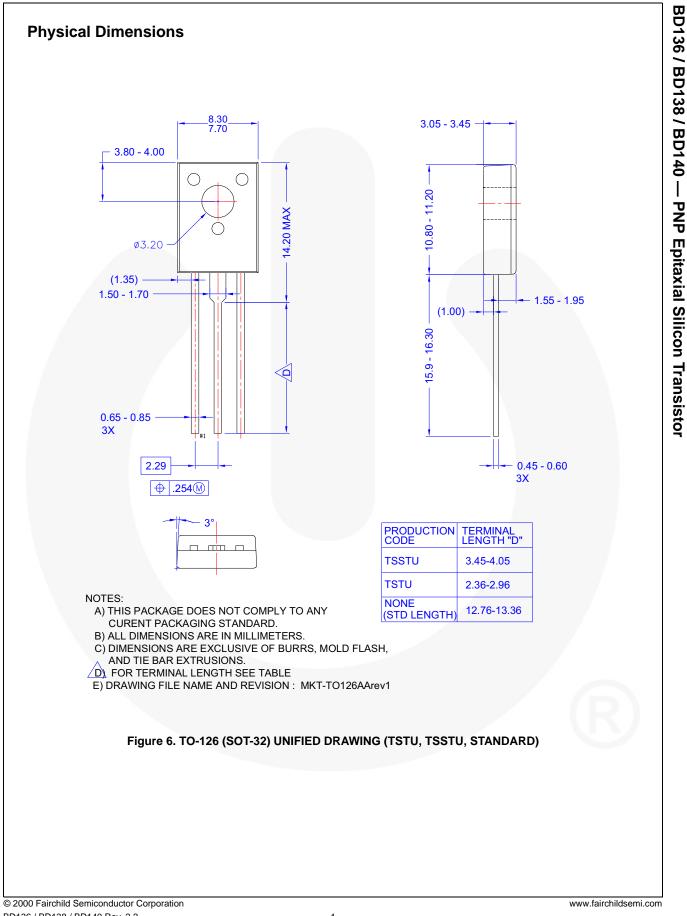
1. Pulse test: pulse width =  $350 \ \mu$ s, duty cycle = 2.0% pulsed.

# h<sub>FE</sub> Classification

Classification	10	16
h <sub>FE3</sub>	63 ~ 160	100 ~ 250

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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