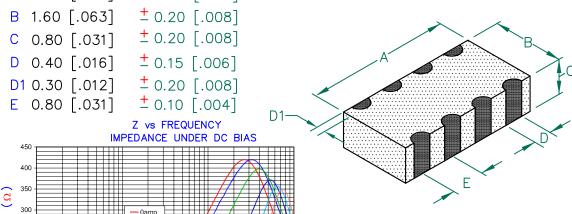
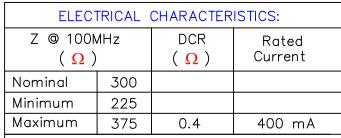
DA1206D301R-10

UNCONTROLLED DOCUMENT





LINE TO LINE INSULATION RESISTANCE $>10M \Omega AT 12 VOLTS.$

IMPEDANCE 250 200 150 100 50

± 0.20 [.008]

PHYSICAL DIMENSIONS:

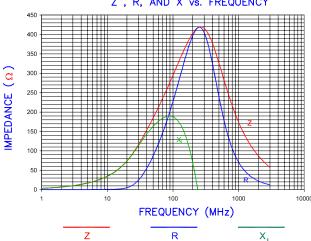
A 3.20 [.126]

NOTES: UNLESS OTHERWISE SPECIFIED

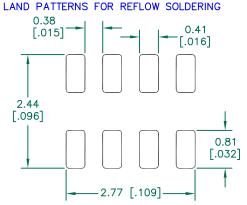
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 3000 PCS/REEL.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMPERATURE TEMP: -40°C~+125°C (INCLUDING SELF-HEATING)

Z , R, AND X vs. FREQUENCY

FREQUENCY (MHz)

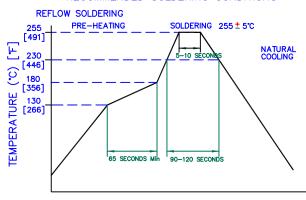


AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3236



(For wave soldering, add 0.762 (0.030) to this dimension)

RECOMMENDED SOLDERING CONDITIONS





		DIMENSIONS ARE IN mm [INCHE	ES].	This print is the property of Laird			
E					Tech. and is loaned in confidence subject to return upon request and		
					with the understanding that no		
					copies shall be made without the		
	E	OPERATING TEMPERATURE UPDATE LAIRD LOGO	08/05/13		written consent of Laird Tech. All rights to design or invention are reserved.		
	D	UPDATE INSULATION RESISTANCE AND REFLOW	05/06/13	QU	PROJECT/PART NUMBER: REV PART TYPE: DRAWN BY:		
	O	UPDATE COMPANY LOGO	05/21/09	JRK	DA1206D301R-10 E co-FIRE TMB		
	В	D1 chgd from 0.008 ±0.004 to 0.012 ±0.008. ADD ROHS, UPDATE COMPANY LOGO	10/31/07	JRK			
					DATE: 03/30/04 SCALE: NTS SHEET:		
	Α	ORIGINAL DRAFT	03/30/04	TMB	CAD #		
	REV	DESCRIPTION	DATE	INT			

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