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October 2013

FCB20N60 N-Channel SuperFET[®] MOSFET

600 V, 20 A, 190 mΩ

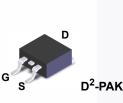
Features

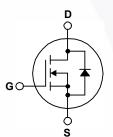
- 650 V @T_{.1} = 150 °C
- Typ. R_{DS(on)} = 150 mΩ
- Ultra Low Gate Charge (Typ. Q_q = 75 nC)
- Low Effective Output Capacitance (Typ. C_{oss}.eff = 165 pF)
- 100% Avalanche Tested
- · RoHS Compliant

Application

- · Lighting
- Solar Inverter

· AC-DC Power Supply





SuperFET[®] MOSFET is Fairchild Semiconductor's first generation of high voltage super-junction (SJ) MOSFET family that is

utilizing charge balance technology for outstanding low on-

resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switch-

ing performance, dv/dt rate and higher avalanche energy. Con-

sequently, SuperFET MOSFET is very suitable for the switching

power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.

Description

MOSFET Maximum Ratings T_C = 25°C unless otherwise noted

Symbol		FCB20N60TM	Unit	
V _{DSS}	Drain to Source Voltage	600	V	
ID	Drain Current	- Continuous (T _C = 25 ^o C)	20	
	Drain Current	- Continuous (T _C = 100 ^o C)	12.5	— A
I _{DM}	Drain Current	- Pulsed (Note 1)	60.0	A
V _{GSS}	Gate to Source Voltage	±30	V	
E _{AS}	Single Pulsed Avalanche	Energy (Note 2)	690	mJ
I _{AR}	Avalanche Current	(Note 1)	20	А
E _{AR}	Repetitive Avalanche Energy		20.8	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3		4.5	V/ns
P _D	Dower Dissinction	(T _C = 25°C)	208	W
	Power Dissipation	- Derate above 25°C	1.67	W/ºC
T _J , T _{STG}	Operating and Storage Te	-55 to +150	°C	
TL	Maximum Lead Tempera 1/8" from Case for 5 Seco	300	°C	

Thermal Characteristics

Symbol	Parameter	FCB20N60TM	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max	0.6	
Р	Thermal Resistance, Junction to Ambient (minimum pad of 2 oz copper), Max.	62.5	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction to Ambient (1 in ² pad of 2 oz copper), Max.	40	

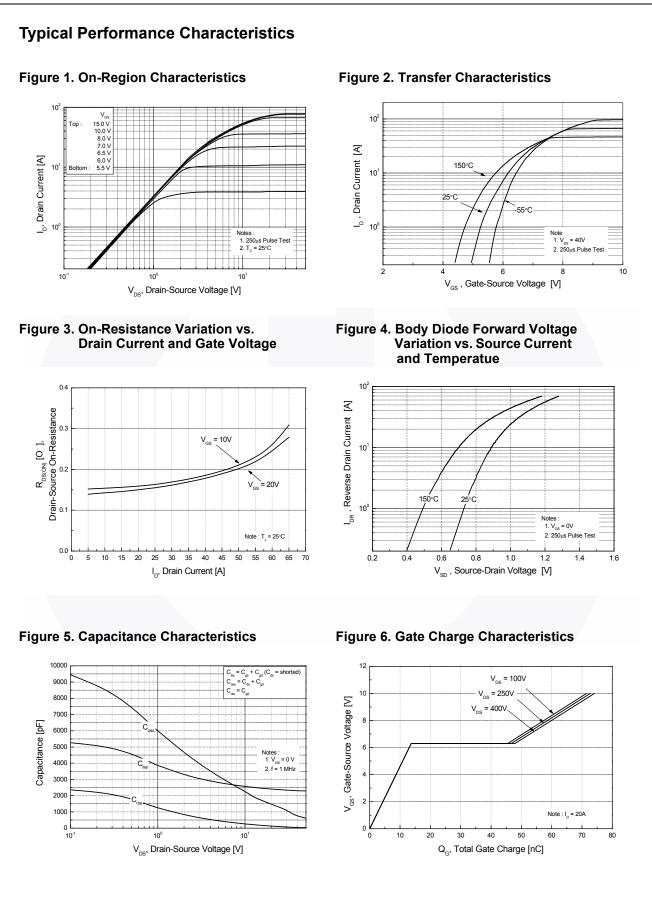
Device Ma	rking	Device	Package	e Re	el Size	Тар	e Width		Quantit	у
FCB20N	160	FCB20N60TM	D ² -PAK	3	30mm		24m		800	
Electrica	Chara	acteristics T _C = 25	5ºC unless o	otherwise noted						
Symbol		Parameter		Test	Conditions		Min.	Тур.	Max.	Unit
Off Charac	teristics	5								
BV _{DSS}	Drain to Source Breakdown Voltage		age	$\frac{V_{GS} = 0 \text{ V,I}_{D} = 250 \mu\text{A}, T_{C} = 25^{o}\text{C}}{V_{GS} = 0 \text{ V,I}_{D} = 250 \mu\text{A}, T_{C} = 150^{o}\text{C}}$			600	-	-	V
							-	650	-	V
ΔΒV _{DSS} / ΔΤ _J	Breakdo Coefficie	wn Voltage Temperature nt	•	I_D = 250 µA, Referenced to 25 ^o C			-	0.6	-	V/ºC
BV _{DS}	Drain-Sc Voltage	ource Avalanche Breakd	own	V _{GS} = 0 V, I _D = 20 A			-	700	-	V
	Zara Ca	to Valtage Drain Current		V _{DS} = 600 V, V _{GS} = 0 V			-		1	
IDSS	Zero Ga	te Voltage Drain Current	[$V_{DS} = 480 \text{ V}, V_{GS} = 0 \text{ V}, T_{C} = 125^{\circ}\text{C}$			-	-	10	μA
I _{GSS}	Gate to I	Body Leakage Current		V _{GS} = ±30 V, V	_{DS} = 0 V		-	-	±100	nA
On Charac	teristics	5								
V _{GS(th)}	Gate Th	reshold Voltage		$V_{GS} = V_{DS}, I_{D} =$	= 250 μA		3.0	-	5.0	V
R _{DS(on)}	Static Dr	ain to Source On Resist	ance	$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		-	0.15	0.19	Ω	
9 _{FS}	Forward Transconductance			$V_{DS} = 40 \text{ V}, \text{ I}_{D} = 10 \text{ A}$			-	17	-	S
Dynamic C	haracte	ristics								
C _{iss}	Input Ca	pacitance			-	2370	3080	pF		
C _{oss}	Output C	apacitance		$V_{DS} = 25 V, V_{GS} = 0 V$ f = 1.0 MHz $V_{DS} = 480 V, V_{GS} = 0 V, f = 1.0 MHz$		-	1280	1665	pF	
C _{rss}	Reverse	Transfer Capacitance				-	95	-	pF	
C _{oss}	Output C	apacitance				-	65	85	pF	
C _{oss} eff.	Effective	Output Capacitance		$V_{DS} = 0 V$ to 40	0 V, V _{GS} = (0 V	-	165	-	pF
Switching	Charact	eristics								
t _{d(on)}	1	Delay Time					-	62	135	ns
t _r		Rise Time		V _{DD} = 300 V, I _D	= 20 A	-	-	140	290	ns
t _{d(off)}	Turn-Off	Delay Time		$R_{G} = 25 \Omega$ (Note 4)		- /	230	470	ns	
t _f		Fall Time				-	65	140	ns	
Q _{g(tot)}	Total Gat	e Charge at 10V		$V_{DS} = 480 \text{ V}, \text{ I}_{D} = 20 \text{ A},$ $V_{GS} = 10 \text{ V}$ (Note 4)			-	75	98	nC
Q _{gs}	Gate to S	Source Gate Charge				-	13.5	18	nC	
Q _{gd}	Gate to D	Drain "Miller" Charge				-	36	-	nC	
	ce Diod	e Characteristics								
I _S	Maximum Continuous Drain to Source Diode Forward Current					-	-	20	Α	
I _{SM}	Maximum Pulsed Drain to Source Diode F						-	- /	60	Α
V _{SD}		Source Diode Forward V		$V_{GS} = 0 \text{ V}, \text{ I}_{SD} = 20 \text{ A}$			-	-	1.4	V
		Recovery Time	0.				-	530		ns
t _{rr}				V _{GS} = 0 V, I _{SD} = 20 A dI _F /dt = 100 A/μs					1	

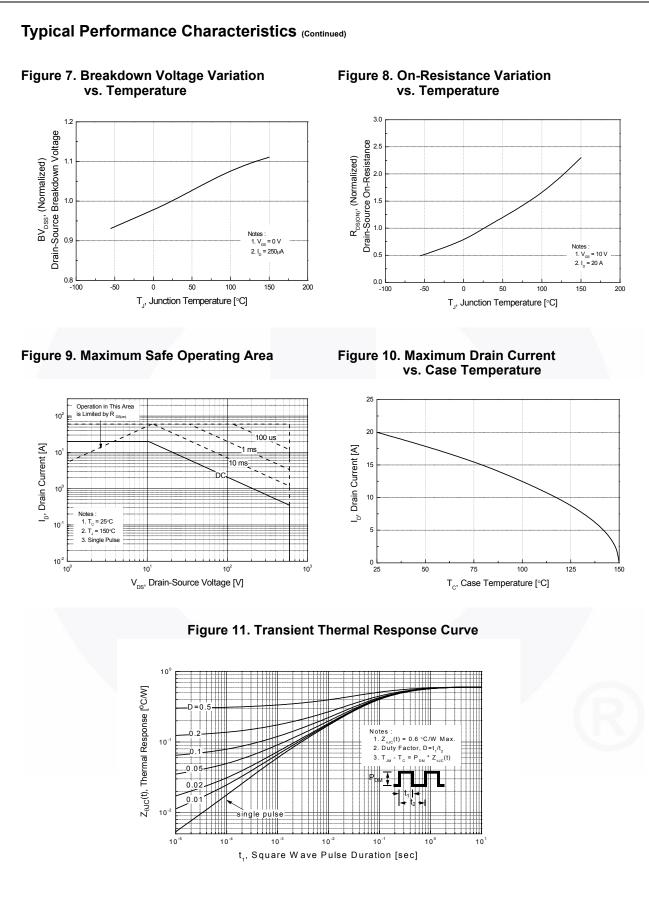
2. I_{AS} = 10 A, V_{DD} = 50 V, R_G = 25 Ω , Starting T_J = 25°C

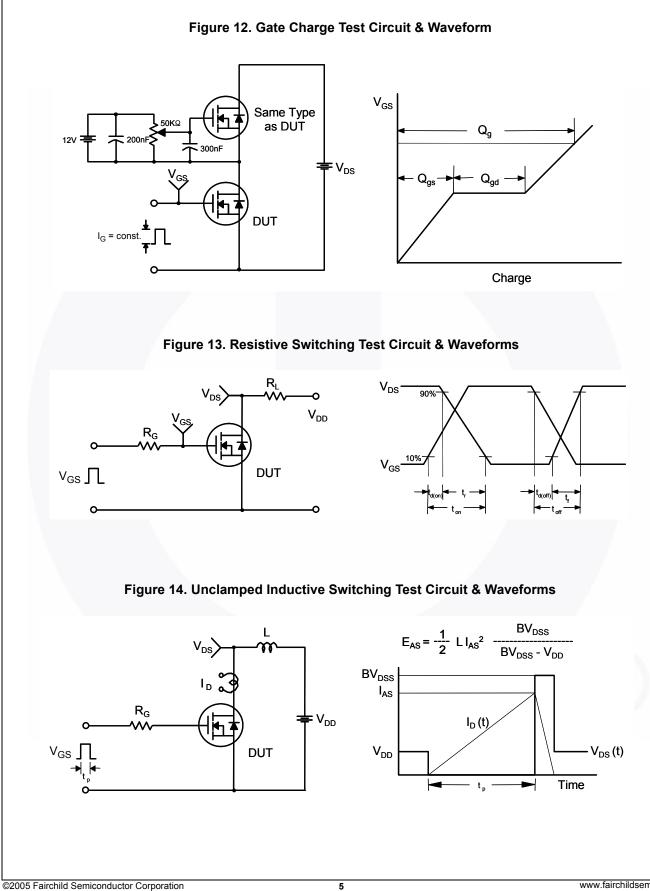
3. $I_{SD} \leq~$ 20 A, di/dt \leq 200 A/µs, $V_{DD} \leq BV_{DSS},$ Starting T_J = 25°C

4. Essentially Independent of Operating Temperature Typical Characteristics

FCB20N60 — N-Channel SuperFET[®] MOSFET







FCB20N60 Rev. C1

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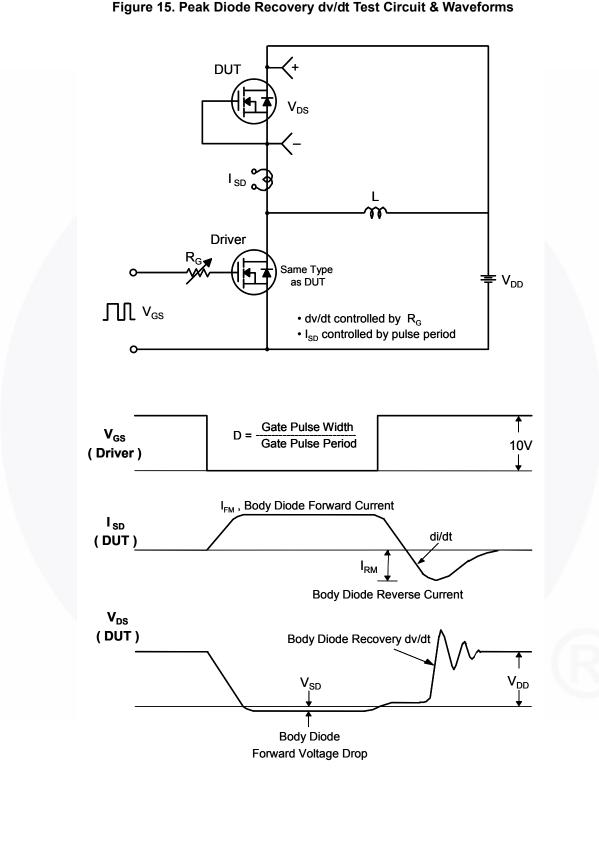
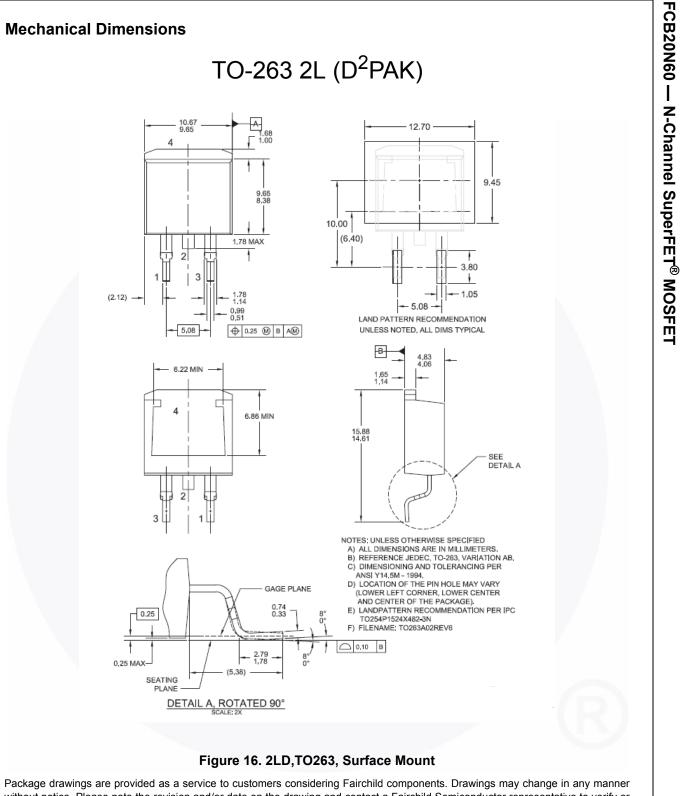


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



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Dimension in Millimeters



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