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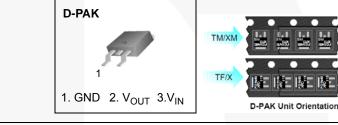
## KA78RH33R Low Dropout Voltage Regulator

## Features

- Fixed Output Voltage of +3.3 V
- Space-Saving SMD Types of DPAK
- 1 V (Typical) Dropout at I<sub>O</sub> = 800 mA
- Output Current: 800 mA
- Thermal Shutdown Protection
- Over-Current Protection
- Output Trimmed to ±1% Tolerance
- No Minimum Load Requirement

### Description

The KA78RH33 is a +3.3V, fixed, low dropout voltage regulator specifically designed for use in low-voltage operation. The maximum load current is 0.8 A and the dropout voltage is guaranteed to be 1 V (typical). The dropout voltage varies with load current. The regulator consists of composite PNP-NPN pass transistors.

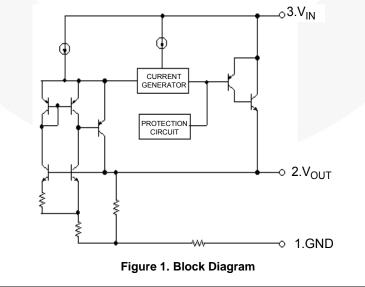


## **Ordering Information**

Part Number	Operating Tem- perature Range	Top Mark	Package	Packing Method
KA78RH33RTF	-25°C to +125°C	KA78RH33	TO-252 3L (DPAK)	Tape and Reel
KA78RH33RTM	-25°C to +125°C	KA78RH33	TO-252 3L (DPAK)	Tape and Reel

\* Refer to above unit orientation figure for TM / TF suffix packing.

## **Block Diagram**



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## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>IN</sub>	Power Supply Input Voltage	15	V
Ι <sub>Ο</sub>	Output Load Current	800	mA
TJ	Junction Temperature	150	°C
T <sub>OPR</sub>	Operating Junction Temperature	-25 to 125	°C
T <sub>STG</sub>	Storage Temperature	-55 to 150	°C

### **Temperature Characteristics**

Symbol	Parameter	Value	Unit
$\Delta V_O / \Delta T$	Temperature Coefficient of Output Voltage	±0.02	%/ °C

## **Thermal Characteristics**

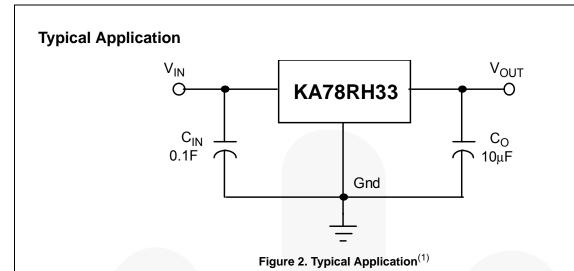
Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient	110	°C/W

## **Electrical Characteristics**

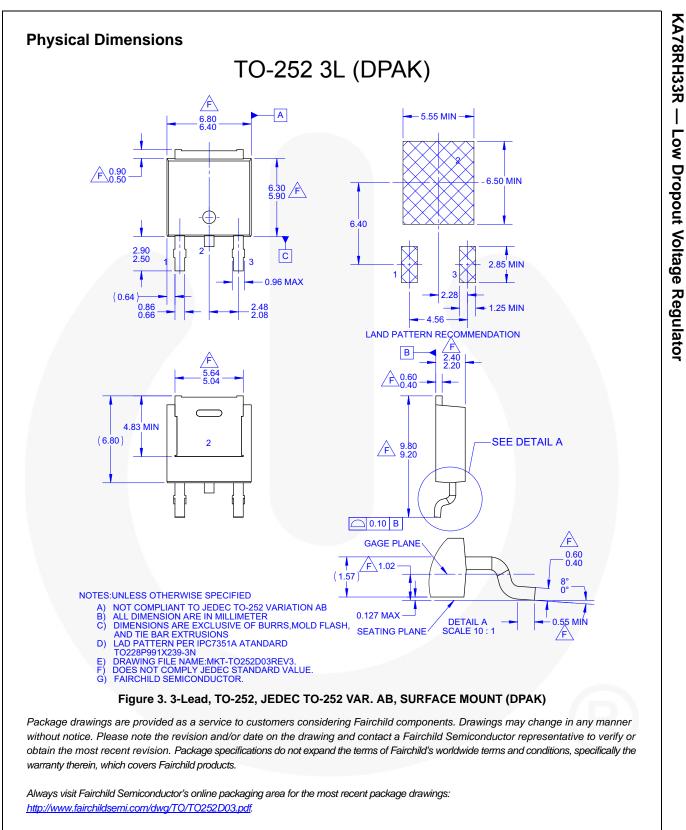
Refer to the test circuit, values are at  $V_{IN}$  = 5 V,  $C_O$  = 10  $\mu$ F, and  $T_A$  = 25°C,unless otherwise specified.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>OUT</sub>	Output Voltage	I <sub>O</sub> = 10 mA, T <sub>J</sub> = 25°C	3.27	3.30	3.33	V
V <sub>OUT</sub>	Output Voltage	$V_{IN} = 4.8 V \text{ to } 12 V,$ $I_O = 10 \text{ mA to } 800 \text{ mA},$ $T_J = -25^{\circ}\text{C} \text{ to } 125^{\circ}\text{C}$	3.23	3.30	3.37	v
R <sub>line</sub>	Line Regulation	$V_{IN}$ = 4.8 V to 12 V, I <sub>O</sub> = 10 mA		1	10	mV
R <sub>load</sub>	Load Regulation	I <sub>O</sub> = 10 mA to 800 mA		1	20	mV
RR	Ripple Rejection	f = 120 Hz, I <sub>O</sub> = 500 mA, V <sub>IN</sub> = 6.3 ± 1 Vrms	55			dB
		I <sub>O</sub> = 100 mA		1.00	1.20	
V <sub>drop</sub>	Dropout Voltage	I <sub>O</sub> = 500 mA		1.05	1.25	V
		I <sub>O</sub> = 800 mA		1.10	1.40	
۱ <sub>q</sub>	Quiescent Current	V <sub>IN</sub> ≤ 12 V		5	10	mA
$\Delta V_{O} / \Delta T$	Temperature Coefficient of Output Voltage	$T_J = -25^{\circ}C$ to 125°C, $I_O = 10$ mA		0.2		mV/°C
I <sub>pk</sub>	Peak Output Current	V <sub>IN</sub> = 6.3 V	800			mA
V <sub>n</sub>	Output Noise Voltage	f = 10 Hz to 10 kHz		100		μVrms



#### Note:

1. An input capacitor,  $C_{IN}$  is not necessary for stability, but improves the overall performance.



For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area: <u>http://www.fairchildsemi.com/packing\_dwg/PKG-T0252D03.pdf</u>.



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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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