## MIC94090/1/2/3/4/5

High Side Load Switches for Consumer Applications

## General Description

The MIC94090/1/2/3/4/5 is a family of high-side load switches designed for operation from 1.7 V to 5.5 V input voltage. The load switch pass element is an internal $130 \mathrm{~m} \Omega \mathrm{R}_{\mathrm{DS}(\mathrm{ON})} \mathrm{P}$ channel MOSFET which enables the device to support up to 1.2A continuous current. Additionally, the load switch supports 1.5 V logic level control and shutdown features.
The MIC94090 and MIC94091 feature rapid turn on. The MIC94092 and MIC94093 provide a slew rate controlled softstart turn-on of $790 \mu \mathrm{~s}$, while the MIC94094 and MIC94095 provide a slew rate controlled soft-start turn-on of $120 \mu \mathrm{~s}$. The soft-start feature option prevents an in-rush current event from pulling down the input supply voltage.
The MIC94091, MIC94093, and MIC94095 include a $250 \Omega$ auto discharge load circuit that is switched on when the load switch is disabled.
An active pull-down on the enable input keeps the MIC94090/1/2/3/4/5 in a default OFF state until the enable pin is pulled above 1.25 V . Internal level shift circuitry allows low voltage logic signals to switch higher supply voltages. The enable voltage can be as high as 5.5 V and is not limited by the input voltage.
The MIC94090/1/2/3/4/5 operating voltage range makes them ideal for Lithium ion and NiMH/NiCad/Alkaline battery powered systems, as well as non-battery powered applications. The devices provide low quiescent current and low shutdown current to maximize battery life.
Datasheets and support documentation can be found on Micrel's web site at: www.micrel.com.

## Features

- 1.7 V to 5.5 V input voltage range
- 1.2A continuous operating current
- $130 \mathrm{~m} \Omega \mathrm{R}_{\mathrm{DS}(\mathrm{ON})}$
- Internal level shift for CMOS/TTL control logic
- Ultra low quiescent current
- Micro-power shutdown current
- Rapid turn-on: MIC94090/1
- Soft-Start: MIC94092/3 (790 s ), MIC94094/5 (120 s )
- Load discharge circuit: MIC94091/3/5
- Space saving and thermally capable $1.2 \mathrm{~mm} \times 1.2 \mathrm{~mm}$ Thin MLF ${ }^{\oplus}$ package
- Industry standard SC-70-6 package


## Applications

- Cellular phones
- Portable Navigation Devices (PND)
- GPS modules
- Personal Media Players (PMP)
- Ultra Mobile PCs
- Other Portable applications
- PDAs
- Portable instrumentation
- Industrial and DataComm equipment


## Typical Application



MIC94090 (ultra fast turn on)
MIC94092 ( $790 \mu \mathrm{~s}$ soft-start)
MIC94094 ( $120 \mu \mathrm{~s}$ soft-start)


MIC94091 (ultra fast turn on with auto-discharge)
MIC94093 ( $790 \mu \mathrm{~s}$ soft-start with auto-discharge) MIC94095 (120 s s soft-start with auto-discharge)

[^0][^1]
## Ordering Information

| Part Number Pb-Free | Part Marking Pb-Free ${ }^{(1)}$ | Fast Turn On | Soft- <br> Start | Load Discharge | Package ${ }^{(2)(3)}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| MIC94090YMT | D1 | $\bullet$ |  |  | 4-Pin $1.2 \mathrm{~mm} \times 1.2 \mathrm{~mm}$ Thin $\mathrm{MLF}^{\circledR}$ |
| MIC94091YMT | D2 | $\bullet$ |  | $\bullet$ | 4-Pin $1.2 \mathrm{~mm} \times 1.2 \mathrm{~mm}$ Thin $\mathrm{MLF}^{\circledR}$ |
| MIC94092YMT | D5 |  | 790 $\mu \mathrm{s}$ |  | 4-Pin $1.2 \mathrm{~mm} \times 1.2 \mathrm{~mm}$ Thin MLF ${ }^{\circledR}$ |
| MIC94093YMT | D7 |  | 790 $\mu \mathrm{s}$ | $\bullet$ | 4-Pin $1.2 \mathrm{~mm} \times 1.2 \mathrm{~mm}$ Thin $\mathrm{MLF}^{\circledR}$ |
| MIC94094YMT | OD |  | $120 \mu \mathrm{~s}$ |  | 4-Pin $1.2 \mathrm{~mm} \times 1.2 \mathrm{~mm}$ Thin $\mathrm{MLF}^{\circledR}$ |
| MIC94095YMT | 1D |  | $120 \mu \mathrm{~s}$ | $\bullet$ | 4-Pin $1.2 \mathrm{~mm} \times 1.2 \mathrm{~mm}$ Thin $\mathrm{MLF}^{\circledR}$ |
| MIC94090YC6 | D1D | $\bullet$ |  |  | SC-70-6 |
| MIC94091YC6 | D2D | - |  | $\bullet$ | SC-70-6 |
| MIC94092YC6 | D5D |  | 790رs |  | SC-70-6 |
| MIC94093YC6 | D7D |  | $790 \mu \mathrm{~s}$ | - | SC-70-6 |
| MIC94094YC6 | ODD |  | $120 \mu \mathrm{~s}$ |  | SC-70-6 |
| MIC94095YC6 | 1DD |  | $120 \mu \mathrm{~s}$ | $\bullet$ | SC-70-6 |

Notes:

1. Under bar symbol (_) may not be to scale.
2. Thin MLF $^{\circledR} \boldsymbol{\Delta}=$ Pin 1 identifier.
3. Thin MLF ${ }^{\circledR}$ is a GREEN RoHS-compliant package. Lead finish is NiPdAu. Mold compound is Halogen Free.

## Pin Configuration



## Pin Description

| Pin Number |  | Pin Name | Pin Function |
| :---: | :---: | :---: | :--- |
| TMLF-4 | SC-70-6 |  | Vout |
| 1 | 1 | Drain of P-Channel MOSFET. |  |
| 4 | 2,5 | GND | Ground: Connect to electrical ground. |
| 3 | 4 | $V_{\text {IN }}$ | Source of P-Channel MOSFET. |
| 2 | 6 | EN | Enable (Input): Active-high CMOS-compatible control input for switch. Internal $2 \mathrm{M} \Omega$ <br> pull down resistor to GND, output will be off if this pin is left floating. |
| -- | 3 | NC | No Internal Connection. A signal or voltage applied to this pin will have no effect on <br> device operation. |

Absolute Maximum Ratings ${ }^{(1)}$


## Operating Ratings ${ }^{(2)}$

Input Voltage (VIN)..................................... +1.7 to +5.5 V
Junction Temperature $\left(\mathrm{T}_{\mathrm{J}}\right) \ldots . . . . . . . . . . . . . . . . . ~-40^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$
Package Thermal Resistance
Thin $\operatorname{MLF}^{\circledR}\left(\theta_{J C}\right)^{(3)}$. $.60^{\circ} \mathrm{C} / \mathrm{W}$

SC-70-6 ( $\theta_{\mathrm{Jc}}$ ).............................................. $100^{\circ} \mathrm{C} / \mathrm{W}$
SC-70-6 ( $\theta_{\mathrm{JA}}$ ) ............................................... $240^{\circ} \mathrm{C} / \mathrm{W}$

## Electrical Characteristics

$\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$, bold values indicate $-40^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq+85^{\circ} \mathrm{C}$, unless noted.

| Symbol | Parameter | Condition | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {EN_T }}{ }^{\text {d }}$ | Enable Threshold Voltage | $\mathrm{V}_{\mathrm{IN}}=1.7 \mathrm{~V}$ to $4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-250 \mu \mathrm{~A}$ | 0.4 |  | 1.25 | V |
| $\mathrm{I}_{\mathrm{Q}}$ | Quiescent Current Measured on the $\mathrm{V}_{\mathrm{IN}}$ Pin | $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\mathrm{EN}}=5.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=$ OPEN <br> Measured on VIN MIC94090/1 |  | 0.1 | 1 | $\mu \mathrm{A}$ |
|  |  | $\begin{aligned} & \mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {EN }}=5.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=\mathrm{OPEN} \\ & \text { Measured on } \mathrm{V}_{\text {IN }} \text { MIC94092/3/4/5 } \end{aligned}$ |  | 8 | 15 | $\mu \mathrm{A}$ |
| $I_{\text {EN }}$ | Enable Input Current | $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {EN }}=5.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=$ OPEN |  | 2.5 | 4 | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {SD }}$ | Shutdown Current | $\mathrm{V}_{\mathrm{IN}}=+5.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{EN}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=\mathrm{OPEN}$ <br> Measured on the $\mathrm{V}_{\text {IN }} \mathrm{pin}^{(7)}$ |  | 0.01 | 1 | $\mu \mathrm{A}$ |
| ISHUT-SWITCH | OFF State Leakage Current | $\mathrm{V}_{\mathrm{IN}}=+5.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{EN}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=\mathrm{SHORT}$ <br> Measured on $\mathrm{V}_{\text {OUT }}{ }^{(7)}$ |  | 0.01 | 1 | $\mu \mathrm{A}$ |
| $\mathrm{R}_{\mathrm{DS} \text { (ON) }}$ | P-Channel Drain to Source ON Resistance | $\mathrm{V}_{\mathrm{IN}}=+5.0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-100 \mathrm{~mA}, \mathrm{~V}_{\text {EN }}=1.5 \mathrm{~V}$ |  | 130 | 225 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\text {IN }}=+4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-100 \mathrm{~mA}, \mathrm{~V}_{\text {EN }}=1.5 \mathrm{~V}$ |  | 135 | 235 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\text {IN }}=+3.6 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-100 \mathrm{~mA}, \mathrm{~V}_{\text {EN }}=1.5 \mathrm{~V}$ |  | 140 | 255 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\text {IN }}=+2.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-100 \mathrm{~mA}, \mathrm{~V}_{\text {EN }}=1.5 \mathrm{~V}$ |  | 170 | 315 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\mathrm{IN}}=+1.8 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-100 \mathrm{~mA}, \mathrm{~V}_{\text {EN }}=1.5 \mathrm{~V}$ |  | 235 | 355 | $\mathrm{m} \Omega$ |
|  |  | $\mathrm{V}_{\text {IN }}=+1.7 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-100 \mathrm{~mA}, \mathrm{~V}_{\text {EN }}=1.5 \mathrm{~V}$ |  | 260 | 375 | $\mathrm{m} \Omega$ |
| RSHutdown | Turn-Off Resistance | $\begin{aligned} & \mathrm{V}_{\mathrm{IN}}=+3.6 \mathrm{~V}, \mathrm{I}_{\text {TEST }}=1 \mathrm{~mA}, \mathrm{~V}_{\mathrm{EN}}=0 \mathrm{~V} \\ & \mathrm{MIC} 94091 / 3 / 5 \end{aligned}$ |  | 250 | 400 | $\Omega$ |

## Electrical Characteristics (Dynamic)

$T_{A}=25^{\circ} \mathrm{C}$, bold values indicate $-40^{\circ} \mathrm{C} \leq \mathrm{T}_{\mathrm{A}} \leq+85^{\circ} \mathrm{C}$, unless noted.

| Symbol | Parameter | Condition | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ton_DLY | Turn-On Delay Time | $\mathrm{V}_{\mathrm{IN}}=+3.6 \mathrm{~V}, \mathrm{ID}=-100 \mathrm{~mA}, \mathrm{~V}_{\mathrm{EN}}=1.5 \mathrm{~V}$ <br> MIC94090/1 |  | 0.4 | 1.5 | $\mu \mathrm{s}$ |
|  |  | $\mathrm{V}_{\mathrm{IN}}=+3.6 \mathrm{~V}, \mathrm{ID}=-100 \mathrm{~mA}, \mathrm{~V}_{\mathrm{EN}}=1.5 \mathrm{~V}$ <br> MIC94092/3 | 200 | 740 | 1500 | $\mu \mathrm{s}$ |
|  |  | $\mathrm{V}_{\mathrm{IN}}=+3.6 \mathrm{~V}, \mathrm{ID}=-100 \mathrm{~mA}, \mathrm{~V}_{\mathrm{EN}}=1.5 \mathrm{~V}$ <br> MIC94094/5 | 65 | 110 | 165 | $\mu \mathrm{s}$ |
| ton_RISE | Turn-On Rise Time | $\mathrm{V}_{\mathrm{IN}}=+3.6 \mathrm{~V}, \mathrm{ID}=-100 \mathrm{~mA}, \mathrm{~V}_{\mathrm{EN}}=1.5 \mathrm{~V}$ <br> MIC94090/1 |  | 0.4 | 1.5 | $\mu \mathrm{s}$ |
|  |  | $\mathrm{V}_{\mathrm{IN}}=+3.6 \mathrm{~V}, \mathrm{ID}=-100 \mathrm{~mA}, \mathrm{~V}_{\mathrm{EN}}=1.5 \mathrm{~V}$ <br> MIC94092/3 | 400 | 790 | 1500 | $\mu \mathrm{s}$ |
|  |  | $\mathrm{V}_{\mathrm{IN}}=+3.6 \mathrm{~V}, \mathrm{ID}=-100 \mathrm{~mA}, \mathrm{~V}_{\mathrm{EN}}=1.5 \mathrm{~V}$ <br> MIC94094/5 | 65 | 120 | 175 | $\mu \mathrm{s}$ |
| toff_dLy | Turn-Off Delay Time | $\mathrm{V}_{\text {IN }}=+3.6 \mathrm{~V}, \mathrm{ID}=-100 \mathrm{~mA}, \mathrm{~V}_{\text {EN }}=1.5 \mathrm{~V}$ |  | 60 | 200 | ns |
| toff_FALL | Turn-Off Fall Time | $\mathrm{V}_{\text {IN }}=+3.6 \mathrm{~V}, \mathrm{ID}=-100 \mathrm{~mA}, \mathrm{~V}_{\text {EN }}=1.5 \mathrm{~V}$ |  | 10 | 100 | ns |

## Notes:

1. Exceeding the absolute maximum rating may damage the device.
2. The device is not guaranteed to function outside its operating rating.
3. With backside thermal contact to PCB. See thermal considerations section.
4. Pulse width $<300 \mu$ s with $<2 \%$ duty cycle.
5. Continuous body diode current conduction (reverse conduction, i.e. $\mathrm{V}_{\text {Out }}$ to $\mathrm{V}_{\text {IN }}$ ) is not recommended.
6. Devices are ESD sensitive. Handling precautions recommended. HBM (Human body model), $1.5 \mathrm{k} \Omega$ in series with 100 pF .
7. Measured on the MIC94090YMT.

## Typical Characteristics













## Typical Characteristics



## Functional Characteristics

## MIC94090





TIME $(20 \mu \mathrm{~s} / \mathrm{div})$




TIME $(4 \mu \mathrm{~s} / \mathrm{div})$

## MIC94091









## MIC94092






## MIC94093







## MIC94094





## MIC94095







## Application Information

## Power Dissipation Considerations

As with all power switches, the ultimate current rating of the switch is limited by the thermal properties of the package and the PCB it is mounted on. There is a simple, ohms law type relationship between thermal resistance, power dissipation and temperature which are analogous to an electrical circuit:


Figure 1. Electrical Circuit
From this simple circuit we can calculate Vx if we know Isource, Vz and the resistor values, Rxy and Ryz using the equation:

$$
V x=\text { Isource } \cdot(R x y+R y z)+V z
$$

Thermal circuits can be considered using these same rules and can be drawn similarly replacing current sources with Power dissipation (in Watts), Resistance with Thermal Resistance (in ${ }^{\circ} \mathrm{C} / \mathrm{W}$ ) and Voltage sources with temperature (in ${ }^{\circ} \mathrm{C}$ ).


Figure 2. Thermal Equivalent Circuit
Now replacing the variables in the equation for Vx , we can find the junction temperature ( Tj ) from power dissipation, ambient temperature and the known thermal resistance of the $\operatorname{PCB}\left(R \theta_{C A}\right)$ and the package $\left(R \theta_{J A}\right)$.

$$
\begin{equation*}
T_{J}=P_{\text {DISS }} \cdot\left(R \theta_{J C}+R \theta_{\mathrm{CA}}\right)+T_{\mathrm{AMB}} \tag{1}
\end{equation*}
$$

It is this equation that is used to determine the graphs on page 7. $\mathrm{P}_{\text {DIss }}$ is calculated as $\left(\mathrm{I}_{\text {switch }}{ }^{2} \times \mathrm{R}_{\text {swmax }}\right)$. $\mathrm{R} \theta_{\mathrm{Jc}}$ is found in the operating ratings section of the datasheet and $R \theta_{C A}$ (the PCB thermal resistance) values for various PCB copper areas can be taken from 'Designing with Low Dropout Voltage Regulators ${ }^{(1)}$ available from the Micrel website (LDO Application Hints).

## Example:

A switch is intended to drive a 500 mA load and is placed on a printed circuit board which has a ground plane area of at least 25 mm square. The Voltage source is a Li-ion battery with a lower operating threshold of 3 V and the ambient temperature of the assembly can be up to $50^{\circ} \mathrm{C}$.
Summary of variables:
$I_{\text {sw }}=0.5 \mathrm{~A}$
$\mathrm{VIN}=3 \mathrm{~V}$ to 4.2 V
Tamb $=50^{\circ} \mathrm{C}$
$R \theta_{\mathrm{Jc}}=60^{\circ} \mathrm{C} / \mathrm{W}$ from Datasheet (P. 3)
$R \theta_{C A}=53^{\circ} \mathrm{C} / \mathrm{W}$ Read from Graph in Fig. 3

PC Board Heat Sink
Thermal Resistance vs. Area


Figure 3. Excerpt from the LDO Book ${ }^{(1)}$
$P_{\text {DISS }}=I_{\text {SW }}{ }^{2} \times R_{\text {SWmax }}$
The worst case switch resistance ( $\mathrm{R}_{\text {swmax }}$ ) at the lowest VIN of 3 V is not available in the datasheet, so the next lower value of VIN is used.
$\mathrm{R}_{\mathrm{SW} \text { max }} @ 2.5 \mathrm{v}=315 \mathrm{~m} \Omega$
If this were a figure for worst case $\mathrm{R}_{\text {sw max }}$ for $25^{\circ} \mathrm{C}$, an additional consideration is to allow for the maximum junction temperature of $125^{\circ} \mathrm{C}$, the actual worst case resistance in this case will be $30 \%$ higher (See $\mathrm{R}_{\mathrm{DS}(o n)}$ variance vs. temperature graph).
$\mathrm{R}_{\mathrm{sw} \text { max }} @ 2.5 \mathrm{v}\left(@ 125^{\prime} \mathrm{C}\right)=315 \times 1.3=410 \mathrm{~m} \Omega$
Therefore junction temperature ( $\mathrm{T}_{\mathrm{J}}$ ):
$T_{J}=0.5^{2} \times 0.41 \times(60+53)+50 \quad$ from (Eqn. 1)
$\mathrm{T}_{\mathrm{J}}=62^{\circ} \mathrm{C}$

This is well below the maximum $125^{\circ} \mathrm{C}$.

## Package Information



SIDE VIEW


## END VIEW

Nate:

1. ALL dimensions are in millimeters.
2. DIMENSIONS ARE INCLUSIVE DF PLATING.
3. DIMENSIINS ARE EXCLUSIVE $\square F$ MILD FLASH \& METAL BURR.


## TロP VIEW




## BDTTDM VIEW

NDTES ।

1. ALL DIMENSIDNS ARE IN MILLIMETERS
2. MAX, PACKAGE WARPAGE IS 0.05 mm .
3. MAXIMUM ALLIWABE BURRS IS 0.076 mm IN ALL DIRECTIDNS
4. PIN \#1 ID $Z N$ TDP WILL BE LASER/INK MARKED.

DIMENSIDN APPLIES TO METALIZED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25 mm FRQM TERMINAL TIP.
6. APPLIED $Z N L Y$ FDR TERMINALS.
A. APPLIED FQR EXPISED PAD AND TERMINALS

## Landing Pattern

All units are in mm
Tolerance $\pm 0.05$ if not noted


Disclaimer: This is only a recommendation based on information available to Micrel from its suppliers. Actual land pattern may have to be significantly different due to various materials and processes used in PCB assembly. Micrel makes no representation or warranty of performance based on the recommended land pattern."
Optional for maximum thermal performance. Heatsink should be connected to GND plane of PCB for maximum thermal performance.

## Suggested Land Pattern 4-Pin Thin MLF ${ }^{\circledR}$ (MT)

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