

## Features

- Lead free as standard
- RoHS compliant\*
- Low capacitance 1.3 pF
- ESD protection >25 kV
- Surge protection

## **Applications**

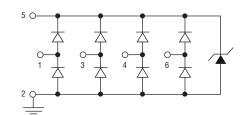
- Personal Digital Assistants (PDAs)
- Notebook and PC computers
- Memory card protection
- SIM card port protection
- Portable electronics

# CDSC706-0504C - Surface Mount TVS Diode Array

### **General Information**

The CDSC706-0504C device provides ESD, EFT and Surge protection for high speed data ports meeting IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements. The Transient Voltage Suppressor array, protecting up to 4 data lines, offers a Working Peak Reverse Voltage of 5 V and Minimum Breakdown Voltage of 6 V.

The SC70-6L packaged device will mount directly onto the industry standard SC70-6 footprint. Bourns® Chip Diodes conform to JEDEC standards, are easy to handle with standard pick and place equipment and the flat configuration minimizes roll away.



### Thermal Characteristics (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CDSC706-0504C	Unit
Peak Pulse Power ( $t_p = 8/20 \ \mu s$ )	IPP	6.5	A
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C
Operating Temperature	T <sub>OPR</sub>	-55 to +85	°C
Operating Supply Voltage	VDC	6	V
ESD per IEC61000-4-2 (Air) (I/O Pins) ESD per IEC61000-4-2 (Contact) (I/O Pins)	V <sub>esd</sub> IO	18 14	kV
ESD per IEC61000-4-2 (Air) ( $V_{CC}$ to GND) ESD per IEC61000-4-2 (Contact) ( $V_{CC}$ to GND)	V <sub>esd</sub> VCC	30 30	kV
DC Voltage at any I/O Pin	V <sub>IO</sub>	(GND-0.5) to (VCC+0.5)	V

### Electrical Characteristics (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CDSC706-0504C	Unit
Maximum Reverse Standoff Voltage 1	V <sub>RWM</sub>	5.0	v
Maximum Leakage Current <sup>1</sup> @ V <sub>RWM</sub>	١L	5.0	μΑ
Maximum Channel Leakage Current @ V <sub>RWM</sub>	I <sub>CD</sub>	1.0	μΑ
Minimum Reverse Breakdown Voltage <sup>1</sup> @ I <sub>BV</sub> =1 mA	V <sub>BR</sub>	6.0	v
Maximum Forward Voltage <sup>4</sup> @ I <sub>F</sub> = 15 mA	V <sub>F</sub>	1.0	V
Maximum Clamping Voltage <sup>2</sup> @ 5 A 8/20 $\mu$ s	V <sub>C</sub>	9.0	V
Typical ESD Clamping Voltage- I/O <sup>2</sup>	V <sub>clamp io</sub>	12.5	v
Maximum Channel Input Capacitance <sup>2</sup> @ V <sub>PIN5</sub> =5 V, V <sub>PIN2</sub> =0 V, V <sub>IN</sub> =2.5 V, f=1 MHz	C <sub>IN</sub>	1.6	pF
Max. Channel to Channel Input Capacitance <sup>3</sup> @ V <sub>PIN5</sub> =5 V, V <sub>PIN2</sub> =0 V, V <sub>IN</sub> =2.5 V, f=1 MHz	C <sub>CROSS</sub>	0.14	pF
Max. Variation of Channel Input Capacitance @ $V_{PIN5}$ =5 V, $V_{PIN2}$ =0 V, $V_{IN}$ =2.5 V, f=1 MHz (I/O Pin to GND)	$\Delta C_{IN}$	0.07	pF

Notes: 1: Pin 5 to Pin 2 (ground)

2: Pin 1, 3, 4 or 6 to Pin 2 (ground)



### WARNING Cancer and Reproductive Harm www.P65Warnings.ca.gov

\*RoHS Directive 2015/863. Mar 31. 2015 and Annex.

4: Pin 2 (ground) to Pin 5

3: Between any two of pins 1, 3, 4, 6.

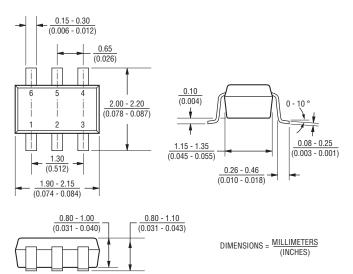
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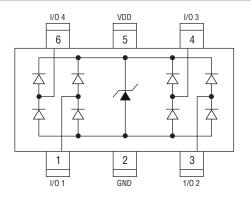
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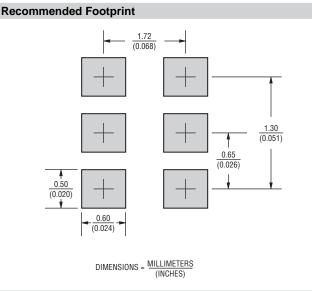
#### **Product Dimensions**

This is a molded JEDEC SC70-6L package with lead free 100 % Matte Sn on the lead frame. It weighs approximately 7 mg and has a flammability rating of UL 94V-0.



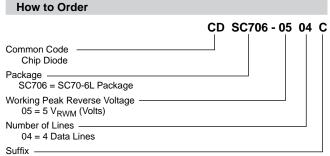
### Configuration





#### **Typical Part Marking**

CDSC706-0504C.....C05



C = Low Capacitance

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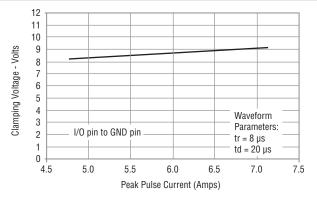
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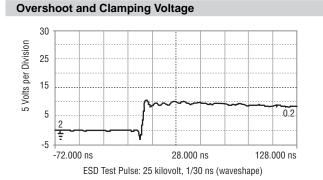
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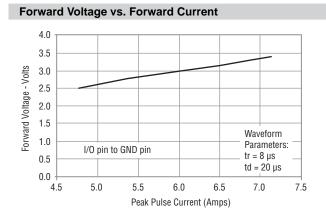
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### **Rating & Characteristic Curves**

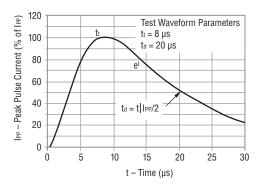
## Clamping Voltage vs. Peak Pulse Current

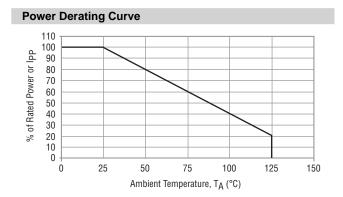


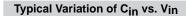


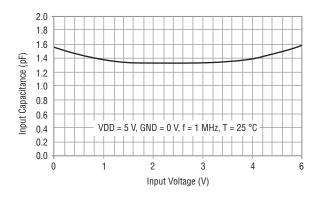


**Pulse Waveform** 









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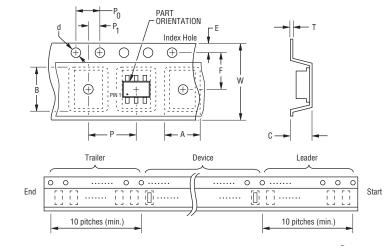
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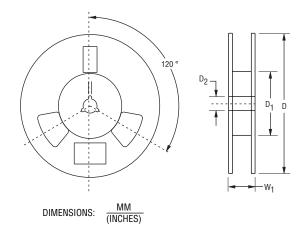
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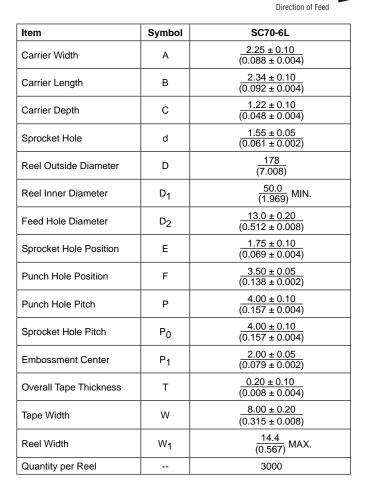
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### **Packaging Information**

The product is packaged in tape and reel format per EIA-481 Standard.







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