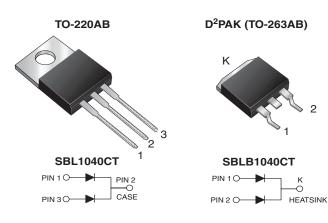
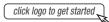


Vishay General Semiconductor

Dual Common Cathode Schottky Rectifier



DESIGN SUPPORT TOOLS

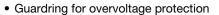




| PRIMARY CHARACTERISTICS | | | | | |
|---|------------------------|--|--|--|--|
| I _{F(AV)} | 2 x 5 A | | | | |
| V_{RRM} | 40 V | | | | |
| I _{FSM} | 175 A | | | | |
| V _F | 0.55 V | | | | |
| T _J max. | 125 °C | | | | |
| Package TO-220AB, D ² PAK (TO-263A | | | | | |
| Circuit configuration | uration Common cathode | | | | |

FEATURES

Power pack





- Low power loss, high efficiency
- Low forward voltage drop
- · High forward surge capability
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D²PAK (TO-263AB) package)
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB package)
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage, high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters, and polarity protection application.

MECHANICAL DATA

Case: TO-220AB, D²PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3_X - RoHS-compliant, AEC-Q101 qualified ("_X" denotes revision code, e.g. A, B, ...)

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

| MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted) | | | | | |
|--|--------------|-----------------------------------|-------------|------|--|
| PARAMETER | | SYMBOL | SBL1040CT | UNIT | |
| Maximum repetitive peak reverse voltage | | V_{RRM} | 40 | V | |
| Working peak reverse voltage | | V _{RWM} | 28 | | |
| Maximum DC blocking voltage | | V_{DC} | 40 | | |
| Maximum average forward rectified current at $T_C = 107$ °C | total device | I _{F(AV)} | 10 | | |
| | per diode | | 5.0 | Α | |
| Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode | | I _{FSM} | 175 | | |
| Operating junction and storage temperature range | | T _J , T _{STG} | -40 to +125 | °C | |



SBL1040CT, SBLB1040CT

Vishay General Semiconductor

| ELECTRICAL CHARACTERISTICS (T _C = 25 °C unless otherwise noted) | | | | | | |
|---|-------------------------------|----------------------|-------------------------|-------|------|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUE | UNIT | |
| Maximum instantaneous forward voltage per diode | V _F ⁽¹⁾ | 5.0 A | | 0.55 | V | |
| Maximum instantaneous reverse current at DC blocking voltage | I _R ⁽²⁾ | Rated V _R | T _C = 25 °C | 0.5 | - mA | |
| per diode | | | T _C = 100 °C | 50 | | |

Notes

 $^{(1)}$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 40 ms

| THERMAL CHARACTERISTICS (T _C = 25 °C unless otherwise noted) | | | | | |
|---|-----------------|-----|------|------|--|
| PARAMETER | SYMBOL | SBL | SBLB | UNIT | |
| Typical thermal resistance per diode | $R_{\theta JC}$ | 3.0 | 3.0 | °C/W | |

| ORDERING INFORMATION (Example) | | | | | | |
|--------------------------------|-----------------------|-----------------|--------------|---------------|---------------|--|
| PACKAGE | PREFERRED P/N | UNIT WEIGHT (g) | PACKAGE CODE | BASE QUANTITY | DELIVERY MODE | |
| TO-220AB | SBL1040CT-E3/45 | 1.85 | 45 | 50/tube | Tube | |
| TO-263AB | SBLB1040CT-E3/45 | 1.35 | 45 | 50/tube | Tube | |
| TO-263AB | SBLB1040CT-E3/81 | 1.35 | 81 | 800/reel | Tape and reel | |
| TO-263AB | SBLB1040CTHE3_B/P (1) | 1.35 | Р | 50/tube | Tube | |
| TO-263AB | SBLB1040CTHE3_B/I (1) | 1.35 | I | 800/reel | Tape and reel | |

Note

(1) AEC-Q101 qualified, available in D2PAK (TO-263AB) package only

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RATINGS AND CHARACTERISTICS CURVES (T_C = 25 °C unless otherwise noted)

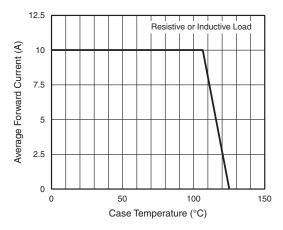
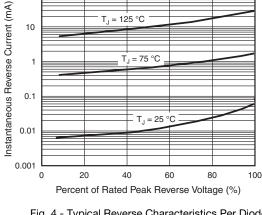


Fig. 1 - Forward Current Derating Curve



100

Fig. 4 - Typical Reverse Characteristics Per Diode

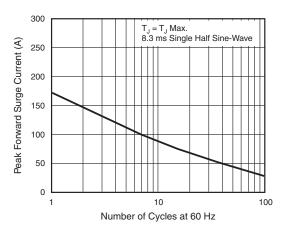


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

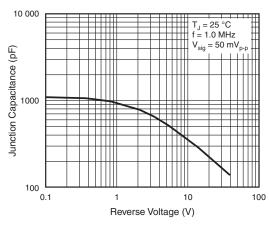


Fig. 5 - Typical Junction Capacitance Per Diode

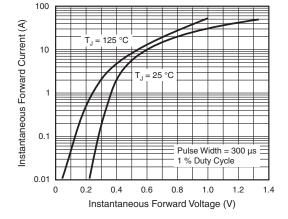


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

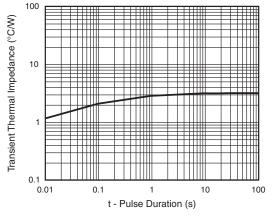
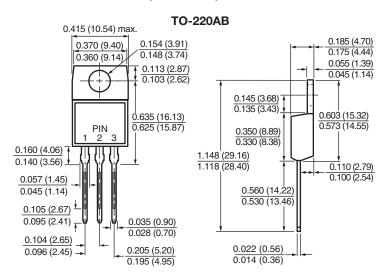


Fig. 6 - Typical Transient Thermal Impedance Per Diode

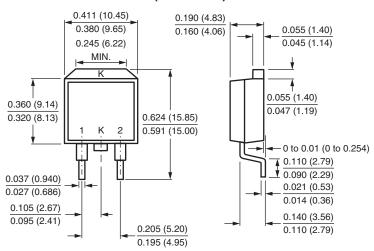


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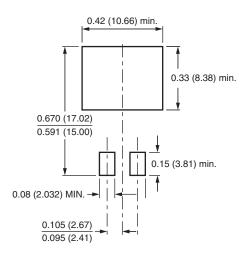
PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



D²PAK (TO-263AB)



Mounting Pad Layout





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