

# NOT RECOMMENDED FOR NEW DESIGN USE ZXTP5240F-7



DSS5240T

#### **40V PNP LOW SATURATION TRANSISTOR IN SOT23**

#### **Features**

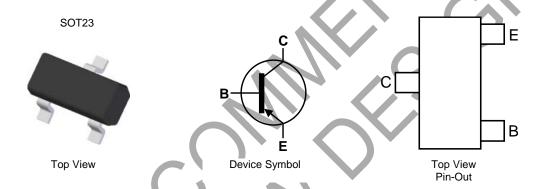
- BV<sub>CEO</sub> > -40V
- I<sub>C</sub> = -2A High Continuous Collector Current
- I<sub>CM</sub> = -3A Peak Pulse Current
- Low Saturation Voltage -225mV Max @ I<sub>C</sub> = -1A
- R<sub>CE(SAT)</sub> = 90mΩ at 0.5A for a Low Equivalent On-Resistance
- 730mW Power Dissipation
- Complimentary NPN Type: DSS4240T
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight 0.008 grams (Approximate)

### **Application**

- Gate Driving MOSFETs and IGBTs
- Load Switch
- DC-DC Converters
- Battery Charging



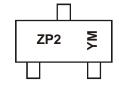
### Ordering Information (Note 4 & 5)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DSS5240T-7	NRND (Use ZXTP5240F-7)	ZP2	7	8	3000
DSS5240T-13	NRND (Use ZXTP5240F-7)	ZP2	13	8	10,000
DSS5240TQ-7	NRND	ZP2	7	8	3000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.
- 6. NRND Not recommended for new design.

#### Marking Information



ZP2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: C = 2015) M = Month (ex: 9 = September)

Date Code Key

Year	2013	2014	2015	2016	2017	2018	201	19 2	2020	2021	2022	2023
Code	Α	В	С	D	E	F	G	i	Н	1	J	K
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



### Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-40	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-40	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Peak Pulse Collector Current	I <sub>CM</sub>	-3	А
Continuous Collector Current	Ic	-2	А
Base Current	I <sub>B</sub>	-300	mA

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	P <sub>D</sub>	730	mW
Power Dissipation (Note 8)	P <sub>D</sub>	600	mW
Thermal Resistance, Junction to Ambient Air (Note 7)	R <sub>OJA</sub>	171	°C/W
Thermal Resistance, Junction to Ambient Air (Note 8)	R <sub>OJA</sub>	209	°C/W
Thermal Resistance, Junction to Lead (Note 9)	R <sub>OJL</sub>	75	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-55 to +150	°C

### ESD Ratings (Note 10)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge—Human Body Model	ESD HBM	4000	V	3A
Electrostatic Discharge—Machine Model	ESD MM	400	V	С

Notes:

- 7. For a device mounted with the collector lead on 15mm × 15mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.

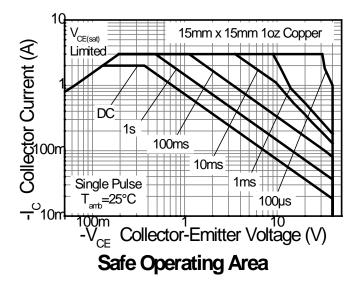
  8. Same as Note 7, except the device is mounted on minimum recommended pad layout.

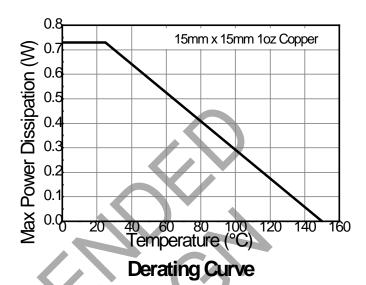
  9. Thermal resistance from junction to solder-point (at the end of the collector lead).

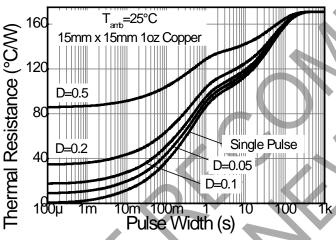
  10. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

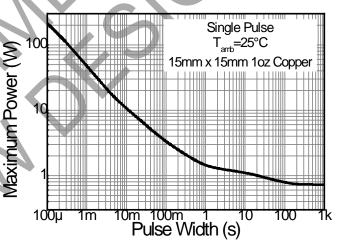


### **Thermal Characteristics and Derating Information**









**Transient Thermal Impedance** 

**Pulse Power Dissipation** 



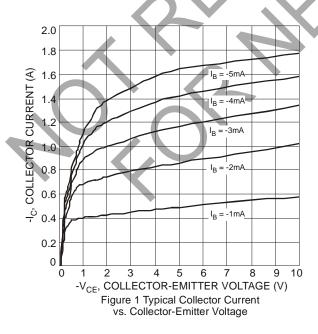
# **Electrical Characteristics** (@ $T_A = +25$ °C, unless otherwise specified.)

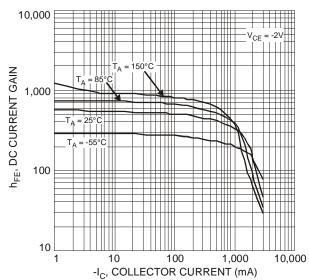
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-40	_	_	V	I <sub>C</sub> = -100μA
Collector-Emitter Breakdown Voltage (Note 11)	BV <sub>CEO</sub>	-40	_	_	V	I <sub>C</sub> = -10mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-5	_	_	V	$I_E = -100 \mu A$
Collector-Base Cutoff Current		_	_	-100	nA	$V_{CB} = -30V, I_{E} = 0$
Collector-Base Cutoff Current	I <sub>CBO</sub>	_	_	-50	μA	$V_{CB} = -30V$ , $I_E = 0$ , $T_A = +150$ °C
Emitter-Base Cutoff Current	I <sub>EBO</sub>	_	_	-100	nA -	$V_{EB} = -4V, I_{C} = 0$
ON CHARACTERISTICS (Note 11)						
		300	_	_		$V_{CE} = -2V, I_{C} = -0.1A$
DC Current Gain		260	_	_		V <sub>CE</sub> = -2V, I <sub>C</sub> = -0.5A
DC Current Gain	h <sub>FE</sub>	210	_	-	7	V <sub>CE</sub> = -2V, I <sub>C</sub> = -1A
		100	_	4-		V <sub>CE</sub> = -2V, I <sub>C</sub> = -2A
		_		-100		I <sub>C</sub> = -100mA, I <sub>B</sub> = -1mA
		1	45	-110		$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>			-225	mV	$I_C = -750 \text{mA}, I_B = -15 \text{mA}$
			<b>\</b>	-225	~ /	$I_C = -1A$ , $I_B = -50mA$
		-//		-350		$I_C = -2A$ , $I_B = -200mA$
Equivalent On-Resistance	R <sub>CE(SAT)</sub>	7	90	220	mΩ	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	-	<b>3</b> –	-1.1	V	I <sub>C</sub> = -2A, I <sub>B</sub> = -200mA
Base-Emitter Turn-on Voltage	V <sub>BE(ON)</sub>	1-1	- 4	-0.75	V	V <sub>CE</sub> = -2V, I <sub>C</sub> = -100mA
SMALL SIGNAL CHARACTERISTICS					•	
Transition Frequency	fτ	100		Y	MHz	V <sub>CE</sub> = -10V, I <sub>C</sub> = -100mA, f = 100MHz
Output Capacitance	$C_{ob}$	1		28	pF	V <sub>CB</sub> = -10V, f = 1MHz

Note:

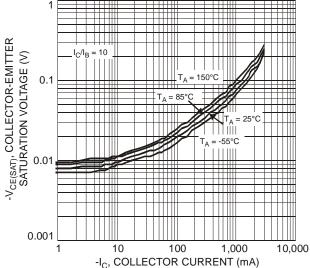
11. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.

# Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

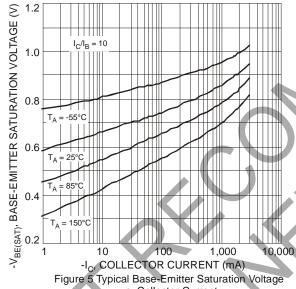








-I<sub>C</sub>, COLLECTOR CURRENT (mA)
Figure 3 Typical Collector-Emitter Saturation Voltage vs. Collector Current



vs. Collector Current

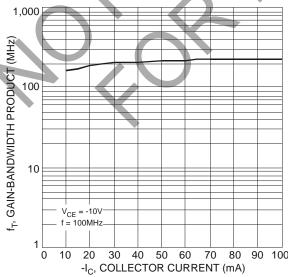
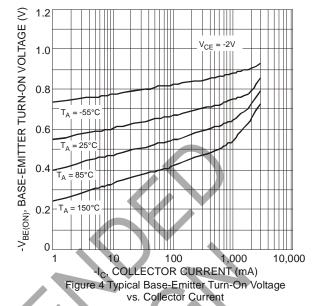


Figure 7 Typical Gain-Bandwidth Product vs. Collector Current



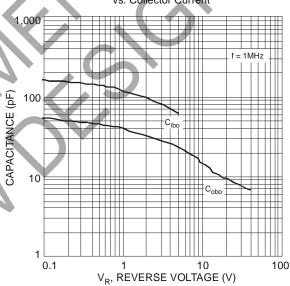
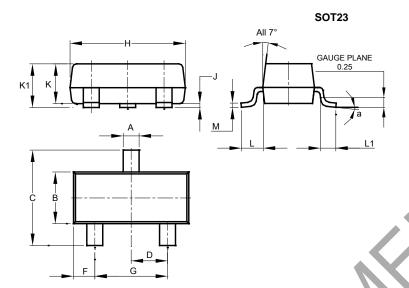


Figure 6 Typical Capacitance Characteristics



### **Package Outline Dimensions**

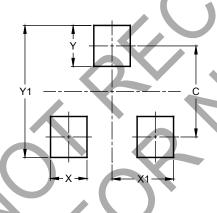
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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