

TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

TLP331, TLP332

Programmable Controllers
AC/DC-Input Module
Telecommunication

The TOSHIBA TLP331 and TLP332 consist of a gallium arsenide infrared emitting diode optically coupled to a photo-transistor in a six lead plastic DIP package.

This photocoupler provides the unique feature of high current transfer ratio at both low output voltage and low input current. This makes it ideal for use in low power logic circuits, telecommunications equipment and portable electronics isolation applications.

TLP332 has no-base internal connection for high-EMI environments.

- Collector-emitter voltage: 55 V (min)
- Isolation voltage: 5000 Vrms (min)
- UL recognized: UL1577, file no. E67349
- c-UL approved: CSA Component Acceptance Service
No. 5A, File No.E67349

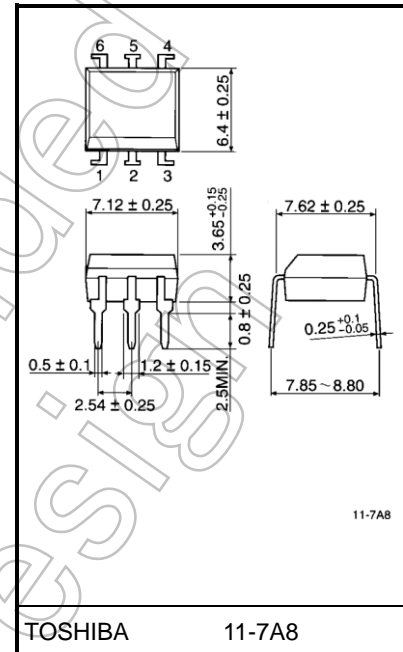
- Current transfer ratio

Classification (Note 1)	Current Transfer Ratio (min)			Marking of Classification
	Ta = 25°C		Ta = -25 to 75°C	
	If = 1 mA VCE = 0.5V	If = 0.5 mA VCE = 1.5V	If = 1 mA VCE = 0.5V	
Rank BV	200%	100%	100%	BV
Standard	100%	50%	50%	BV, blank

Note 1: ex. Standard: TLP331
Rank BV: TLP331(BV)

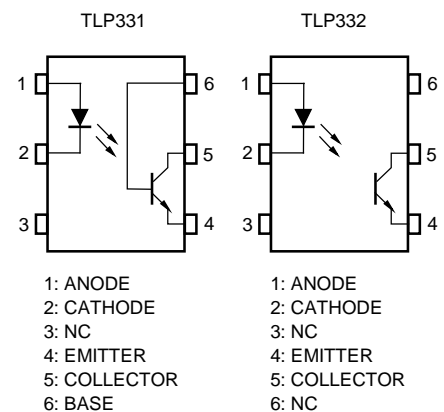
Note: Application type name for certification test,
please use standard product type name, i.e.
TLP331(BV): TLP331

Unit: mm



Weight: 0.4 g (typ.)

Pin Configurations (top view)



Start of commercial production
1986-03

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
LED	Forward current	I _F	50	mA
	Forward current derating (Ta ≥ 39°C)	ΔI _F /°C	-0.7	mA/°C
	Peak forward current (100 μs pulse, 100 pps)	I _{FP}	1	A
	Reverse Voltage	V _R	5	V
	Diode power dissipation	P _D	50	mW
	Diode power dissipation derating (Ta > 39 °C)	ΔP _D /°C	-0.58	mW/°C
	Junction temperature	T _j	125	°C
Detector	Collector-emitter voltage	V _{CEO}	55	V
	Collector-base voltage (TLP331)	V _{CBO}	80	V
	Emitter-collector voltage	V _{ECO}	7	V
	Emitter-base voltage (TLP331)	V _{EBO}	7	V
	Collector current	I _C	50	mA
	Power dissipation	P _C	150	mW
	Power dissipation derating (Ta ≥ 25°C)	ΔP _C /°C	-1.5	mW/°C
	Junction temperature	T _j	125	°C
Storage temperature range		T _{stg}	-55 to 125	°C
Operating temperature range		T _{opr}	-55 to 100	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Total package power dissipation		P _T	250	mW
Total package power dissipation derating (Ta ≥ 25°C)		P _T /°C	-2.5	mW/°C
Isolation voltage (AC, 60 s, RH ≤ 60%) (Note 1)		BV _S	5000	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Device considered a two terminal device: Pins 1, 2 and 3 shorted together and pins 4, 5 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Typ.	Max	Unit
Supply voltage	V _{CC}	—	5	25	V
Forward current	I _F	—	1.6	25	mA
Collector current	I _C	—	1	10	mA
Operating temperature	T _{opr}	-25	—	75	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5 V	—	—	10	μA
	Capacitance	C _T	V = 0 V, f = 1 MHz	—	30	—	pF
Detector	Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C = 0.5 mA	55	—	—	V
	Emitter-collector breakdown voltage	V _{(BR)ECO}	I _E = 0.1 mA	7	—	—	V
	Collector-base breakdown voltage (TLP331)	V _{(BR)CBO}	I _C = 0.1 mA	80	—	—	V
	Emitter-base breakdown voltage (TLP331)	V _{(BR)EBO}	I _E = 0.1 mA	7	—	—	V
	Collector dark current	I _{CEO}	V _{CE} = 24 V	—	10	100	nA
			V _{CE} = 24 V, Ta = 85°C	—	2	50	μA
	Collector dark current (TLP331)	I _{CER}	V _{CE} = 24 V, Ta = 85°C R _{BE} = 1 MΩ	—	0.5	10	μA
	Collector dark current (TLP331)	I _{CBO}	V _{CB} = 10 V	—	0.1	—	nA
	DC forward current gain (TLP331)	h _{FE}	V _{CE} = 5 V, I _C = 0.5 mA	—	1000	—	—
	Capacitance (collector to emitter)	C _{CE}	V = 0 V, f = 1 MHz	—	12	—	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I _C /I _F	I _F = 1 mA, V _{CE} = 0.5 V Rank BV	100	—	1200	%
			200	—	1200	
Low input CTR	I _C /I _F (low)	I _F = 0.5 mA, V _{CE} = 1.5 V Rank BV	50	—	—	%
			100	—	—	
Base photo-current (TLP331)	I _{PB}	I _F = 1 mA, V _{CB} = 5 V	—	10	—	μA
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 0.5 mA, I _F = 1 mA	—	—	0.4	V
		I _C = 1 mA, I _F = 1 mA	—	0.2	—	
		Rank BV	—	—	0.4	

Coupled Electrical Characteristics (Ta = 25 to 75°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I _C /I _F	I _F = 1 mA, V _{CE} = 0.5 V Rank BV	50	—	—	%
			100	—	—	
Low input CTR	I _C /I _F (low)	I _F = 0.5 mA, V _{CE} = 1.5 V Rank BV	—	50	—	%
			—	100	—	

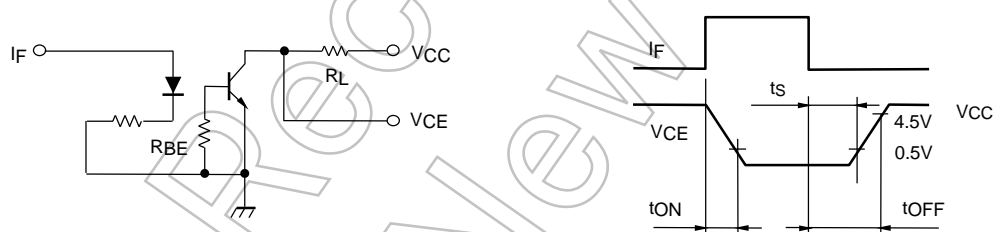
Isolation Characteristics (Ta = 25°C)

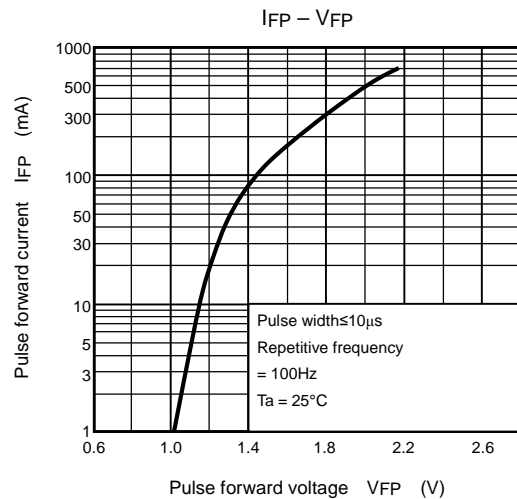
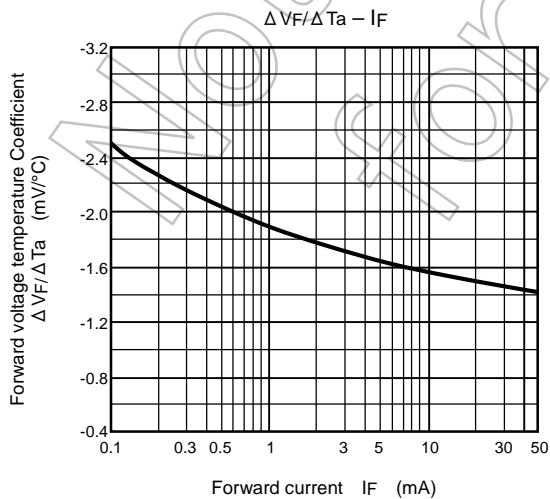
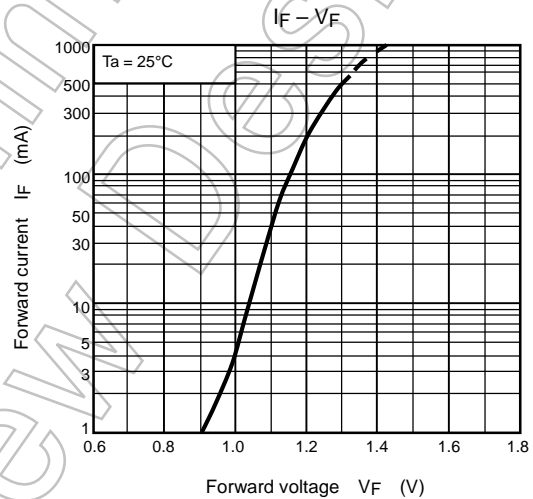
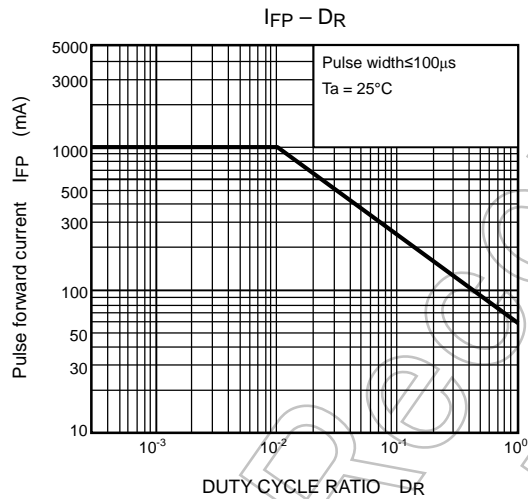
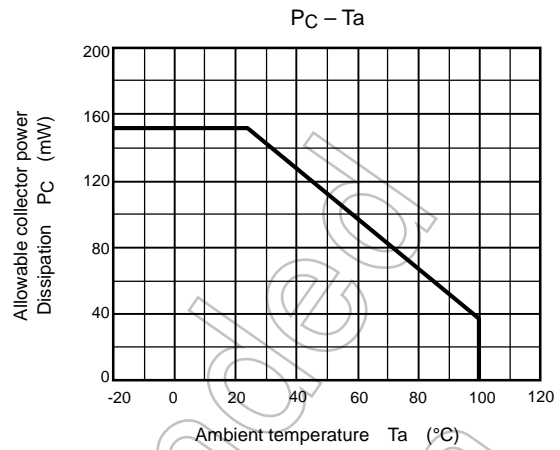
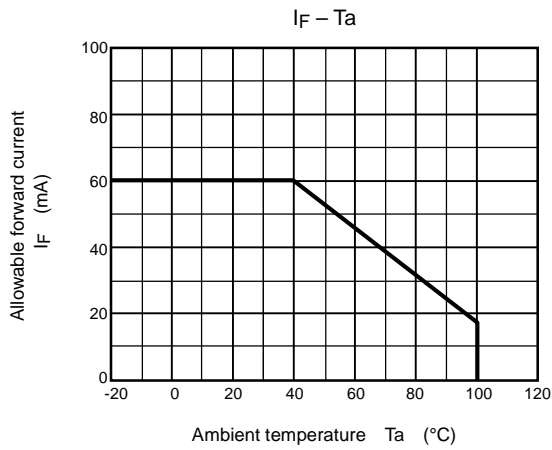
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance (input to output)	CS	V _S = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation resistance	RS	V = 500 V, RH ≤ 60%	5×10 ¹⁰	10 ¹⁴	—	Ω
Isolation voltage	BVS	AC, 60 s	5000	—	—	V _{rms}
		AC, 1 s, in oil	—	10000	—	
		DC, 60 s, in oil	—	10000	—	V _{dc}

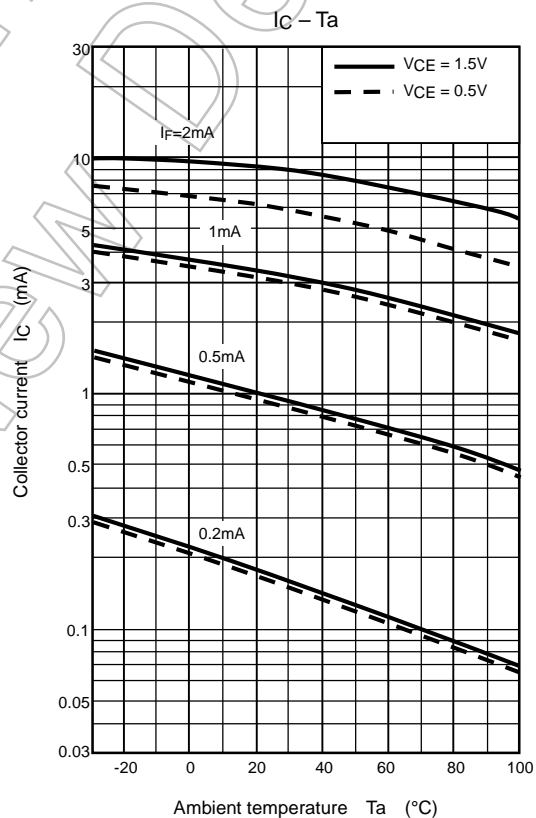
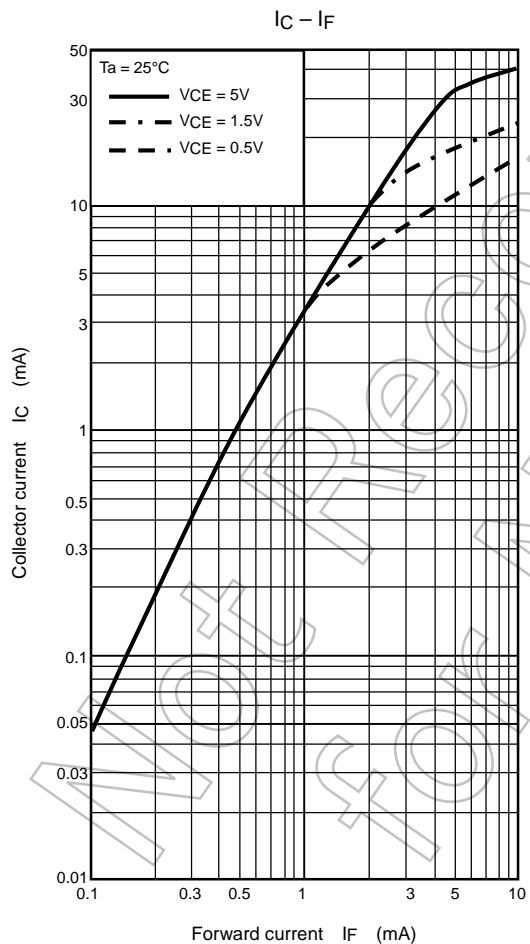
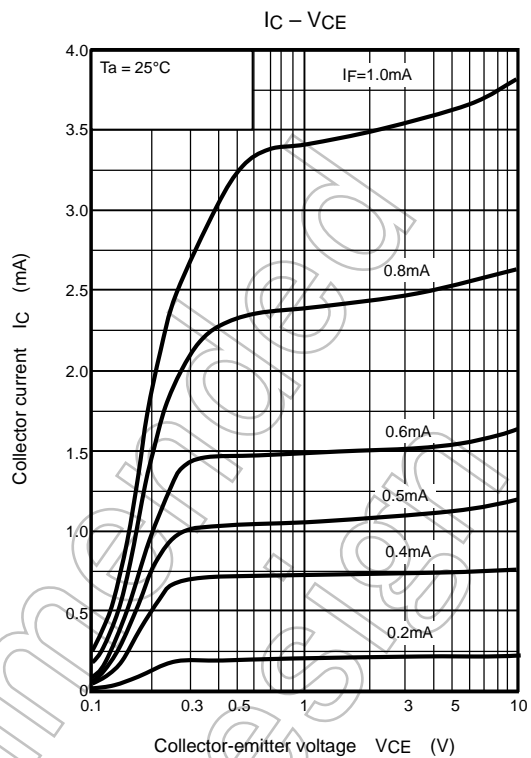
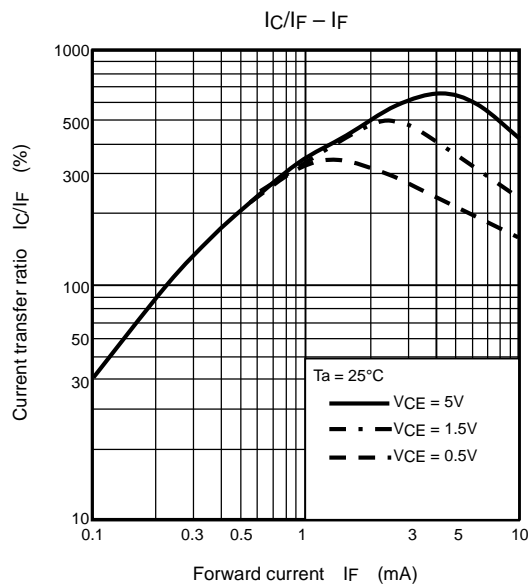
Switching Characteristics (Ta = 25°C)

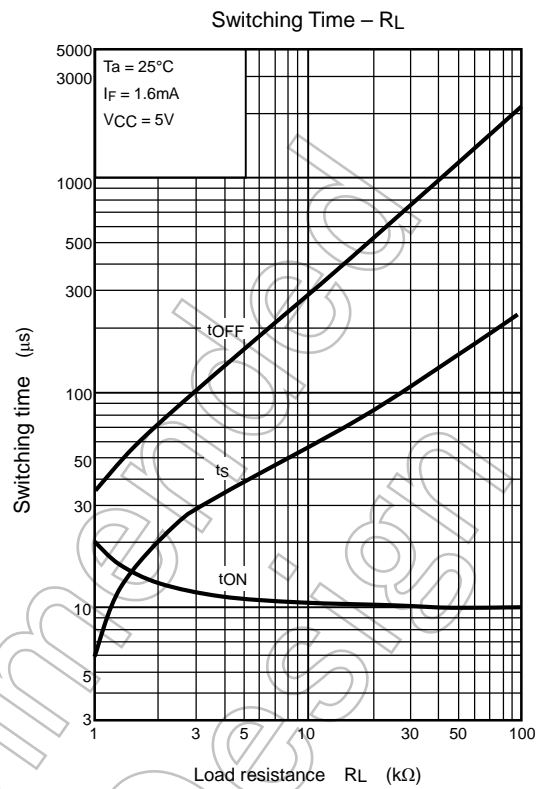
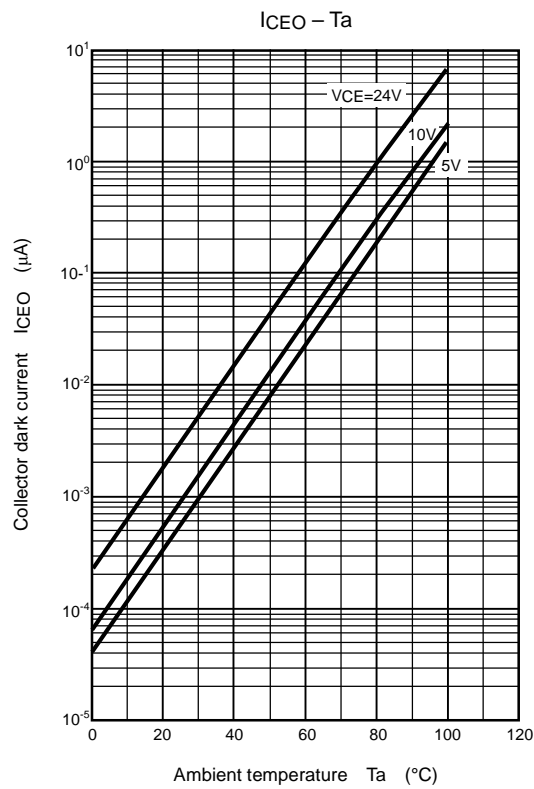
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Rise time	t _r	V _{CC} = 10 V, I _C = 2 mA R _L = 100 Ω	—	8	—	μs
Fall time	t _f		—	8	—	
Turn-on time	t _{on}		—	10	—	
Turn-off time	t _{off}		—	8	—	
Turn-on time	t _{ON}	R _L = 4.7 kΩ (Fig.1) R _{BE} = OPEN V _{CC} = 5 V, I _F = 1.6 mA	—	10	—	μs
Storage time	t _S		—	50	—	
Turn-off time	t _{OFF}		—	300	—	
Turn-on time	t _{ON}	R _L = 4.7 kΩ (Fig.1) R _{BE} = 470 kΩ (TLP331) V _{CC} = 5 V, I _F = 1.6 mA	—	12	—	μs
Storage time	t _S		—	30	—	
Turn-off time	t _{OFF}		—	100	—	

Fig. 1 Switching time test circuit









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