Vishay Dale



# IHLP<sup>®</sup> Commercial Inductors, Low DCR Series





### DESIGN SUPPORT TOOLS click logo to get started



esign Tools

STANDARD ELECTRICAL SPECIFICATIONS						
L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR TYP. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) <sup>(1)</sup>	SATURATION CURRENT DC TYP. (A) <sup>(2)</sup>		
1.0	1.86	2.05	41	27.5		
1.5	3.12	3.43	31	21		
2.2	4.57	5.03	26	19		
3.3	6.64	7.30	20.5	14		
4.7	8.47	9.32	18	12		
5.6	11.09	12.20	15	11.5		
6.8	12.54	13.79	14.5	10.5		
10.0	17.2	18.92	12	8		
15.0	27.8	30.58	9	7.5		
22.0	42.7	46.97	7.2	6.2		
33.0	64.4	70.84	6.5	6		
47.0	98.60	108.46	5	4.3		

#### Notes

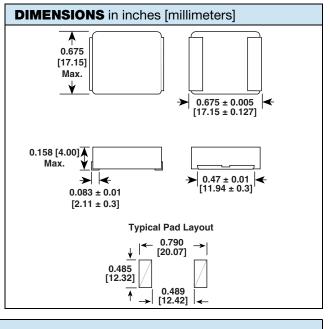
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- Rated operating voltage (across inductor) = 50 V
- <sup>(1)</sup> DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- $^{(2)}\,$  DC current (A) that will cause  $L_0$  to drop approximately 20 %

### FEATURES

- Shielded construction
- Frequency range up to 750 kHz
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without
  saturation
- Ultra low buzz noise, due to composite construction
- IHLP design. PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **APPLICATIONS**

- PDA/notebook/desktop/server applications
- High current POL converters
- Low profile, high current power supplies
- · Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)



DESCRIPTION							
IHLP-6767DZ-11	4.7 µH	<b>± 20</b> %	ER	e3			
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC LEAD (Pb)-FREE STANDARD			
GLOBAL PART NUMBER							
I H L	P 6 7	6 7 D Z	E R 4	R 7 M 1 1			
PRODUCT FAM	ILY	SIZE	PACKAGE IN CODE	DUCTANCE TOL. SERIES VALUE			
PATENT(S): www.vishav.com/patents							

#### This Vishay product is protected by one or more United States and international patents.

Revision: 12-Oct-17

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Document Number: 34306

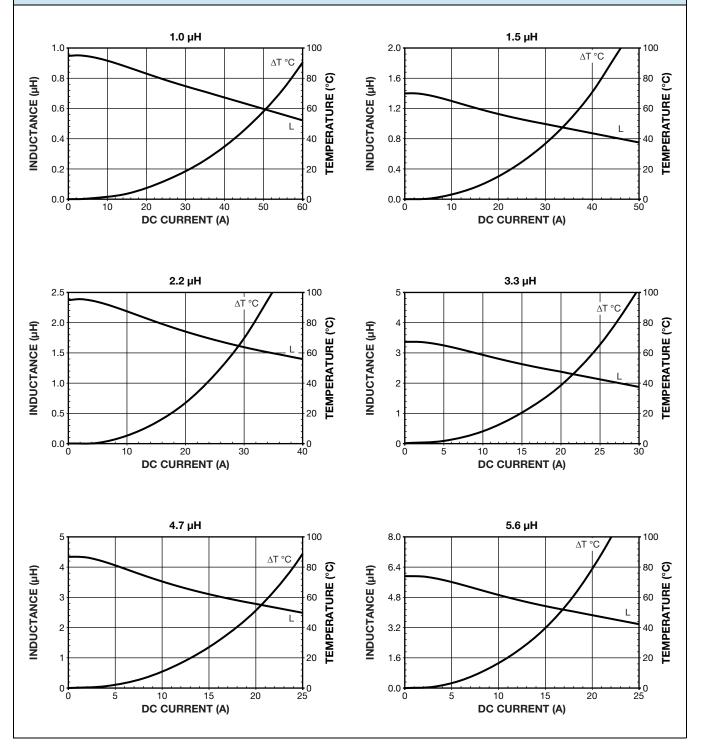






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#### **PERFORMANCE GRAPHS**

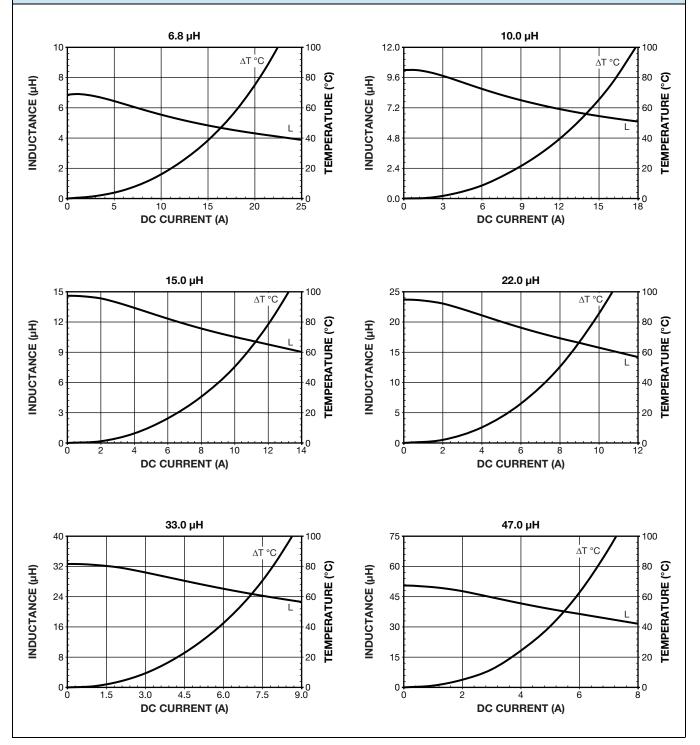


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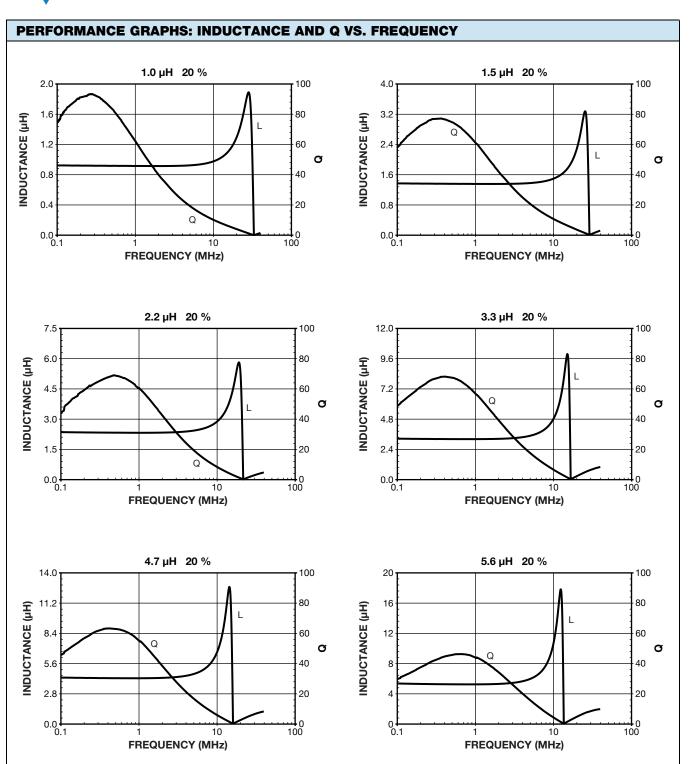
### **PERFORMANCE GRAPHS**



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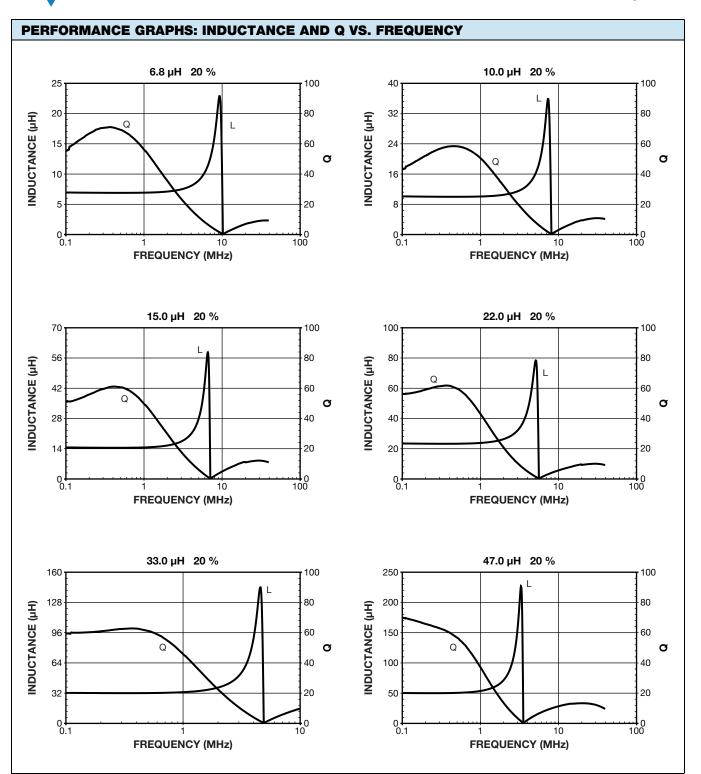


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