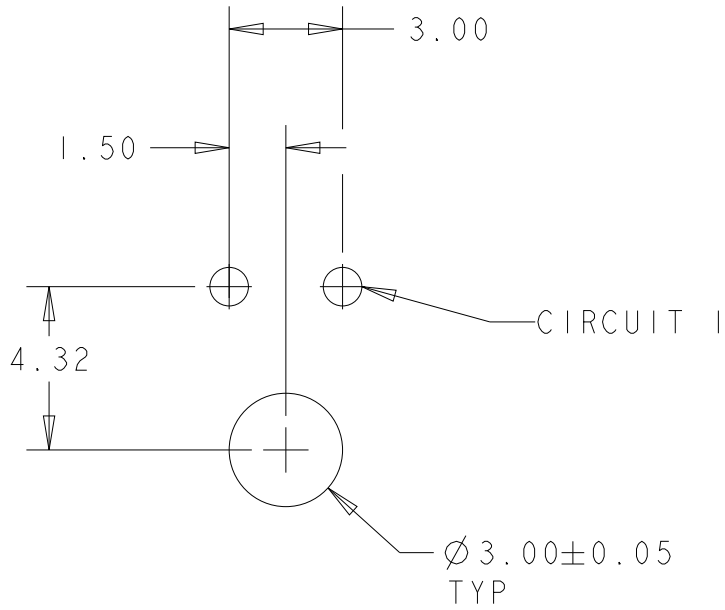


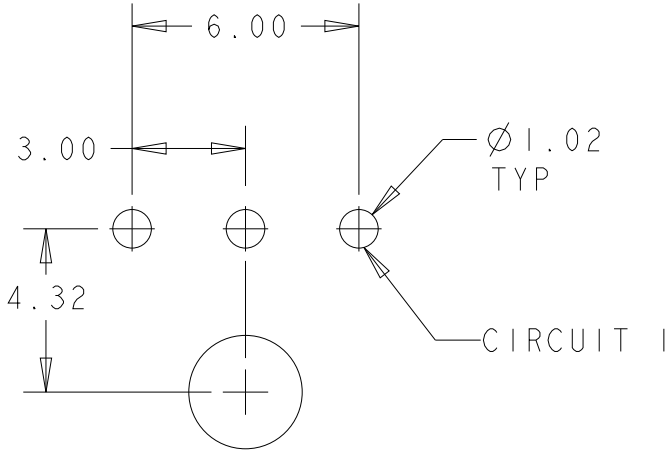
LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
CM	00		-	SEE SHEET 1	-	-	-

D

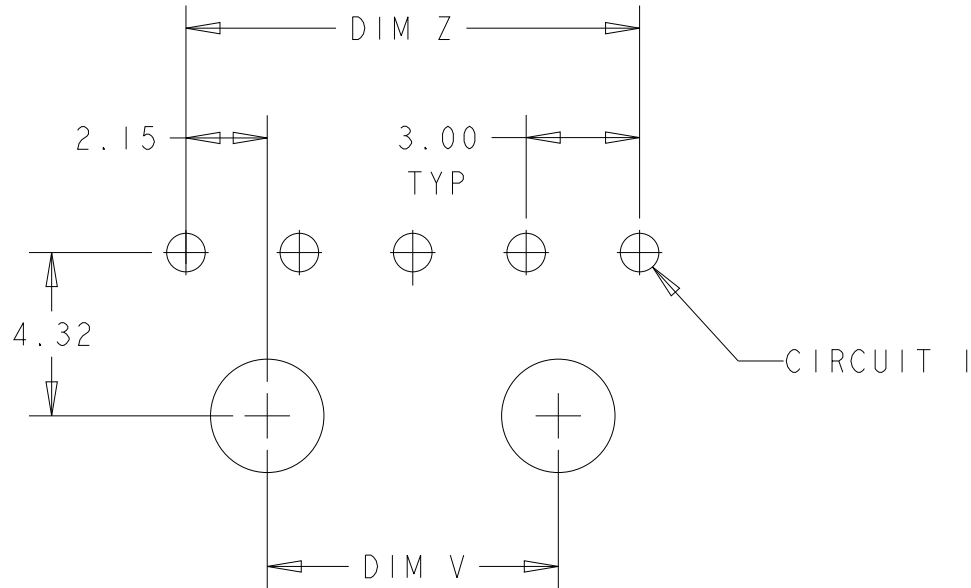
C



PCB LAYOUT FOR 2 POSITION 
COMPONENT SIDE



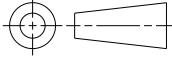
PCB LAYOUT FOR 3 POSITION 
COMPONENT SIDE



PCB LAYOUT FOR 4-12 POSITION 
COMPONENT SIDE

B

A

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN J. ROOT 09OCT2000	 TE Connectivity			
DIMENSIONS:		CHK K. WHITAKER 17-APR-2001				
mm	TOLERANCES UNLESS OTHERWISE SPECIFIED:	APVD C. JONES 17-APR-2001	NAME			
	0 PLC ± 1 PLC ± 2 PLC ±0.13 3 PLC ± 4 PLC ± ANGLES ± FINISH	PRODUCT SPEC - APPLICATION SPEC - WEIGHT -	RIGHT ANGLE THRU HOLE HEADER ASSY, 0.38 MIC GOLD CONTACTS, WITH PLASTIC HOLDDOWNS, SGL ROW, MICRO MATE-N-LOK™			
MATERIAL	SEE TABLE -	CUSTOMER DRAWING	SIZE A2	CAGE CODE 00779	DRAWING NO C-1445089	RESTRICTED TO -
				SCALE 5:1	SHEET 2	OF 2
				REV A2		

D

C

B

A

Mouser Electronics

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