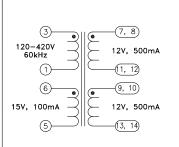
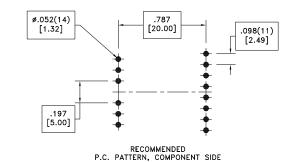


.874 MAX. [22.20]

LOT CODE & DATE CODE .020 SQ.(14) [.50] PART MUST INSERT FULLY TO SURFACE A IN RECOMMENDED GRID









ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE
D.C. RESISTANCE	1-3	@20°C	1.60 ohms max.
D.C. RESISTANCE	5-6	@20°C	0.35 ohms max.
D.C. RESISTANCE	7-11	tie(7+8, 11+12), @20°C	0.09 ohms max.
D.C. RESISTANCE	9-13	tie(9+10, 13+14), @20°C	0.09 ohms max.
INDUCTANCE	1-3	10kHz, 100mVAC, Ls	1.396mH ±10%
SATURATION CURRENT		20% rolloff from initial	700mA
LEAKAGE INDUCTANCE	1-3	tie(5 thru 14), 10kHz, 100mVAC, Ls	50uH max.
DIELECTRIC	1-14	tie(3+5, 7 thru 14), 4000VAC, 1 second	-
DIELECTRIC	1-6	1250VAC, 1 second	-
TURNS RATIO		(3-1):(6-5)	8:1, ±2%
TURNS RATIO		(3-1):(7-11), tie(7+8, 11+12)	9.6:1, ±2%
TURNS RATIO		(3-1):(9-13), tie(9+10, 13+14)	9.6:1, ±2%

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

REV.	DATE	Packaging Specifications	+ 1
		Method: Tray	444-
		PKG-0736	$\Psi \cup$
6B	11/12	www.we-online.com/midcom	CONVENTION PLACEMENT
6A	2/11	SEE REVISION SHEET FOR	REVISION LEVEL

Tolerances unless otherwise specified: Angles: ±1° Decimals: ±.005 [.13] Footprint: ±.001 [.03] Fractions: ±1/64

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

DRAWING TITLE

TRANSFORMER

eiSos p/n: **760871131**

PART NO.

760871131

SPECIFICATION SHEET 1 OF 1



Mouser Electronics

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