

3.3V, 10-Bit, 2-Port, NanoSwitchTM

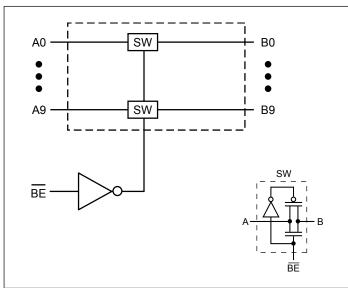
Features

- → Near-Zero propagation delay
- → 5-ohm switches connect inputs to outputs
- → Fast Switching Speed: 4.5ns (max.)
- → Ultra Low Quiescent Power (0.1µA typical)
 - Ideally suited for notebook applications
- → Packaging (Pb-free & Green):
 - 24-pin 150-mil wide plastic QSOP (Q)

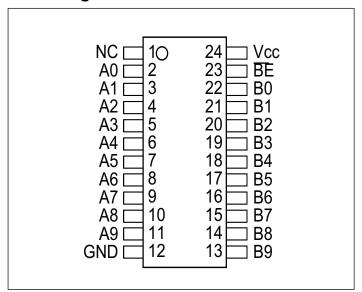
Description

Pericom Semiconductor's PI3B3861 is a 10-bit, 3.3 volt, 2-port bus switch designed with a low On-Resistance (5-ohm) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable (\overline{BE}) input signal.

Block Diagram



Pin Configuration



Truth Table(1)

Function	BE	A0-9
Disconnect	Н	Hi-Z
Connect	L	B0-9

Note: Η = High Voltage Level L = Low Voltage Level Hi-Z = High Impedance

Pin Description

Pin Name	Description	
BE	Bus Enable Input (Active LOW)	
A0-9	Bus A	
B0-9	Bus B	
GND	Ground	
V _{CC}	Power	

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Absolute Maximum Ratings

Parameter		Max.	Units
Storage Temperature		150	°C
Ambient Temperature with Power Applied		85	°C
Supply Voltage to Ground Potential		4.6	V
DC Input Voltage		4.6	V
DC Output Current		120	mA
Power Dissipation		0.5	W

Stress beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40$ °C to +85°C, $V_{CC} = 3.3V \pm 10$ %)

Parameters	Description	Test Conditions(1)	Min	Typ (2)	Max	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
$V_{\rm IL}$	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I _{IH}	Input HIGH Current	GH Current $V_{CC} = Max., V_{IN} = V_{CC}$			±1	μΑ
I_{IL}	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I _{OZH}	High Impedance Output Current	$0 \le A, B \le V_{CC}$			±1	μΑ
V_{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18 \text{ mA}$			-1.2	V
Ron	Switch On Resistance ⁽³⁾	$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA \label{eq:VCC}$ or $64mA$		5	8	Ω
		$V_{CC} = Min, V_{IN} = 2.4V, I_{ON} = 15mA$		10	17	

Notes:

Capacitance ($T_A = 25^{\circ}C$, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Typ	Units
$C_{\rm IN}$	Input Capacitance	$V_{IN} = 0V$	3.0	pF
C_{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0V$	8.0	pF
C _{ON}	A/B Capacitance, Switch On	$V_{IN} = 0V$	16.0	pF

Notes

1. This parameter is determined by device characterization but is not production tested.

PS8148i

 $^{1.} For Max. \ or Min. \ conditions, use appropriate value \ specified \ under \ Electrical \ Characteristics \ for \ the \ applicable \ device \ type.$

^{2.} Typical values are at V_{CC} = 3.3V, T_A = 25°C ambient and maximum loading.

^{3.} Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.



Power Supply Characteristics

Parameters	Description	Test Conditions(1)		Min	Typ (2)	Max	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	$V_{IN} = GND \text{ or } V_{CC}$		0.1	3.0	μΑ
$\Delta_{ m ICC}$	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	$V_{\rm IN} = 3.0 V^{(3)}$			750	μΑ

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, +25°C ambient.
- 3. Per TTL driven input (control input only); A and B pins do not contribute to I_{CC}.

Switching Characteristics over Operating Range

			Com.		
Parameters	Description	Test Conditions(1)	Min	Max	Units
t _{PLH} t _{PHL}	Propagation Delay ^(2,3) Ax to Bx, Bx to Ax			0.25	
t _{PZH}	Bus Enable Time BE to Ax or Bx	$CL = 50 \text{ pF}$ $RL = 500\Omega$	1	4.5	ns
t _{PHZ} t _{PLZ}	Bus Disable Time BE to Ax or Bx		1	4.5	

Notes:

- 1. See test circuit and wave forms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Applications Information

Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, IN may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail* minimizes power consumption.

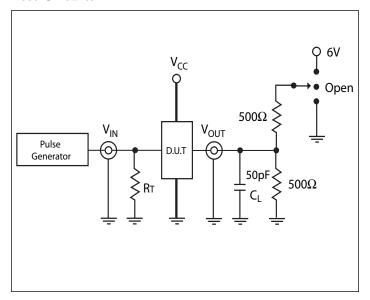
Power-Supply Sequencing and Hot-Plug Information

Proper power-supply sequencing is recommended for all CMOS devices. Always apply V_{CC} and GND before applying signals to input/output or control pins.

Rail-to-Rail is a registeredtrademark of Nippon Motorola, Ltd.



Test Circuits



Switch Position

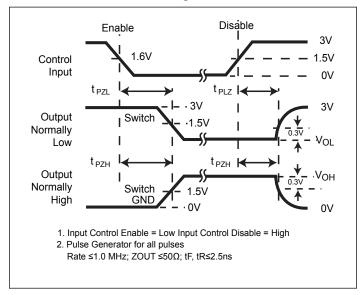
Test	Switch
Disable LOW	6V
Enable LOW	6V
Disable HIGH	GND
Enable HIGH	GND
t_{PD}	Open

Definitions:

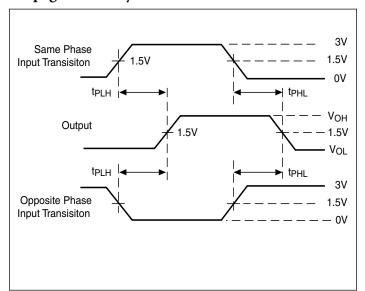
CL = Load capacitance (includes jig and probe capacitance)

RT = Termination resistance (should be equal to ZOUT of the pulse generator)

Enable and Disable Timing

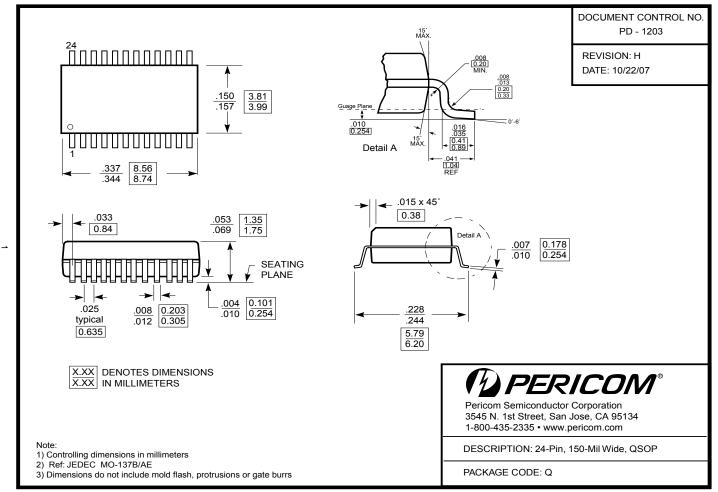


Propagation Delay





Packaging Mechanical: 24-pin QSOP (Q)



07-0475

Ordering Information

Ordering Code	Package Code	Package Type
PI3B3861QE	Q	Pb-free & Green, 24-pin QSOP

^{1.} Thermal characteristics can be found on the company web site at www.pericom.com/packaging/

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Diodes Incorporated:
PI3B3861QEX PI3B3861LEX