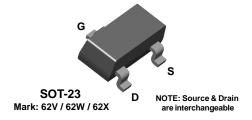


**J210 J211 J212** 

# MMBFJ210 MMBFJ211 MMBFJ212





## **N-Channel RF Amplifier**

This device is designed for HF/VHF mixer/amplifier and applications where Process 50 is not adequate. Sufficient gain and low noise for sensitive receivers. Sourced from Process 90.

#### **Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	25	V
V <sub>GS</sub>	Gate-Source Voltage	- 25	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> ,T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

#### **Thermal Characteristics** TA = 25°C unless otherwise noted

Symbol	Characteristic		Max	Units
		J210-212	*MMBFJ210-212	
P <sub>D</sub>	Total Device Dissipation	350	225	mW
	Derate above 25°C	2.8	1.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

200

μmhos

(continued)

|--|

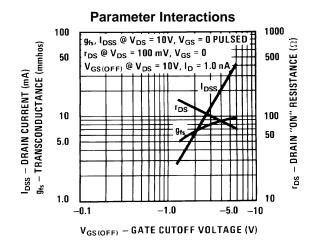
TA = 25°C unless otherwise noted

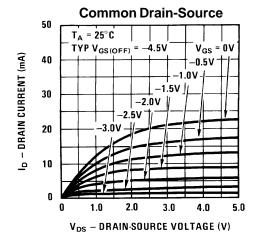
Symbol	Parameter	Test Conditions	Min	Max	Units
055.0114	DA OTEDIOTION	•			
OFF CHA	RACTERISTICS				
$V_{(BR)GSS}$	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu\text{A},  V_{DS} = 0$	- 25		V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = 15 \text{ V}, V_{DS} = 0$		- 100	pА
V <sub>GS(off)</sub>	Gate-Source Cutoff Voltage	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1.0 nA 210 211 212	-1.0 - 2.5 - 4.0	-3.0 - 4.5 - 6.0	V V V
ON CHAR	ACTERISTICS    Zero-Gate Voltage Drain Current*	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 <b>210</b>	2.0	15	
		211			m A
		212	7.0 15	20 40	m A m A m A
SMALL SI	GNAL CHARACTERISTICS  Common Source Forward Transconductance				m A

 $V_{DS} = 15 \text{ V}, V_{GS} = 0, f = 1.0 \text{ kHz}$ 

### **Typical Characteristics**

Common Source Output

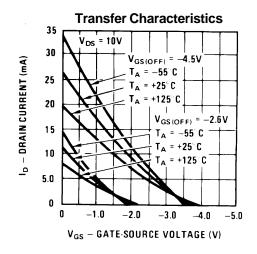


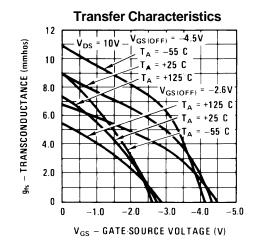


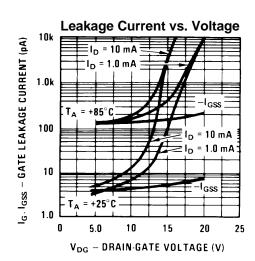
<sup>\*</sup>Pulse Test: Pulse Width  $\leq$  300  $\mu$ S

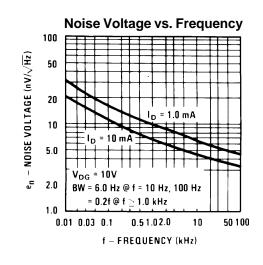
(continued)

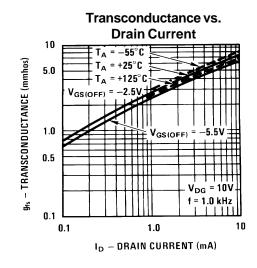
### Typical Characteristics (continued)

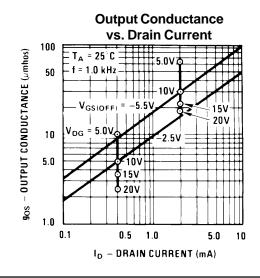






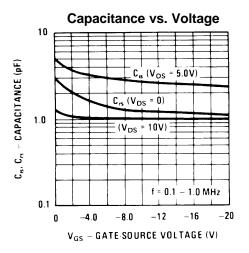




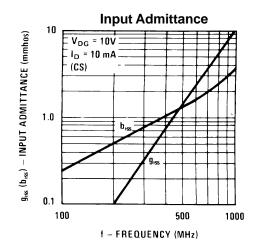


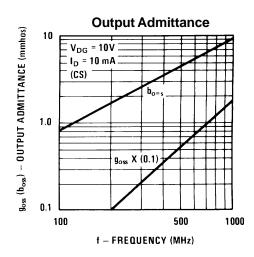
(continued)

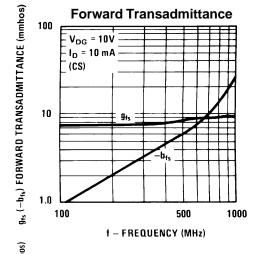
### Typical Characteristics (continued)

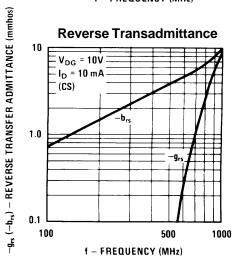


#### **Common Source Characteristics**



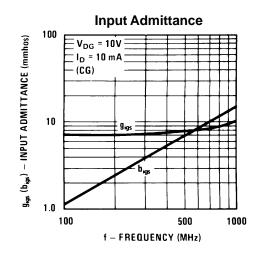


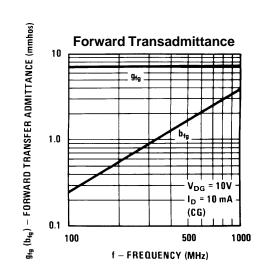


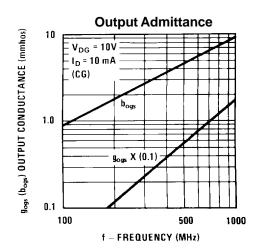


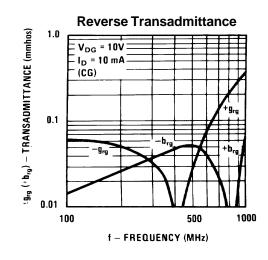
(continued)

#### **Common Gate Characteristics**









#### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

FAST® ACEx™ OPTOPLANAR™ SuperSOT™-3 FASTr™ PACMAN™ SuperSOT™-6 Bottomless™ POPTM CoolFET™ FRFET™ SuperSOT™-8 CROSSVOLT™ SyncFET™ GlobalOptoisolator™ PowerTrench ®  $\mathsf{GTO}^\mathsf{TM}$ TinyLogic™ DenseTrench™ QFET™ UHC™  $HiSeC^{TM}$  $QS^{TM}$ DOME™ EcoSPARK™ ISOPLANAR™ QT Optoelectronics™ UltraFET®  $VCX^{TM}$ E<sup>2</sup>CMOS<sup>TM</sup> LittleFET™ Quiet Series™

EnSigna™ MicroFET™ SILENT SWITCHER® FACT™ MICROWIRE™ SMART START™

FACT Quiet Series™ OPTOLOGIC™ Stealth™

#### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

**ON Semiconductor:** 

J210\_D27Z J210\_Q J212\_Q J210