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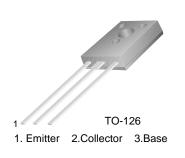
July 2011



BD433/435/437 NPN Epitaxial Silicon Transistor

Features

- Medium Power Linear and Switching Applications
- Complement to BD434, BD436 and BD438 respectively



Ordering Information

Part Number	Marking	Package	Packing Method	Remarks
BD433S	BD433	TO-126	BULK	
BD435S	BD435	TO-126	BULK	
BD435STU	BD435	TO-126	RAIL	
BD437S	BD437	TO-126	BULK	

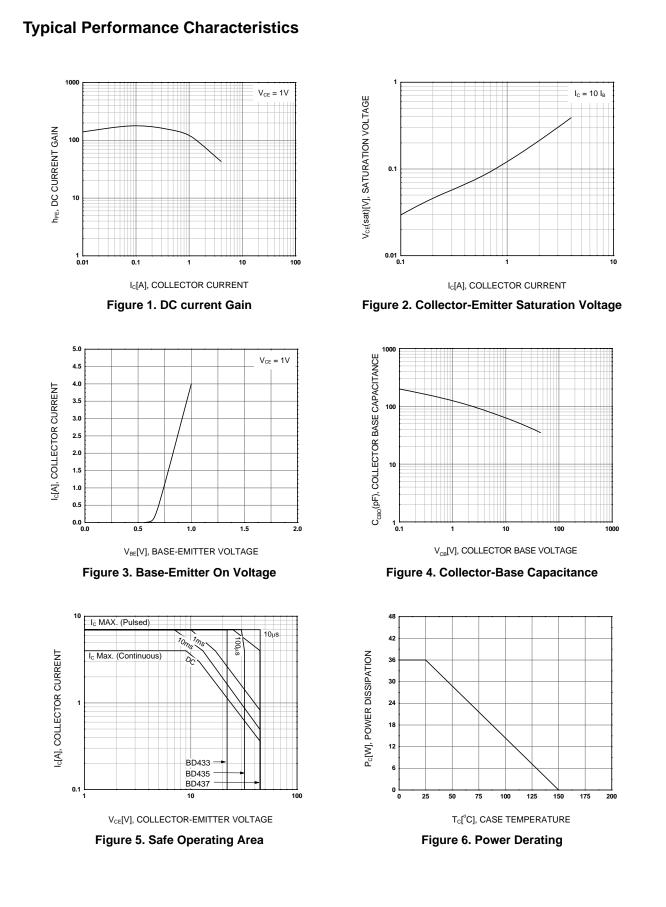
* The suffix "S" of FSID denotes TO126 package.

Absolute Maximum Ratings $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage			
	: BD433	22	V	
	: BD435	32	V	
	: BD437	45	V	
V _{CES}	Collector-Emitter Voltage			
	: BD433	22	V	
	: BD435	32	V	
	: BD437	45	V	
V _{CEO}	Collector-Emitter Voltage			
	: BD433	22	V	
	: BD435	32	V	
	: BD437	45	V	
V _{EBO}	Emitter-Base Voltage	5	V	
۱ _C	Collector Current (DC)	4	A A	
I _{CP}	*Collector Current (Pulse)	7		
I _B	Base Current	1	A	
P _C	Collector Dissipation ($T_C = 25^{\circ}C$)	36	W	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 65 to 150	°C	

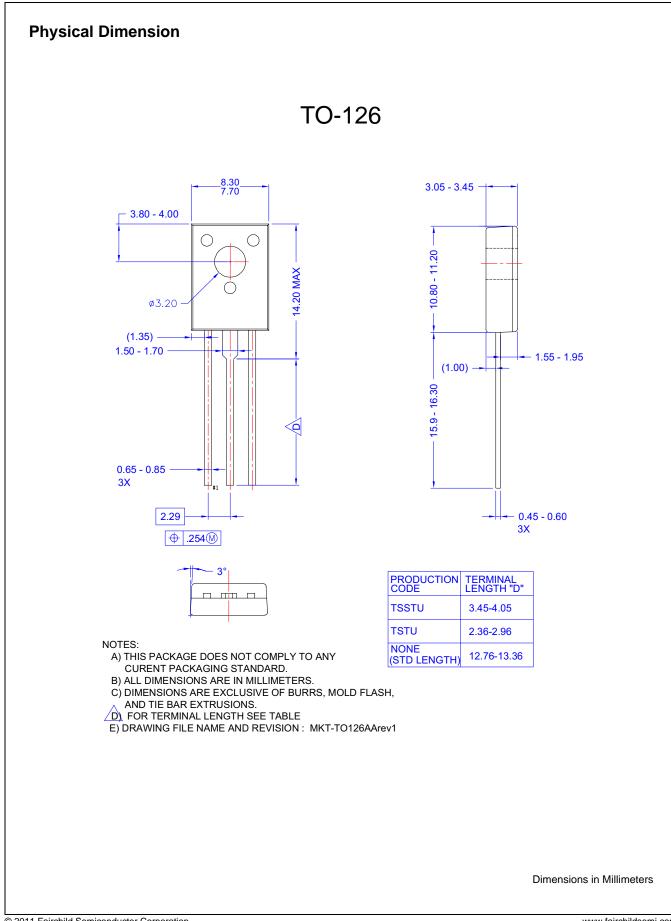
Symbol	Parameter	Test Condition	Min.	Тур.	Max. 100 100 100 100 100 100	Units V V μA μA μA μA
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage : BD433 : BD435 : BD437	$I_{\rm C} = 100 {\rm mA}, I_{\rm B} = 0$	22 32 45			
I _{CBO}	Collector Cut-off Current : BD433 : BD435 : BD437	$V_{CB} = 22V, I_E = 0$ $V_{CB} = 32V, I_E = 0$ $V_{CB} = 45V, I_E = 0$				
I _{CEO}	Collector Cut-off Current : BD433 : BD435 : BD437	$V_{CE} = 22V, V_{BE} = 0$ $V_{CE} = 32V, V_{BE} = 0$ $V_{CE} = 45V, V_{BE} = 0$				
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 5V, I_{C} = 0$			1	mA
h _{FE}	* DC Current Gain : BD433/435 : BD437 : ALL DEVICE : BD433/435 : BD437	$V_{CE} = 5V, I_{C} = 10mA$ $V_{CE} = 1V, I_{C} = 500mA$ $V_{CE} = 1V, I_{C} = 2A$	40 30 85 50 40	130 130 140		
V _{CE} (sat)	* Collector-Emitter Saturation Volt- age : BD433 : BD435 : BD437	$I_{\rm C} = 2$ A, $I_{\rm B} = 0.2$ A		0.2 0.2 0.2	0.5 0.5 0.6	V V V
V _{BE} (on)	* Base-Emitter ON Voltage : BD433 : BD435 : BD437	$V_{CE} = 1V, I_{C} = 2A$			1.1 1.1 1.2	V V V
f _T	Current Gain Bandwidth Product	V _{CE} = 1V, I _C = 250mA	3			MH

Pulse Test: PW \leq 300µs, duty Cycle \leq 1.5% Pulsed



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BD433/435/437 — NPN Epitaxial Silicon Transistor

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Product Status	Definition	
Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.	
First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.	
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