

# SFT1345

## Power MOSFET -100V, 275mΩ, -11A, Single P-Channel



ON Semiconductor®

www.onsemi.com

This P-Channel Power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and low on resistance. This device is suitable for applications with low gate charge driving or low on resistance requirements.

### Features

- Low On-Resistance
- 4V drive
- 100% Avalanche Tested
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance

### Typical Applications

- Reverse Battery Protection
- Load Switch

### SPECIFICATIONS

**ABSOLUTE MAXIMUM RATING** at Ta = 25°C (Note 1, 2)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V <sub>DSS</sub>	-100	V
Gate to Source Voltage	V <sub>GSS</sub>	±20	V
Drain Current (DC)	I <sub>D</sub>	-11	A
Drain Current PW ≤ 10μs, duty cycle ≤ 1%	I <sub>DP</sub>	-44	A
Power Dissipation	P <sub>D</sub>	1.0	W
		T <sub>c</sub> =25°C 35	W
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

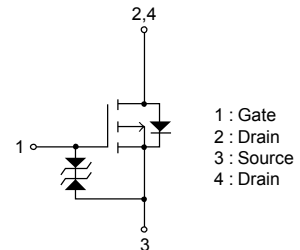
### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Case Steady State	R <sub>θJC</sub>	3.57	°C/W
Junction to Ambient (Note 2)	R <sub>θJA</sub>	125	

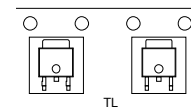
Note 2 : Insertion mounted

V <sub>DSS</sub>	R <sub>DS(on)</sub> Max	I <sub>D</sub> Max
-100V	275mΩ@ -10V	-11A
	315mΩ@ -4.5V	
	330mΩ@ -4V	

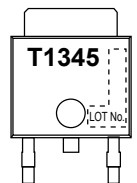
### ELECTRICAL CONNECTION P-Channel



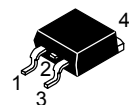
### PACKING TYPE : TL



### MARKING



IPAK(TP)



DPAK(TP-FA)

### ORDERING INFORMATION

See detailed ordering and shipping information on page 6 of this data sheet.

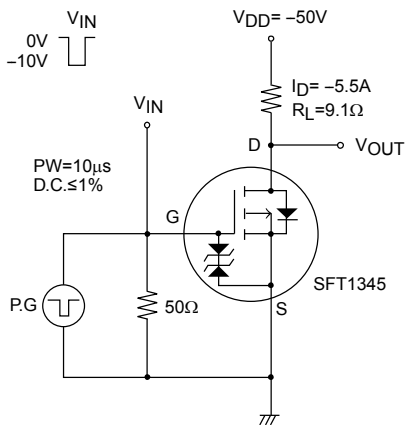
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## ELECTRICAL CHARACTERISTICS at Ta = 25°C (Note 3)

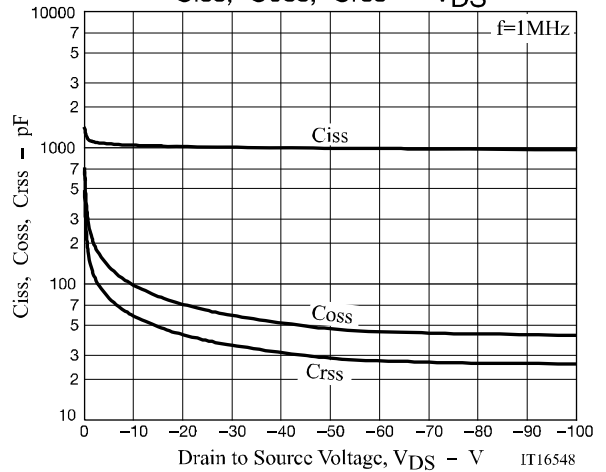
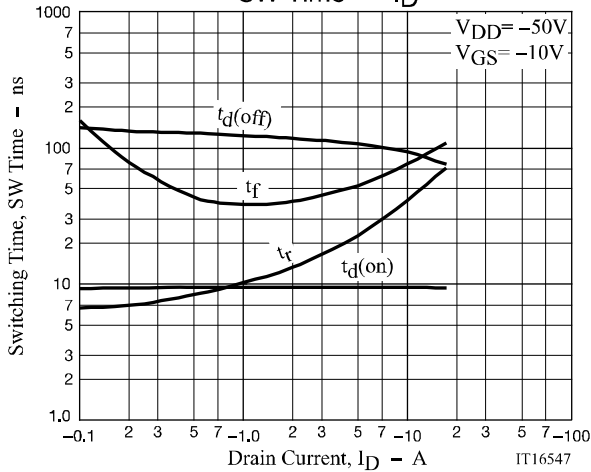
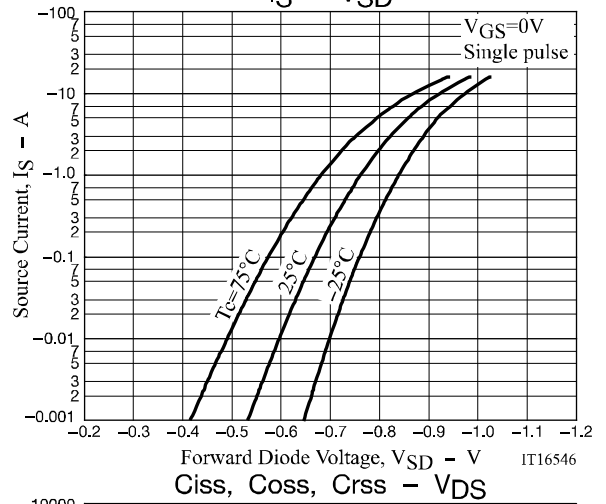
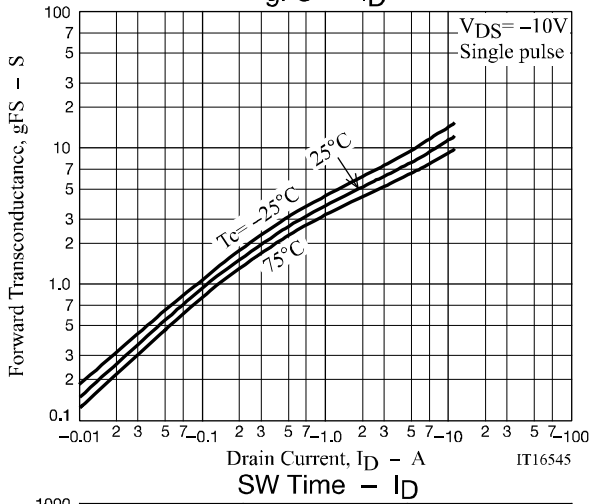
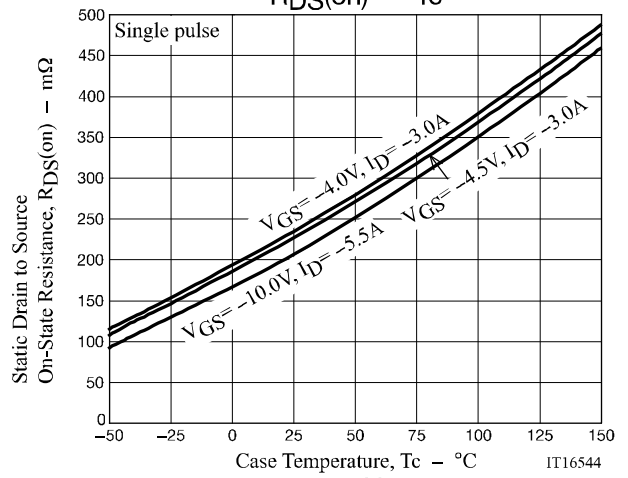
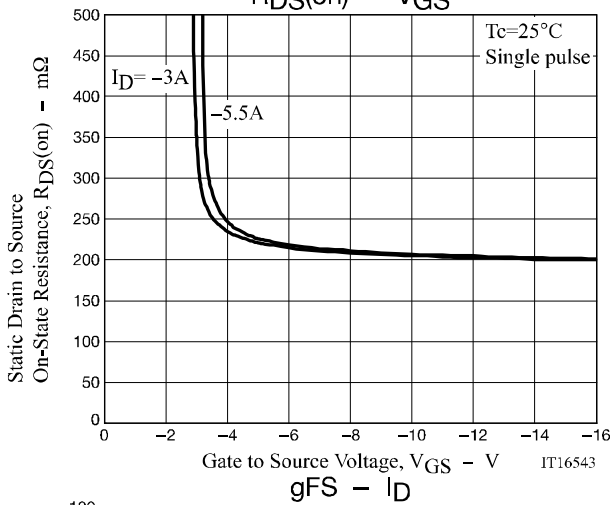
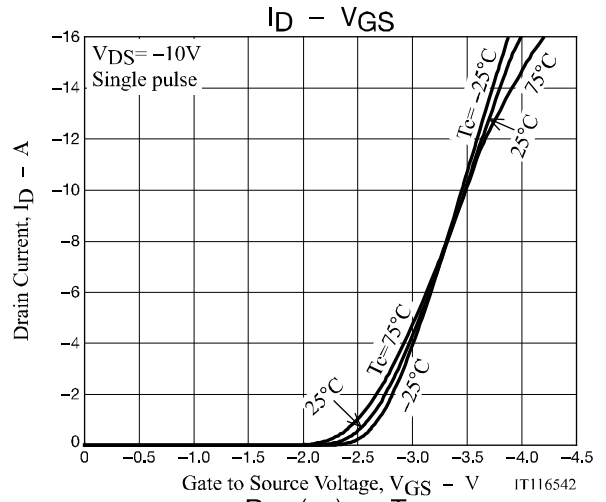
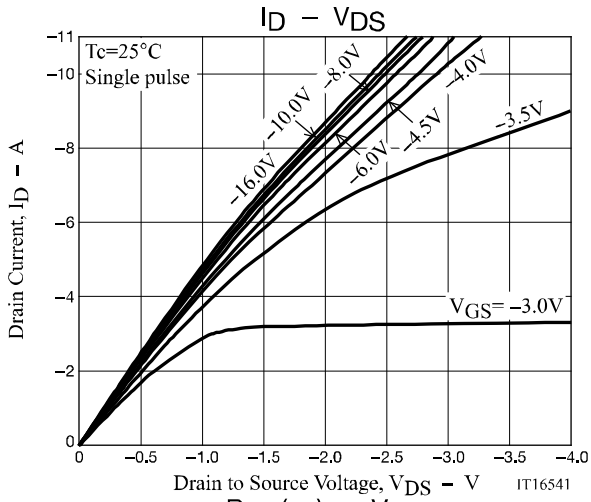
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0V	-100			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V			-1	μA
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.2		-2.6	V
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-5.5A		8.5		S
Static Drain to Source On-State Resistance	R <sub>DS(on)1</sub>	I <sub>D</sub> =-5.5A, V <sub>GS</sub> =-10V		210	275	mΩ
	R <sub>DS(on)2</sub>	I <sub>D</sub> =-3A, V <sub>GS</sub> =-4.5V		225	315	mΩ
	R <sub>DS(on)3</sub>	I <sub>D</sub> =-3A, V <sub>GS</sub> =-4V		235	330	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-20V, f=1MHz		1020		pF
Output Capacitance	C <sub>oss</sub>			72		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			43		pF
Turn-ON Delay Time	t <sub>d(on)</sub>		See specified Test Circuit		9.5	
Rise Time	t <sub>r</sub>			25		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>			105		ns
Fall Time	t <sub>f</sub>			55		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-50V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-11A			21	
Gate to Source Charge	Q <sub>gs</sub>			3.6		nC
Gate to Drain "Miller" Charge	Q <sub>gd</sub>			4.5		nC
Forward Diode Voltage	V <sub>S</sub> D	I <sub>S</sub> =-11A, V <sub>GS</sub> =0V		-0.93	-1.5	V

Note 3 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

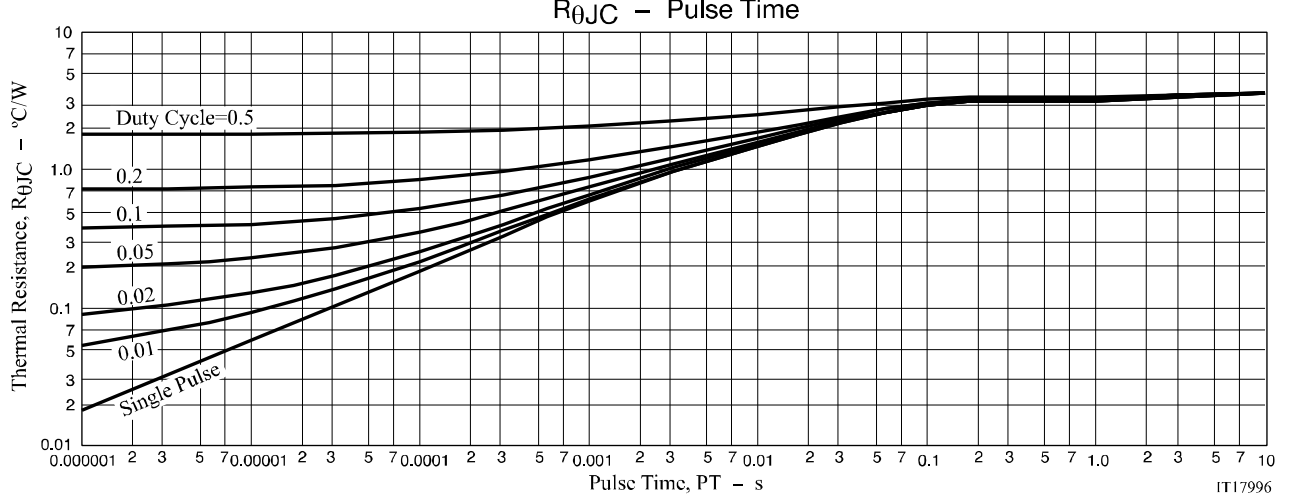
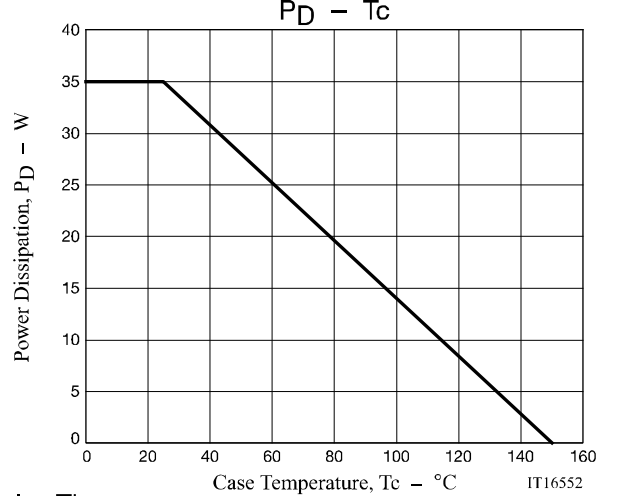
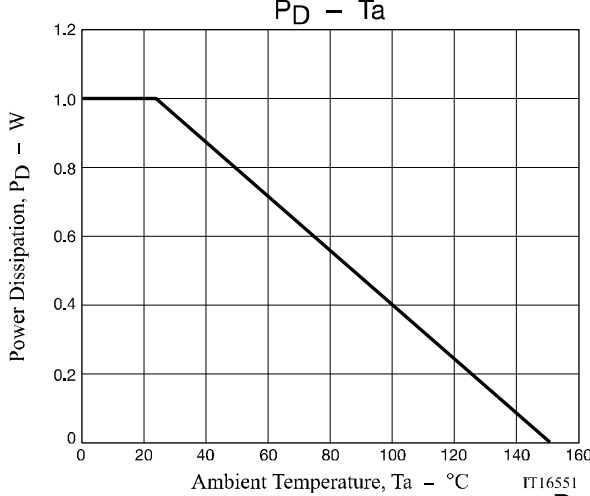
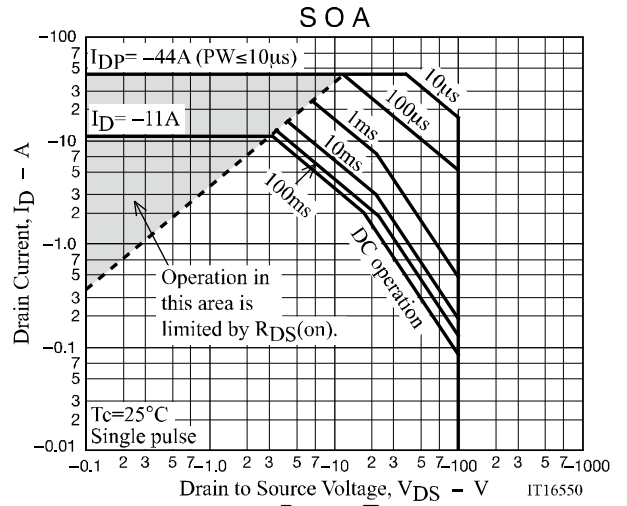
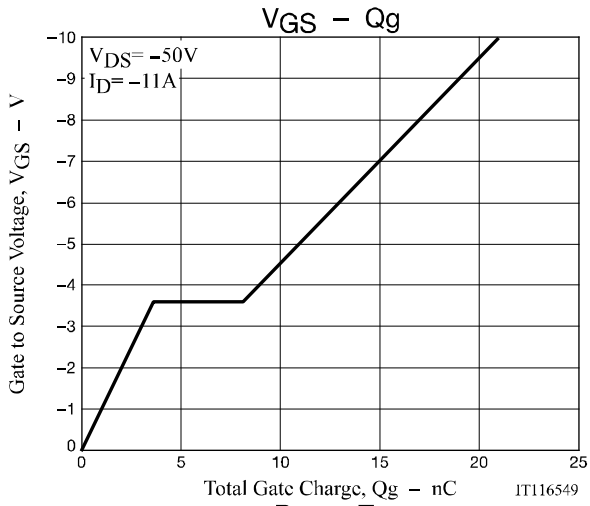
### Switching Time Test Circuit



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# SFT1345

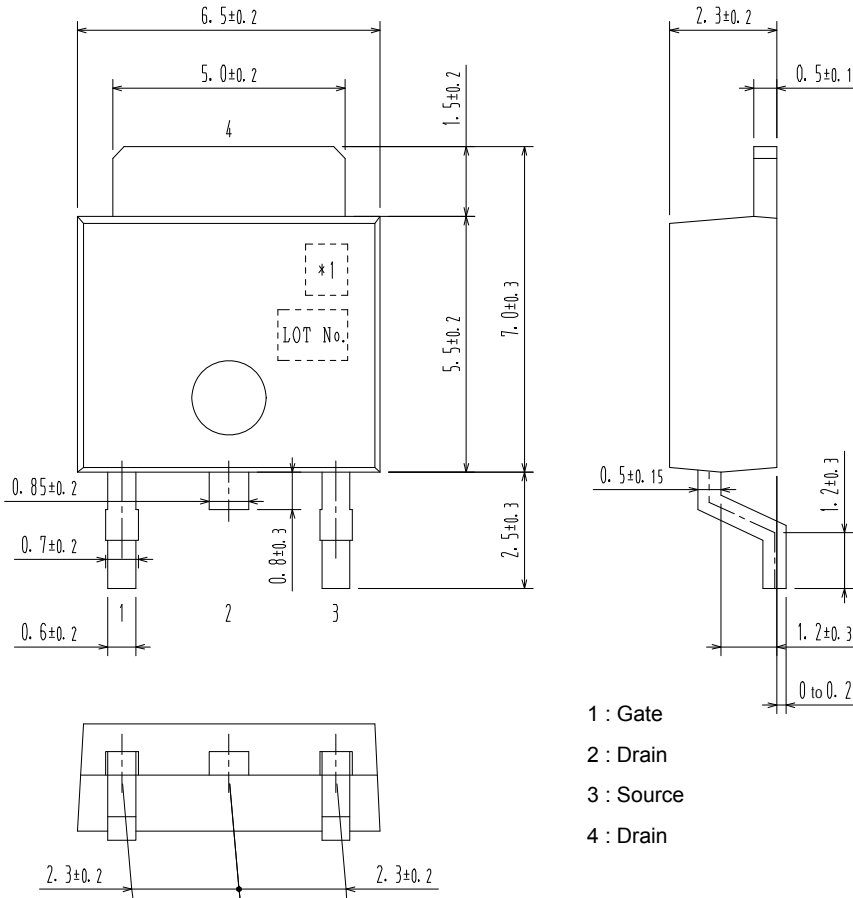


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## PACKAGE DIMENSIONS

unit : mm

DPAK / TP-FA  
CASE 369AH  
ISSUE 0

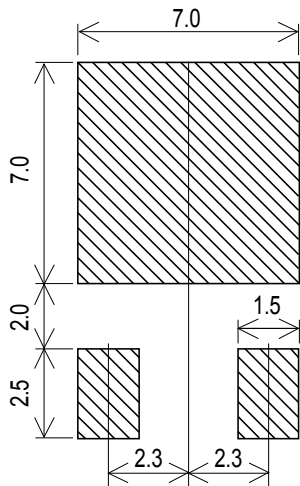


- 1 : Gate
- 2 : Drain
- 3 : Source
- 4 : Drain

Pin 2 is idle pin with electrical designation only carried.

\*1: Lot indication

## Recommended Soldering Footprint

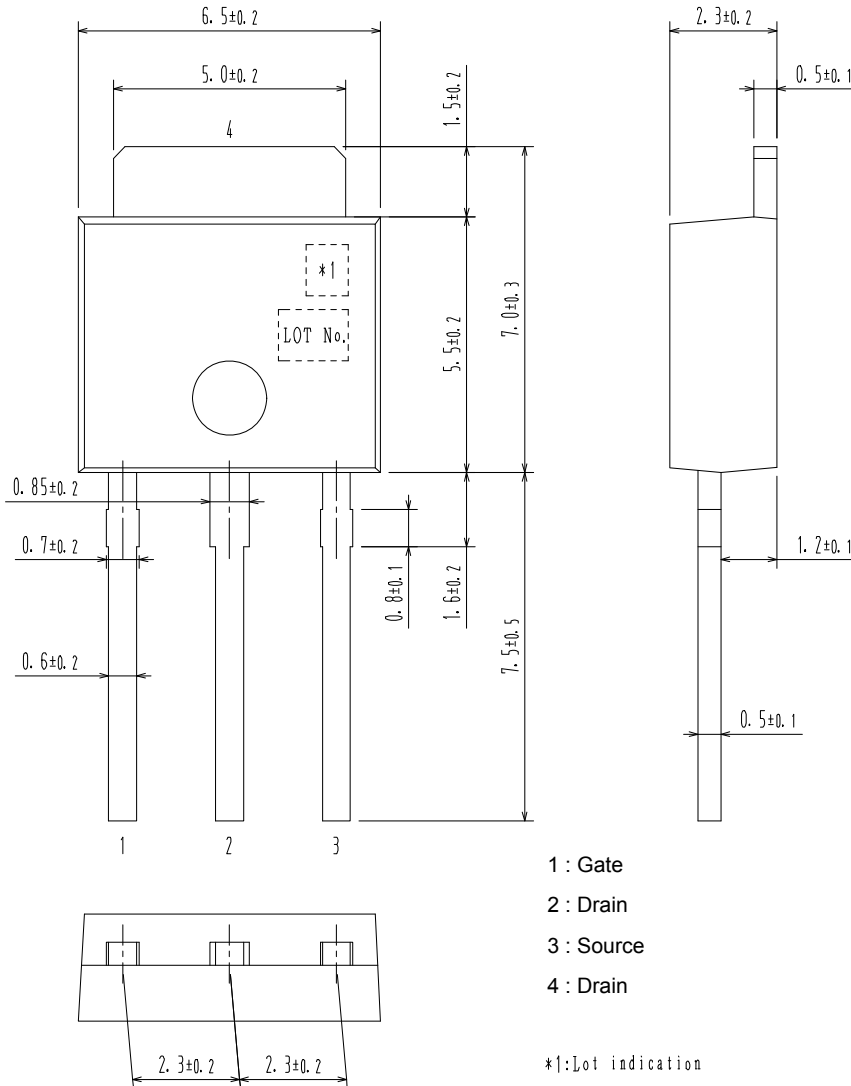


# SFT1345

## PACKAGE DIMENSIONS

unit : mm

IPAK / TP  
CASE 369AJ  
ISSUE O



## ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
SFT1345-H	T1345	IPAK / TP (Pb-Free / Halogen Free)	500 / Bag
SFT1345-TL-H	T1345	DPAK / TP-FA (Pb-Free / Halogen Free)	700 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. [http://www.onsemi.com/pub\\_link/Collateral/BRD8011-D.PDF](http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF)

Note on usage : Since the SFT1345 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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