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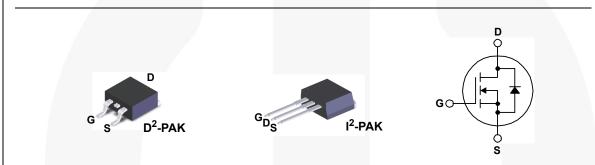
FQB8N60C / FQI8N60C N-Channel QFET® MOSFET 600 V, 7.5 A, 1.2 Ω

Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state • Low Gate Charge (Typ. 28 nC) resistance, and to provide superior switching performance • Low Crss (Typ. 12 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power • 100% Avalanche Tested factor correction (PFC), and electronic lamp ballasts.

Features

- 7.5 A, 600 V, $R_{DS(on)}$ = 1.2 Ω (Max.) @ V_{GS} = 10 V, I_D = 3.75 A
- · RoHS Compliant



Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQB8N60CTM / FQI8N60CTU	Unit V	
V _{DSS}	Drain-Source Voltage	600			
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)	7.5	А		
	- Continuous (T _C = 100°C)		4.6	А	
I _{DM}	Drain Current - Pulsed (Note 1)		30	А	
V _{GSS}	Gate-Source Voltage	± 30	V		
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		230	mJ	
I _{AR}	Avalanche Current (Note 1)		7.5	А	
E _{AR}	Repetitive Avalanche Energy (Note 1)		14.7	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5	V/ns	
	Power Dissipation $(T_A = 25^{\circ}C)^*$	3.13			
P _D	Power Dissipation ($T_C = 25^{\circ}C$)	147 V			
	- Derate above 25°C	1.18	W/°C		
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C		
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds.	300	°C		

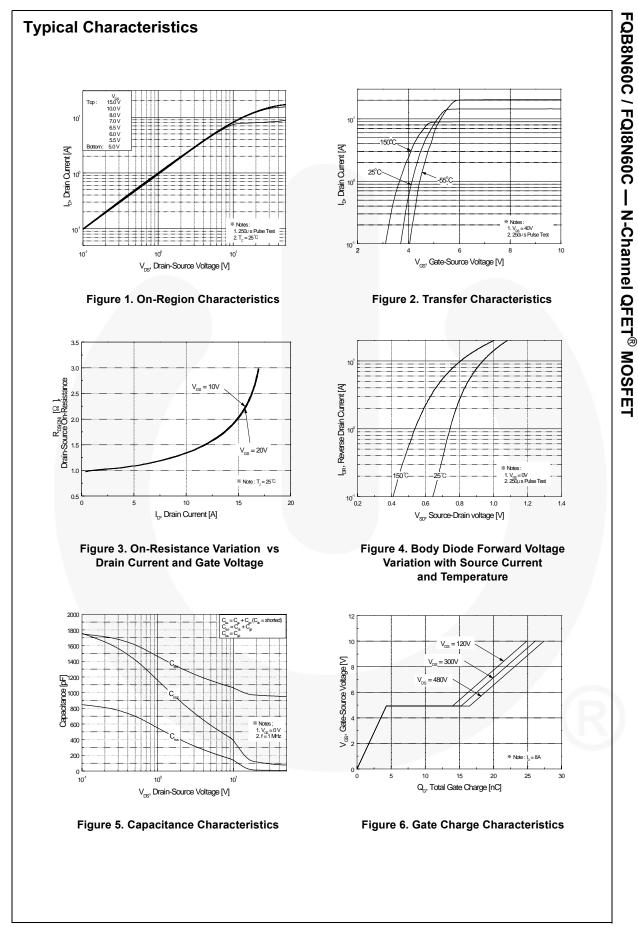
Thermal Characteristics

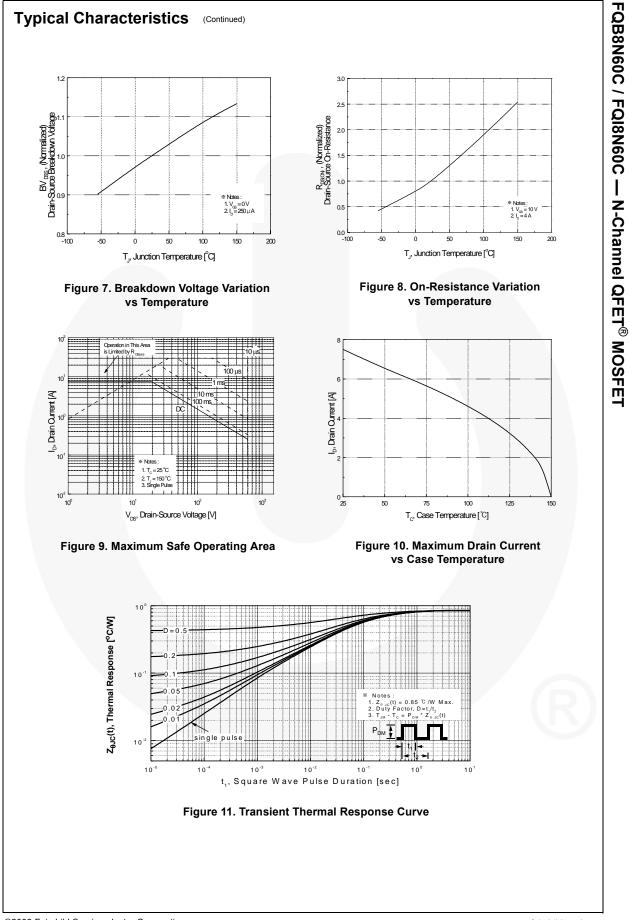
Symbol	Parameter	FQB8N60CTM / FQI8N60CTU	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	0.85	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	62.5	
	Thermal Resistance, Junction to Ambient (*1 in ² Pad of 2-oz Copper), Max.	40	

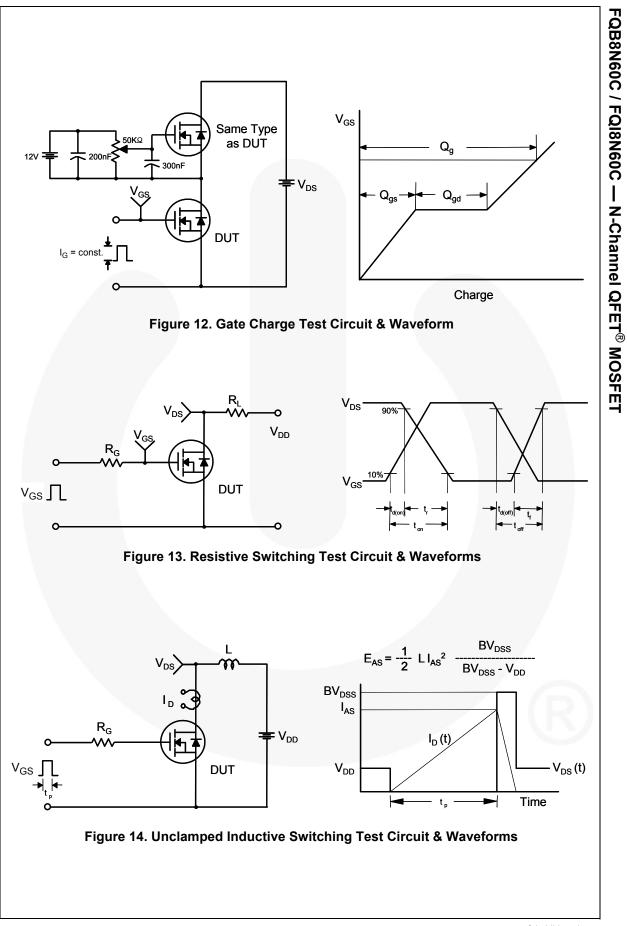
December 2013

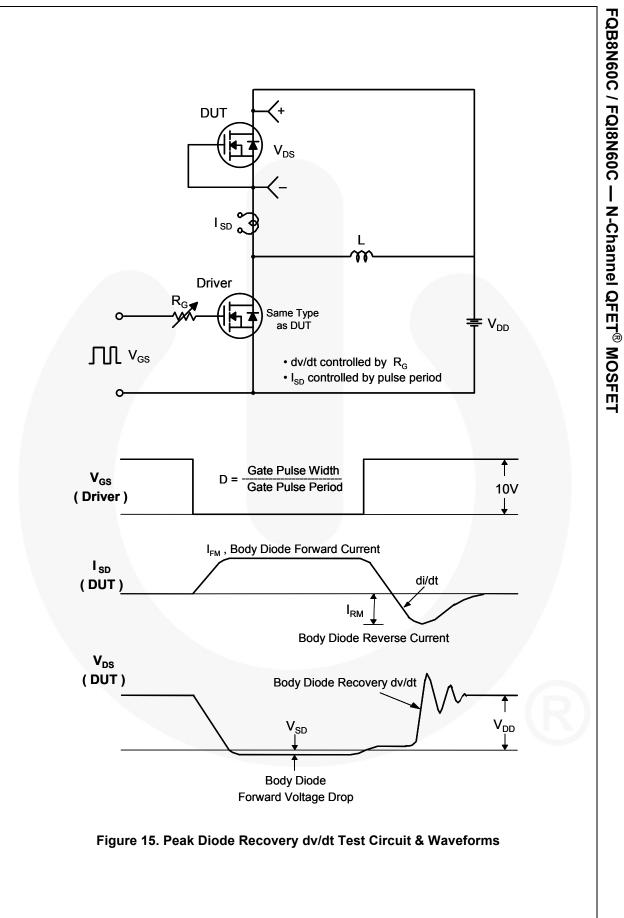
Part Number FQB8N60CTM FQI8N60CTU		Top Mark	Mark Pac		kage Packing Method		Reel Size		ïdth	Quantity
		FQB8N60C	D²-F	PAK Tape and Reel		330	mm	24 mm		800 units
		FQI8N60C I ² -P		PAK Tube		N/	A	N/A		50 units
lectri	cal Cha	racteristics	T _C = 25°0	C unless oth	erwise noted.					
Symbol		Parameter			Test Conditions		Min.	Тур.	Max.	Unit
Off Cha	aracterist	ics		•					•	
BV _{DSS}	Drain-Sou	rce Breakdown Volta	ige	$V_{GS} = 0$	0 V, I _D = 250 μA		600			V
ΔBV_{DSS} / ΔT_J	Breakdow Coefficien	n Voltage Temperatu t	re	$I_D = 250 \ \mu$ A, Referenced to 25°C			0.7		V/°C	
I _{DSS}				V _{DS} = 600 V, V _{GS} = 0 V					1	μA
	Zero Gate	Zero Gate Voltage Drain Current		$V_{\rm DS} = 480 \text{ V}, \text{ T}_{\rm C} = 125^{\circ}\text{C}$					10	μA
I _{GSSF}	Gate-Body	/ Leakage Current, F	/ Leakage Current, Forward		$30 \text{ V}, \text{ V}_{\text{DS}} = 0 \text{ V}$				100	nA
I _{GSSR}	Gate-Body	/ Leakage Current, F	Reverse	$V_{GS} = -$	30 V, V _{DS} = 0 V				-100	nA
On Cha	aracterist	ics								
V _{GS(th)}	-	shold Voltage		V _{DS} = V	/ _{GS} , I _D = 250 μA		2.0		4.0	V
R _{DS(on)}	Static Drai On-Resist			V _{GS} = ²	10 V, I _D = 3.75 A			1.0	1.2	Ω
9 _{FS}	Forward T	ransconductance		$V_{DS} = 4$	10 V, I _D = 3.75 A			8.7		S
-										
-	ic Charac						1	065	1055	~ 5
C _{iss}	Input Capa			-	25 V, V _{GS} = 0 V,			965	1255	pF
C _{oss}	Output Ca			f = 1.0	MHz			105	135	pF
C _{rss}	Reverse I	ransfer Capacitance						12	16	pF
Switch	ing Chara	acteristics								
t _{d(on)}	Turn-On D	elay Time			300 V, I _D = 7.5A,			16.5	45	ns
t _r	Turn-On F	Rise Time			$R_{\rm G} = 25 \Omega$			60.5	130	ns
t _{d(off)}	Turn-Off D	elay Time		- G -				81	170	ns
t _f	Turn-Off F	all Time		Ī		(Note 4)		64.5	140	ns
Qg	Total Gate	Charge		$V_{DS} = 4$	180 V, I _D = 7.5A,			28	36	nC
Q _{gs}	Gate-Sour	rce Charge		V _{GS} = 10 V				4.5		nC
Q _{gd}	Gate-Drai	n Charge				(Note 4)		12		nC
	1	ode Characteris			-			1	1	
I _S		Continuous Drain-S							7.5	A
I _{SM}		Pulsed Drain-Source							30	A
V _{SD}	Drain-Sou	rce Diode Forward \	oltage		0 V, I _S = 7.5 A				1.4	V
t _{rr}		Recovery Time			0 V, I _S = 7.5 A,			365		ns
Q _{rr}	Reverse F	Recovery Charge		dl _F / dt	= 100 A/μs			3.4		μC

4. Essentially independent of operating temperature.









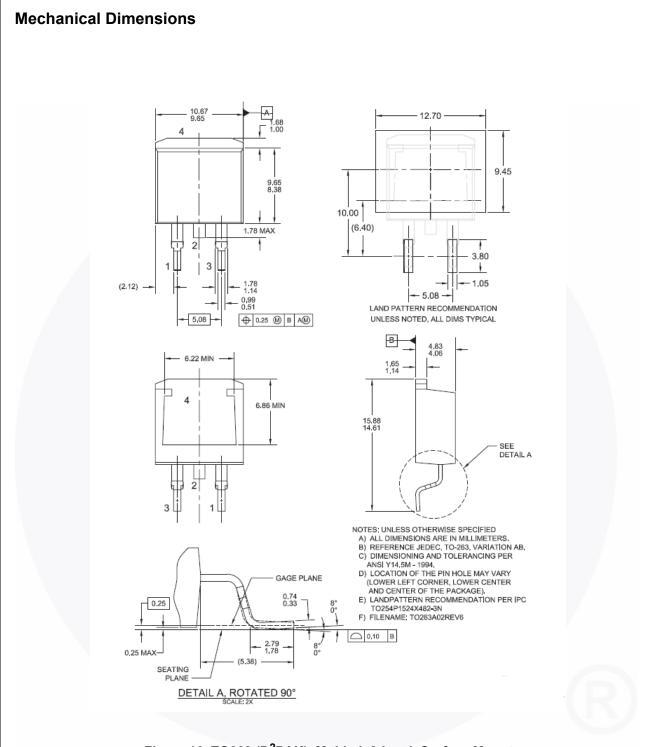
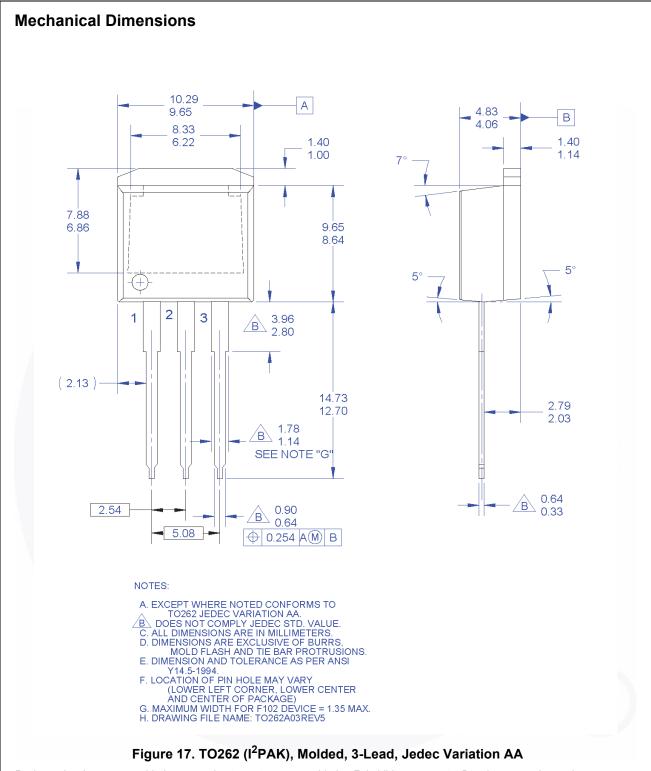


Figure 16. TO263 (D²PAK), Molded, 2-Lead, Surface Mount

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N-Channel QFET[®] MOSFET



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