

Vishay Siliconix

N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
60	0.10 at V _{GS} = 10 V	3.2		
	0.13 at V _{GS} = 4.5 V	2.8		

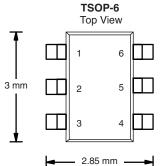


- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % Rg Tested
- Compliant to RoHS Directive 2002/95/EC

(3) G C



Available



Ordering Information: Si3458DV-T1-E3 (Lead (Pb)-free)

Si3458DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

(4) S N-Channel MOSFET

(1, 2, 5, 6) D

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise r	noted		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 25 °C	- I _D	3.2		
	T _A = 70 °C		2.5	•	
Pulsed Drain Current		I _{DM}	15	A	
Silngle Avalanche Current		I _{AS}	10		
Maximum Power Dissipation ^{a,b}	T _A = 25 °C	- P _D	2	w	
	T _A = 70 °C		1.3	V	
Operating Junction and Storage Temperature Rar	nge	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 5 s	- R _{thJA}		62.5	
Maximum Junction-to-Ambient ^a	Steady State		' 'thJA	106	
Maximum Junction-to-Lead	Steady State	R _{thJL}	35		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. t ≤ 5 s.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•	•			
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 V, I_{D} = 250 \mu A$	60			v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1				
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$		1			
		$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$			50	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	10			Α	
Drain-Source On-State Resistance ^a	Б	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3.2 \text{ A}$		0.085	0.10	Ω	
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 2.8 \text{ A}$		0.110	0.13		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 4.5 \text{ V}, I_D = 3.2 \text{ A}$		8		S	
Dynamic ^b							
Total Gate Charge	Qg			8	16	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = 30 V, V_{GS} = 10 V, I_{D} = 3.2 A		4.0			
Gate-Drain Charge	Q _{gd}			2.0			
Gate Resistance	R _g		1		3.9	Ω	
Turn-On Delay Time	t _{d(on)}			10	20	- ns	
Rise Time	t _r	V_{DD} = 30 V, R_L = 30 Ω		10	20		
Turn-Off Delay Time	t _{d(off)}	$\rm I_D \cong 1$ A, $\rm V_{GEN}$ = 10 V, $\rm R_g$ = 6 Ω		20	40		
Fall Time	t _f			10	20		
Source-Drain Rating Characteristics ^b	<u> </u>		•		·		
Continuous Current	ا _S				1.7	- A	
Pulsed Current	I _{SM}				15		
Diode Forward Voltage ^a	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V			1.2	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _E = 1.7 A, dl/dt = 100 A/μs		50	90	ns	

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

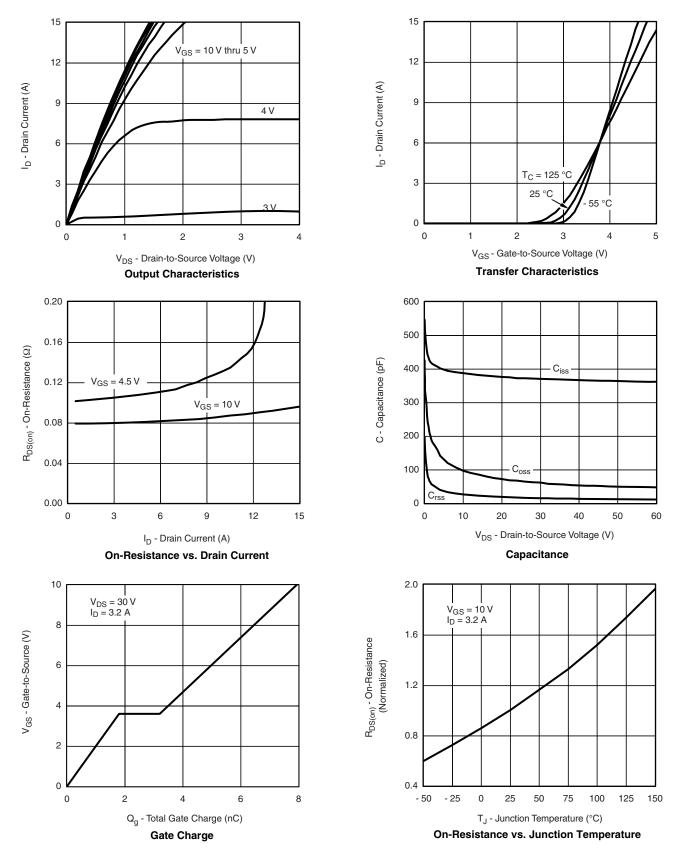
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



Si3458DV

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

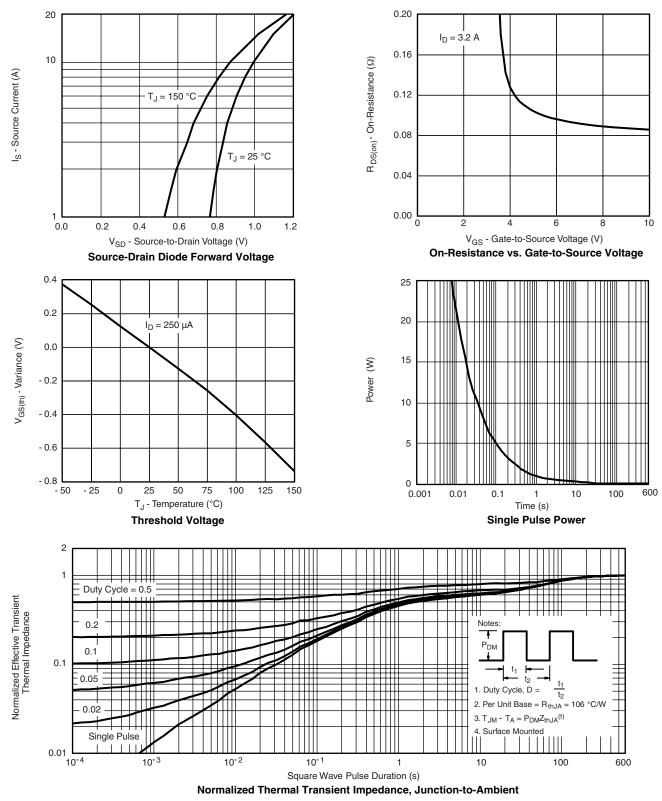


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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?70859.



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