

LOW-VOLTAGE QUAD 2:1MUX/DEMUX BUS SWITCH

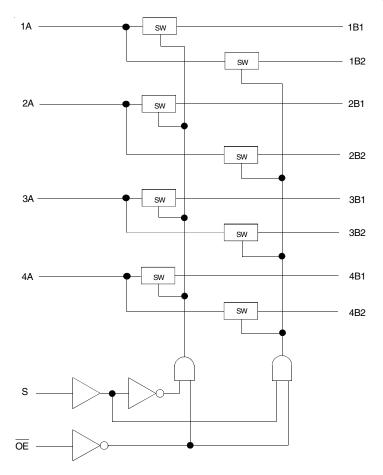
FEATURES:

- Functionally equivalent to QS3257
- + 5 $\!\Omega$ bi-directional switch connection between two ports
- · Isolation under power-off conditions
- Over-voltage tolerant
- · Latch-up performance exceeds 100mA
- Vcc = 2.3V 3.6V, Normal Range
- ESD > 2000V per MIL-STD-883, Method 3015;
 > 200V using machine model (C = 200pF, R = 0)
- Available in QSOP and TSSOP packages

APPLICATIONS:

• 3.3V High Speed Bus Switching, Multiplexing, and Bus Isolation

FUNCTIONAL BLOCK DIAGRAM



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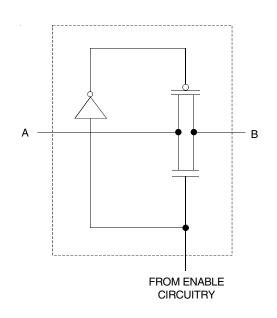
DESCRIPTION:

The CBTLV3257 is a quad 2:1 multiplexer/demultiplexer. The low onstate resistance of the switch allows connections to be made with minimal propagation delay.

The select (S) input controls the data flow. The multiplexers/demultiplexers are enabled when the output-enable (\overline{OE}) input is low.

To ensure the high-impedance state during power up or power down, \overline{OE} should be tied to Vcc through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

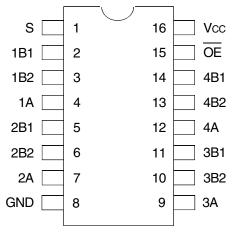
SIMPLIFIED SCHEMATIC, EACH SWITCH



DECEMBER 2014

INDUSTRIAL TEMPERATURE RANGE

PIN CONFIGURATION



QSOP / TSSOP TOP VIEW

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description Max		Unit
Vcc	SupplyVoltage Range	ange0.5 to +4.6	
Vi	Input Voltage Range	-0.5 to +4.6	V
	Continuous Channel Current	128	mA
Ік	Input Clamp Current, VI/O < 0	-50	mA
Tstg	Storage Temperature	–65 to +150	°C

NOTE:

 Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

FUNCTION TABLE⁽¹⁾

Inp	outs	
ŌĒ	S	Function
L	L	A Port = B1 Port
L	Н	A Port = B2 Port
Н	Х	Disconnect

NOTE:

1. H = HIGH Voltage Level

L = LOW Voltage Level

X = Don't Care

OPERATING CHARACTERISTICS, $TA = 25^{\circ}C^{(1)}$

Symbol	Parameter Test Conditions		Min.	Max.	Unit
Vcc	Supply Voltage		2.3	3.6	V
Vih	High-Level Control Input Voltage	Vcc = 2.3V to 2.7V	1.7	—	V
		Vcc = 2.7V to 3.6V	2	—	
VIL	Low-Level Control Input Voltage	Vcc = 2.3V to 2.7V	—	0.7	V
		Vcc = 2.7V to 3.6V	—	0.8	
TA	Operating Free-Air Temperature		-40	85	°C

NOTE:

1. All unused control inputs of the device must be held at Vcc or GND to ensure proper device operation.

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified: Operating Conditions: TA = -40 °C to +85 °C

Symbol	Parameter	Test Conditions		Min.	Typ. ⁽¹⁾	Max.	Unit
Vik	Control Inputs, Data Inputs	Vcc = 3V, II = -18mA			_	-1.2	V
li	Control Inputs	Vcc = 3.6V, VI = Vcc or 0	GND	_	_	±1	μA
loz	Data I/O	Vcc = 3.6V, Vo = 0 or 3.6	V, switch disabled	_	_	20	μA
loff		Vcc = 0, VI or Vo = 0 to 3	.6V		_	50	μA
lcc		Vcc = 3.6V, Io = 0, VI =	Vcc or GND		_	10	μA
$\Delta ICC^{(2)}$	Control Inputs	Vcc = 3.6V, one input at 3	Vcc = 3.6V, one input at 3V, other inputs at Vcc or GND			300	μA
Сі	Control Inputs	VI = 3V or 0			4	—	pF
CIO(OFF)	A Port	Vo = 3V or 0, OE = Vcc = 3.3V			13	_	pF
	B Port				6	-	
	Vcc = 2.3V	VI = 0	Io = 64mA	_	5	8	
	Typ. at Vcc = 2.5V		Io = 24mA	_	5	8	
Ron ⁽³⁾		VI = 1.7V	Io = 15mA	_	27	40	Ω
		VI = 0	Io = 64mA	_	5	7	1
	Vcc = 3V		Io = 24mA	_	5	7	1
		VI = 2.4V	Io = 15mA		10	15	1

NOTES:

1. Typical values are at Vcc = 3.3V, +25°C ambient.

2. The increase in supply current is attributable to each current that is at the specified voltage level rather than Vcc or GND.

3. This is measured by the voltage drop between the A and B terminals at the indicated current through the switch. On-state resistance is determined by the lower of the voltages of the two (A or B) terminals.

SWITCHING CHARACTERISTICS

		$Vcc = 2.5V \pm 0.2V$		$Vcc = 3.3V \pm 0.3V$		
Symbol	Parameter	Min.	Max.	Min.	Max.	Unit
tPD ⁽¹⁾	Propagation Delay	_	0.15	-	0.25	ns
	A to B or B to A					
t SEL	Select Time	1	6.1	1	5.3	ns
	S to A or B					
ten	Enable Time	1	6.1	1	5.3	ns
	S to B					
tois	DisableTime	1	4.8	1	4.5	ns
	S to B					
ten	Output Enable Time	1	5.6	1	5	ns
	OE to A or B					
tois	OutputDisableTime	1	5.5	1	5.5	ns
	OE to A or B					

NOTE:

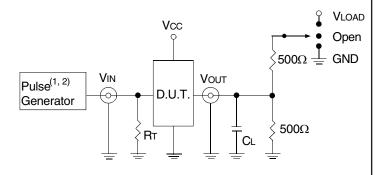
1. The propagation delay is the calculated RC time constant of the typical on-state resistance of the switch and the specified load capacitance driven by an ideal voltage source (zero output impedance).

IDT74CBTLV3257 LOW-VOLTAGE QUAD 2:1 MUX/DEMUX BUS SWITCH

TEST CIRCUITS AND WAVEFORMS

TEST CONDITIONS

Symbol	$Vcc^{(1)} = 3.3V \pm 0.3V$	Vcc ⁽²⁾ =2.5V±0.2V	Unit
VLOAD	6	2 x Vcc	V
Vih	3	Vcc	V
Vт	1.5	Vcc / 2	V
Vlz	300	150	mV
VHZ	300	150	mV
CL	50	30	pF



Test Circuits for All Outputs

DEFINITIONS:

CL = Load capacitance: includes jig and probe capacitance.

RT = Termination resistance: should be equal to ZOUT of the Pulse Generator.

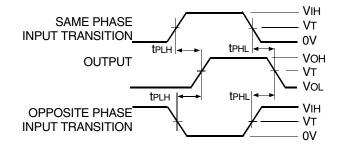
NOTES:

- 1. Pulse Generator for All Pulses: Rate \leq 10MHz; tF \leq 2.5ns; tR \leq 2.5ns.
- 2. Pulse Generator for All Pulses: Rate \leq 10MHz; tF \leq 2ns; tR \leq 2.5ns.

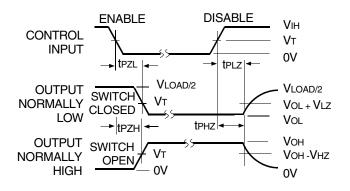
SWITCH POSITION

Test	Switch
tplz/tpzl	Vload
tрнz/tрzн	GND
tsel	Open
ted	Open

INDUSTRIAL TEMPERATURE RANGE

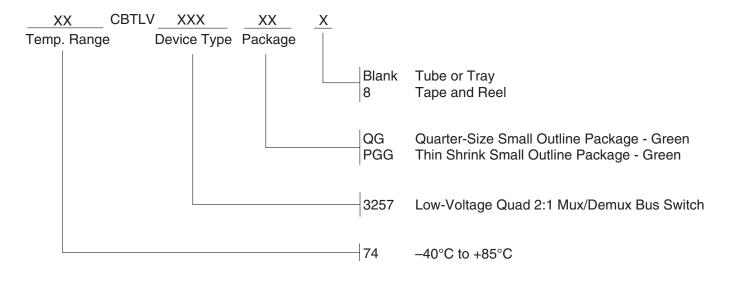






Enable and Disable Times

ORDERING INFORMATION



Datasheet Document History

12/18/2014 Pg. 5 Updated the ordering information by removing non RoHS part and by adding Tape and Reel information.



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IDT (Integrated Device Technology): 74CBTLV3257PGG 74CBTLV3257QG8 74CBTLV3257QG 74CBTLV3257PGG8