BSS84L, BVSS84L

Power MOSFET Single P-Channel SOT-23 -50 V, 10 Ω

- SOT-23 Surface Mount Package Saves Board Space
- BV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	50	Vdc
Gate-to-Source Voltage - Continuous	V_{GS}	± 20	Vdc
Drain Current Continuous @ $T_A = 25^{\circ}C$ Pulsed Drain Current $(t_p \le 10 \ \mu s)$	I _D I _{DM}	130 520	mA
Total Power Dissipation @ T _A = 25°C	P_{D}	225	mW
Operating and Storage Temperature Range	T _J , T _{stg}	– 55 to 150	°C
Thermal Resistance - Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	TL	260	°C

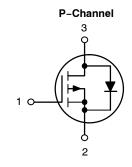
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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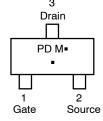
V _{(BR)DSS}	R _{DS(ON)} MAX		
-50 V	10 Ω @ 10 V		





SOT-23 **CASE 318** STYLE 21

MARKING DIAGRAM & PIN ASSIGNMENT



PD = Specific Device Code

= Date Code M = Pb-Free Package

(*Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BSS84LT1G, BVSS84LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BSS84LT7G	SOT-23 (Pb-Free)	3,500 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

С	Symbol	Min	Тур	Max	Unit		
OFF CHARACTERISTICS		•		•			
Drain-to-Source Breakdown Volt ($V_{GS} = 0 \text{ Vdc}, I_D = -250 \mu\text{Adc}$	V _{(BR)DSS}	-50	-	-	Vdc		
Zero Gate Voltage Drain Current	I _{DSS}	- - -	- - -	-0.1 -15 -60	μAdc		
Gate-Body Leakage Current (V _G	$_{\rm S} = \pm 20 \text{Vdc}, \text{V}_{\rm DS} = 0 \text{Vdc})$	I _{GSS}	-	-	±10	nAdc	
ON CHARACTERISTICS (Note 1)						
Gate-Source Threaded Voltage (V _{GS(th)}	-0.9	_	-2.0	Vdc		
Static Drain-to-Source On-Resis	R _{DS(on)}	-	4.7	10	Ω		
Transfer Admittance (V _{DS} = -25 \	y _{fs}	50	-	-	mS		
DYNAMIC CHARACTERISTICS							
Input Capacitance	V _{DS} = 5.0 Vdc	C _{iss}	_	36	-	– pF	
Output Capacitance	V _{DS} = 5.0 Vdc	C _{oss}	_	17	-		
Transfer Capacitance	V _{DG} = 5.0 Vdc	C _{rss}	=	6.5	-		
SWITCHING CHARACTERISTIC	S (Note 2)						
Turn-On Delay Time		t _{d(on)}	_	3.6	-	ns	
Rise Time	V_{DD} = -15 Vdc, I_D = -2.5 Adc, R_L = 50 Ω	t _r	_	9.7	-		
Turn-Off Delay Time	$R_L = 50 \Omega$	t _{d(off)}	_	12	-		
Fall Time		t _f	-	1.7	-		
Gate Charge	$V_{DD} = -40 \text{ Vdc}, I_D = -0.5 \text{ A}, V_{GS} = -10 \text{ V}$	Q _T	-	2.2	_	nC	
SOURCE-DRAIN DIODE CHARA	ACTERISTICS						
Continuous Current	IS	-	-	-0.130	Α		
Pulsed Current	I _{SM}	-	-	-0.520			
Forward Voltage (Note 2)	$V_{GS} = 0 \text{ V}, I_{S} = -130 \text{ mA}$	V_{SD}	_	-	-2.2	V	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 1. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
- 2. Switching characteristics are independent of operating junction temperature.

TYPICAL ELECTRICAL CHARACTERISTICS

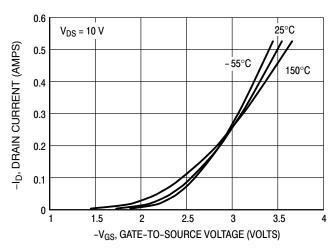


Figure 1. Transfer Characteristics

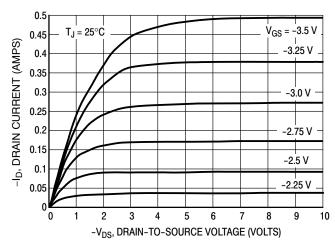


Figure 2. On-Region Characteristics

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TYPICAL ELECTRICAL CHARACTERISTICS

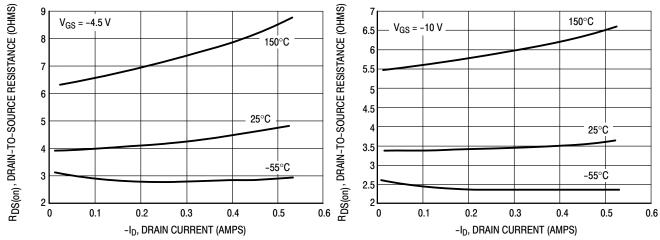


Figure 3. On-Resistance versus Drain Current

Figure 4. On-Resistance versus Drain Current

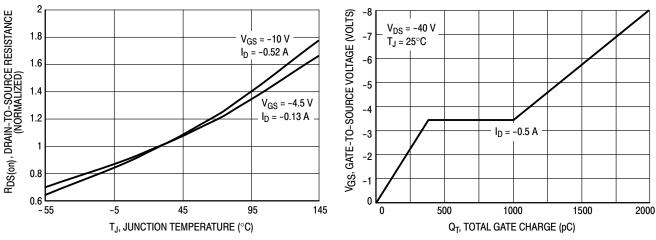


Figure 5. On-Resistance Variation with Temperature

Figure 6. Gate Charge

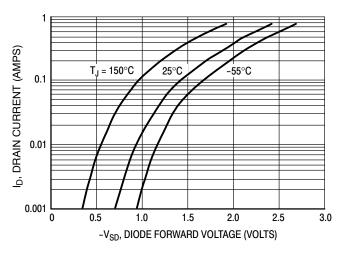
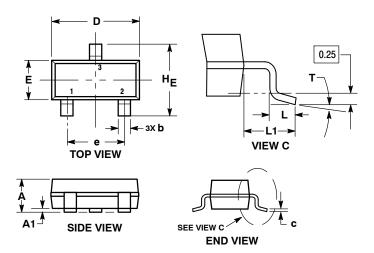


Figure 7. Body Diode Forward Voltage

BSS84L, BVSS84L

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AS**



NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
- PROTRUSIONS, OR GATE BURRS

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°		10°	0°		10°

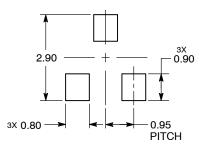
STYLE 21:

PIN 1. GATE

SOURCE

DRAIN

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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